

F 1 004991

BE Eighth Semester (Electronics & Communication Engineering) STATEMENT OF MARKS

KARNATAKA, INDIA

Name of the Student VINUTAN T NAIK

Father's / Mother's Name : TAVAREPPA K L

Seat No.: 4GM05EC117

Name of the College G.M. INSTITUTE OF TECHNOLOGY, DAVANGERE

2		Subject	Exam	Examination Marks	Marks	internal .	Assessm	ent Marks	_	otal Mar	KS
Z C	Code	Title	Max	Min	Obtained	Max	Min	Obtained	Max	Min	Obtained
	DREC81	Wireless Communication	100	35	38	25		23	125	50	
1	NOT COL	AAII CIGGO COIIIII GIII COMOII				2		40	100	70	
2	06EC82	Embedded System Design	100	35	35	25	*	18	125	00	
ا د	OSECSSO	Network Security	100	35	55	25	,	23	125	50	
c	000000		3	2		25		18	125	50	
4	06EC841	Multimedia Communication	-	00	1	1					
ת	06FC85	Project Work	100	40	86	100	50	86	200	90	
0	Dellos					50	20	35	50	20	35
c	00000	Colling		2	,	37		20	125	50	
7	06EC458	Fundamentals of HDL	100	35	48	67		200	160		
00	06ECL488 HDL Lab	HDL Lab	50	20	23	25	12	18	75	32	

*First Attempt Total (in words) FIVE HUNDRED AND FORTY FOUR ONLY

GRAND TOTAL

950

392

544

Results of V to VIII Semesters :

Result of the Semester SECOND CLASS

544	350	257	331	Marks at Eirst Attempt
\(\(\)	\(\)	<	emesters

First Attempt Total (V to VIII Sem) in words: ONE THOUSAND FOUR HUNDRED AND SEVENTY TWO ONLY

SECOND CLASS



/TO VIII



Date: JUN 29, 2013

Result of the Degree

Total Marks (Max.)

900

900

700 SC

950 SC

> 3450 1472

SC