

CO224 Computer Architecture

Course Plan - 2020

Timetable			
	Mon	Wed	Fri
8.55am - 9.50am	Lecture/Discussion	Lab	Lecture/Discussion
10.10am - 11.05am			

Special Notes :

- First set of labs will use ARM assembly for ISA, and second set of labs will use Verilog for computer organization
- Two lecture/discussion hours per week
Two lab hours per week
One optional lecture/discussion hour per week
- Labs & Assignments:
 - Use QEMU emulator for ARM, iVerilog for HDL
 - Students work in groups of two
 - Marks awarded to groups for completion of lab tasks
 - 15 minutes quiz (individual) at end of every lab, to test understanding
 - First 50% of lab (group) marks will be weighted based on individual quiz marks
 - Labs + Assignments marks component = 40% of the course mark
 - Assessing labs to be done in the class itself, in a strict manner.

Apr 06 -Apr 10	9	Pipelining - Continued	2	[Tutorial 5] - Building a Simple Processor	2	[Optional]	-		-
Apr 13 -Apr 17	New Year Vacation								
Apr 20 -Apr 24	10	Memory Hierarchy - Storage - Main memory - Caching - VM	2	[Lab 5] - Building a Simple Processor	2	[Optional]	-		-
Apr 27 -May 01	11		2		- Quiz 5	2	Public Holiday	-	
May 04 -May 08	12		2	[Tutorial 6] - Memory and Caching	2	Public Holiday	-		-
May 11 -May 15	13	Multiprocessors - Shared-memory - Clusters - Message-passing - Flynn’s Taxonomy	2	[Lab 6] - Memory and Caching	2	[Optional]	-		-
May 18 -May 22	14		2		- Quiz 6	2	Arithmetic - Integer arithmetics - Floating point arithmetics - Conversion		-
May 25 -May 29	15	Public Holiday	2	[Optional]	-	[Optional]	-		-
	Study Break & Final Exam								