COMPUTER SCIENCE & ENGINEERING (BTECH)

	COURSE CURRIC	CULU	M FO	R TE	HE NEW	PROGRAM	ME (B.Tech) w.e.f. 2007 BATCH				
	Semester I						Semester – II				
Course code	Course Name	Credit Structure				Course Code	Course Name	Credit Structure			
CH 103+	Chemistry	L 2	T 1	P 0	6	MA 106 And MA 108	Linear Algebra and Ordinary Differential Equations I	L 3	T 1	P 0	C 8
CS 101	Computer Programming	2	0	2	6	CH 103+	Chemistry	2	1	0	6
HS 101	Economics	3	0	0	6	PH 103*	Electricity and Magnetism	3	0	0	6
MA 105	Calculus	3	1	0	8	PH 105*	Modern Physics	2	1	0	6
PH 103*	Electricity and Magnetism	2	1	0	6	CS 152	Department Introductory Course	3	0	0	6
PH 105*	Modern Physics	2	1	0	6	IC 102	Data Analysis and Interpretations	2	1	0	6
CH 117+	Chemistry Lab	0	0	3	3	CH 117*	Chemistry Lab.	0	0	3	3
ME 113*	Workshop Practice	0	1	3	5	ME 113+	Workshop Practice	0	1	3	5
ME 119*	Engineering Graphics and Drawing	1	0	3	5	ME 119+	Engineering Graphics and Drawing	0	1	3	5
PH 117+	Physics Lab	0	0	3	3	PH 117*	Physics Lab.	0	0	3	3
						CS 154	Abstractions and Paradigms in Programming Lab	0	0	3	3
NC 101#	National Cadet Corps (NCC)	0	0	0	P/NP	NC 102#	National Cadet Corps (NCC)	0	0	0	P/NP
NO 101#	National Sports Organization (NSS)	0	0	0	P/NP	NO 102#	National Sports Organization (NSS)	0	0	0	P/NP
NS 101#	National Service Scheme (NSS)	0	0	0	P/NP	NS 102#	National Service Scheme (NSS)	0	0	0	P/NP
* Any one for D3 D 4	e of these two courses and any one of the	nese La	ab cou	rses	only	* Any one D1 D2	of these two courses and any one of the	se Lab	course	es on	ly for
+ Only fo						+ Only for					
# Any one	e of these three P/NP courses					# Any one	of these three P/NP courses				

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Course					Semester –IV						
code	Course Name	Course Name Credit Structure		ure	Course Code	Course Name	C	redit S	Structi	ıre	
		L	T	P	C			L	T	P	C
ES 200 And HS 200	Environmental Studies: Science and Engg And Environmental Studies	3	0	0	3	MA 214	Numerical Analysis	3	1	0	8
EE 101	Introduction to Electrical and Electronic Circuits	3	1	0	8	CS 208	Automata Theory and Logic	3	0	0	6
CS 207	Discrete Structures	3	0	0	6	CS 218	Design and Analysis of Algorithms	3	0	0	6
CS 213	Data Structures and Algorithms	3	0	0	6	CS 210	Logic Design	3	0	0	6
IC 211	Experimentation and Measurement Lab	0	0.5	3	4	CS 296	Software Systems Lab	2	0	2	6
CS 293	Data Structures and Algorithms Lab	0	0	3	3	CS 288	Logic Design Lab	0	0	3	3
					33						35
COURSE	ES FOR HONOR REQUIREMENT					COURSE	S FOR HONOR REQUIREMENT				<u> </u>
							CSE Elective	3	0	0	6
COURSES FOR MINOR REQUIREMENT							S FOR MINOR REQUIREMENT				
CS 207	Discrete Structures	3	0	0	6	CS 213	Data Structures and Algorithms	3	0	0	6

		NCE & ENGINEERING V PROGRAMME (B.Tech.) w.e.f. 2007 BATCH	ī	

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	Semester V				Semester –VI						
Course code			Course Code	Course Name	Credit Structure						
		L	T	P	C			L	T	P	C
HS 301/	Philosophy/	3	0	0	6	CS 344	Artificial Intelligence	3	0	0	6
HS 303/	Psychology/										
HS 305/	Literature/										
HS 307	Sociology										
CS 305	Computer Architecture	3	0	0	6	CS 302	Implementation of Programming Languages	3	1	0	8
CS 347	Operating Systems	3	0	0	6	CS 348	Computer Networks	3	0	0	6
CS 317	Database and Information Systems	3	0	0	6	CS 386	Artificial Intelligence Lab	0	0	3	3
CS 387	Database and Information Systems Lab	0	0	3	3	CS 306	Implementation of Programming Languages Lab	0	0	3	3
CS 341	Computer Architecture Lab	0	0	3	3	CS 378	Computer Networks Lab	0	0	3	3
CS 377	Operating Systems Lab	0	0	3	3	CS 308	Embedded Systems Lab	1	0	2	4
					33						33
COURSE	ES FOR HONOR REQUIREMENT	l l				COURSES	S FOR HONOR REQUIREMENT				
	CSE Elective						CSE Elective/				
						CS 492	BTP I				
COURSE	COURSES FOR MINOR REQUIREMENT						S FOR MINOR REQUIREMENT		1	I	l
CS 348	Computer Networks	3	0	0	6	CS 317	Data Base and Information Systems				

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	Semester VII				Semester –VIII							
Course code	Course Name	urse Name Credit Structure		Course Code				Credit Structure				
		L	T	P	C			L	T	P	C	
	Elective 1	3	0	0	6		Elective 4	3	0	0	6	
	Elective 2	3	0	0	6		Elective 5	3	0	0	6	
	Elective 3	3	0	0	6		Elective 6	3	0	0	6	
	Institute Elective 1	3	0	0	6		Institute Elective 2	3	0	0	6	
					24						24	
COURSE	ES FOR HONOR REQUIREMENT	<u> </u>				COURSES FOR HONOR REQUIREMENT						
	CSE Elective/						CSE Elective/					
CS 492	BTP I/					CS 496	BTP 2					
CS 496	BTP 2											
	CSE Elective						CSE Elective					
COURSES FOR MINOR REQUIREMENT							S FOR MINOR REQUIREMENT					
CS 344	Artificial Intelligence					CS 463	Design and Analysis of Algorithms	3	0	0	6	

We plan to teach CS207 Discrete Structures and CS 213 Data Structures and Algorithms every year. Four other CSE theory core courses would be offered as Minor courses in any two year cycle.

CS 213 is pre-requisite for all Minor courses except for CS 207 Discrete Structures. The Minor courses listed in the schedule from Semester 6 to 8 are given as a sample.

Course Proposal

i	Title of the course	CS 2	xxx In	trodu	action to Embedded Systems (Lab)
ii	Credit Structure	L	T	P	С
		1	0	2	4
iii	Prerequisite, if any (for the students		wledg gramm	-	programming using C and some system level
iv	Course content (Separate sheet may be used, if necessary)	for e	vention embed	nal pr ded s s con	embedded systems as distinct from rogramming systems, Programming paradigms systems such as Real-time Operating Systems ventional operating systems, Esterel,
		and gene	other erating	devic g puls	microcontrollers, Interfacing digital sensors ees, Timers/Counters, speed control by se width modulation (PWM), interfacing analog to-D conversion.
		Ada	ptive (Cruis	obot Programming: White Line Following and e Control, Realtime systems and RTOS on mple application.
V	Texts/References (Separate sheet may be used, if necessary)	So			Systems Design – A Unified Hardware/troduction, by Vahid and Givargis, Wiley,
		2. I	Oata-sl	neets	related to robots and related devices.
vi	Instructor(s) name				
vii	Name of other departments to whom the course is relevant	Mec Con		al En	gineering, Electrical Engineering, Systems &

viii	Justification	Embedded systems are going to be as ubiquitous as PCs are today. Our course uses robots to motivate the study of embedded systems. Many CS students will be building digitally controlled systems embedded into environments where the traditional boundaries between hardware and software dissolve. This is already true with applications that embed components/ sub-systems such as SoCs, FPGAs, etc. By the end of the course our students should be able to build drivers and applications for artifacts such as automatic greenhouses, autonomous robots, and the many yet to be invented devices that they might help invent in future.
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