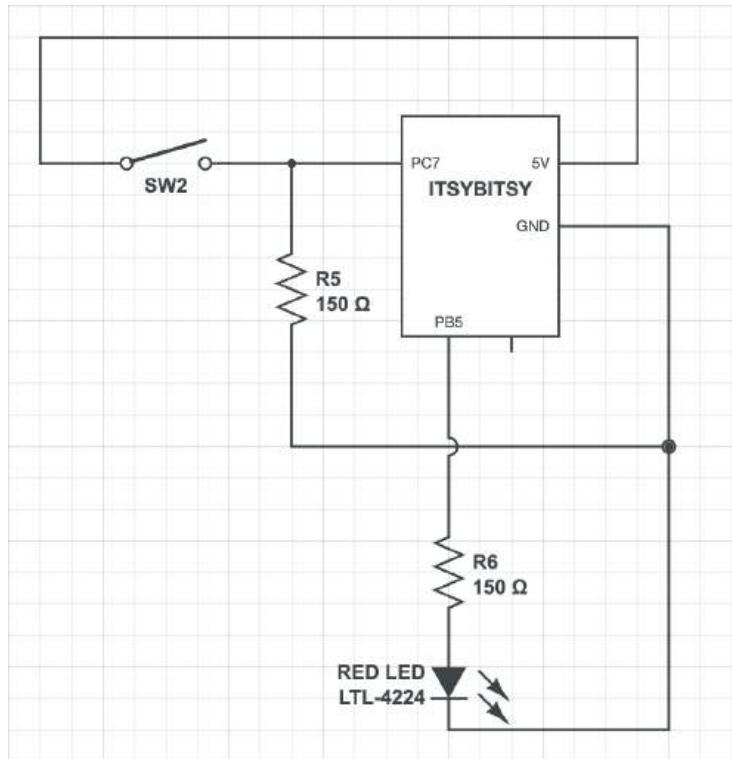
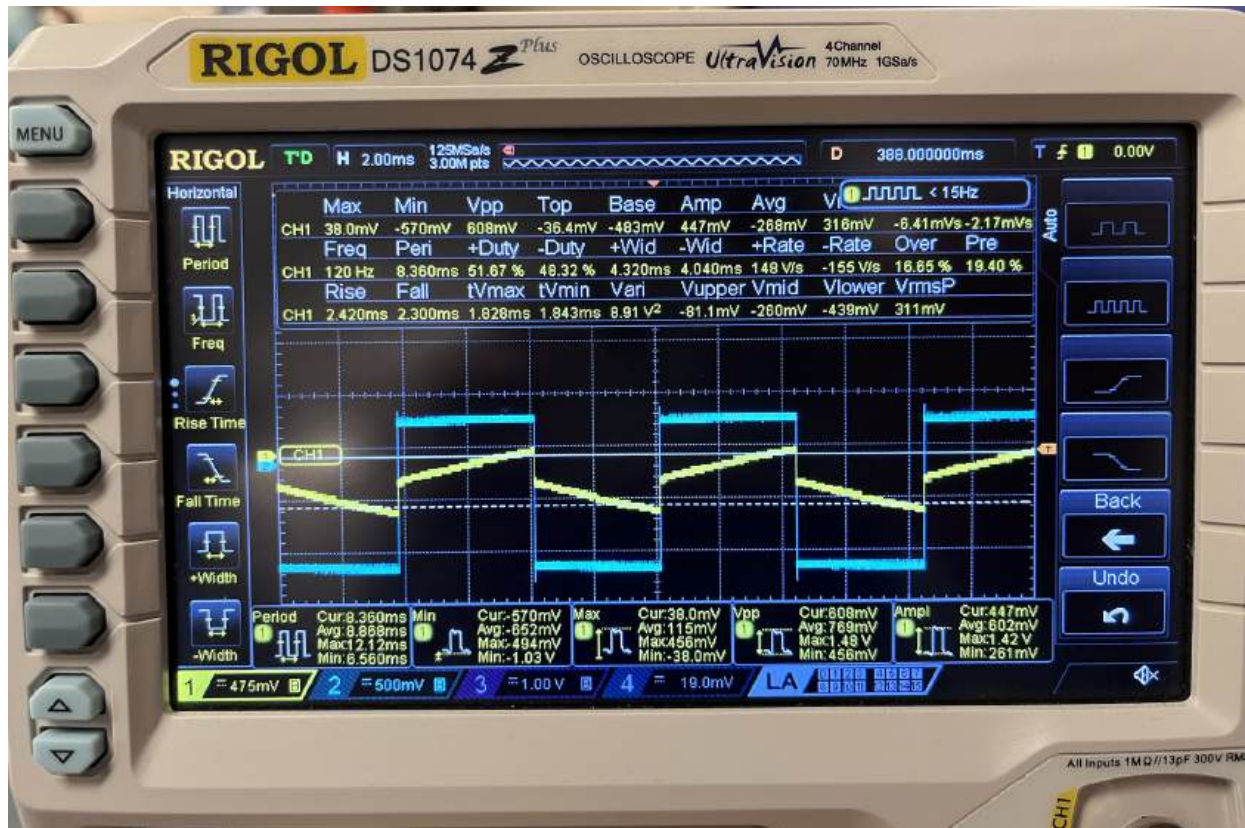
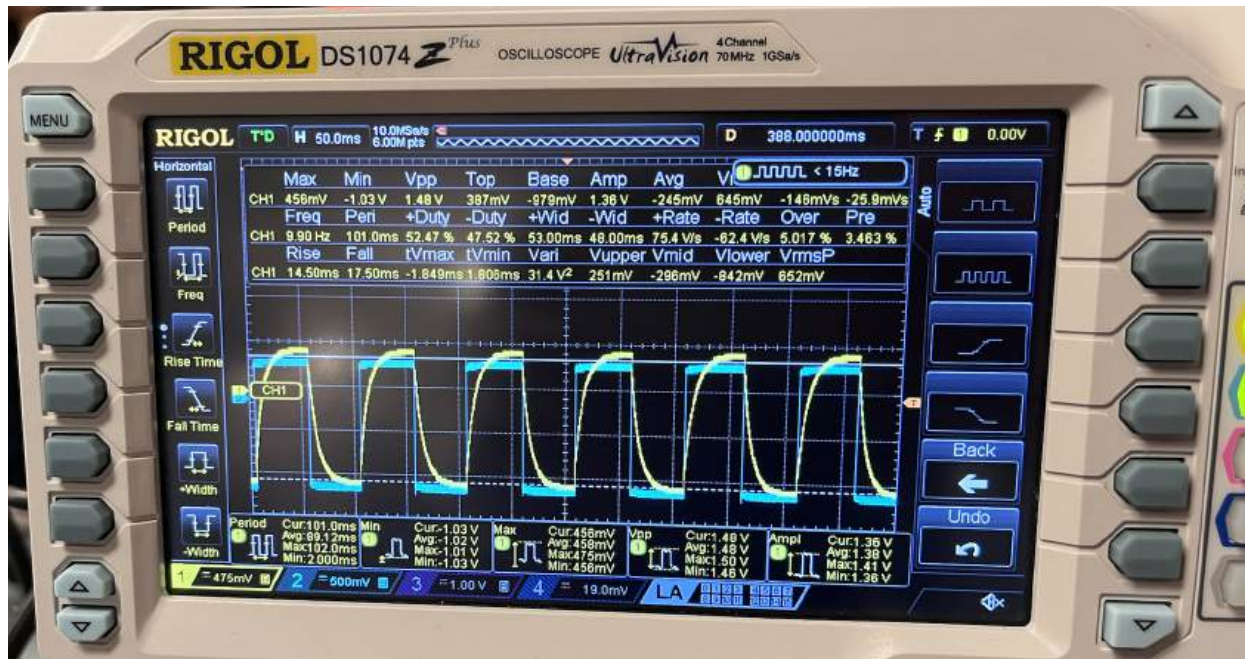


# MEAM 5100 LAB2

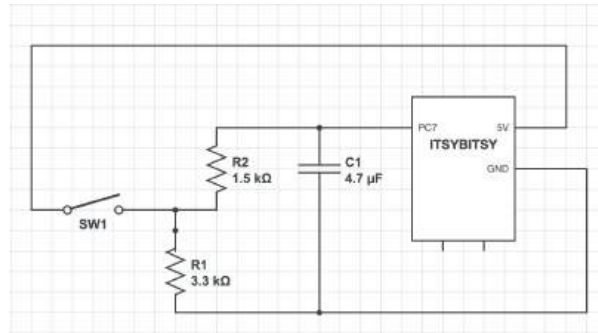
## 2.1.1



## 2.1.2



### 2.1.3



Fastest time - 1250

### 2.2.1

Table of potential drop across phototransistor in series with different resistance values

Resistor	Pot.drop with Light	Pot.drop without Light	ATmega32U4 valid logic high	ATmega32U4 valid logic low
4.7 kΩ	2 V	4.70 V	YES	NO
47 kΩ	500 mV	4.47 V	YES	YES
470 kΩ	80 mV	1.8 V	NO	NO

In general when intensity of light increases the electrical conductivity of a phototransistor also increases.

While using a 4.7 kΩ resistor we never get a valid logic low.

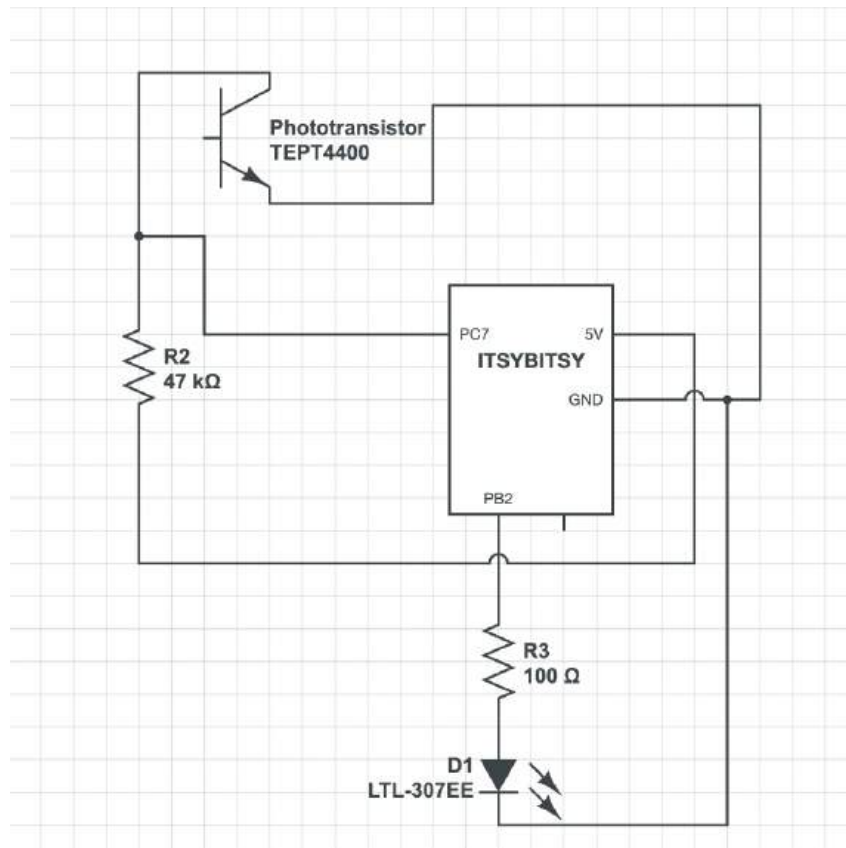
While using a 470 kΩ resistor we never get a valid logic high.

47 kΩ resistance is in the sweet spot and we get valid logic highs and valid logic lows for the ATmega32U4.

### 2.2.2A

[DEMO VIDEO](#)

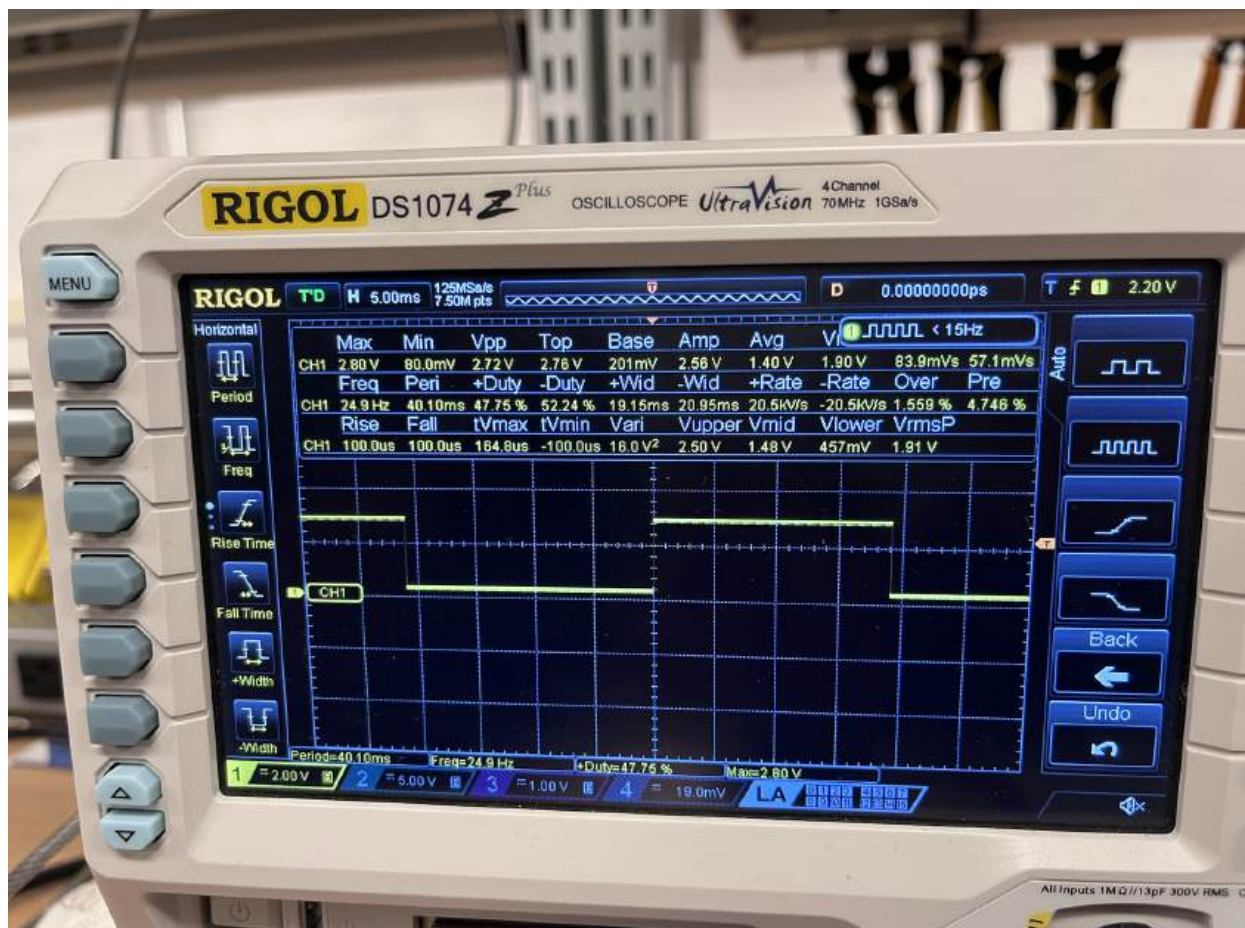
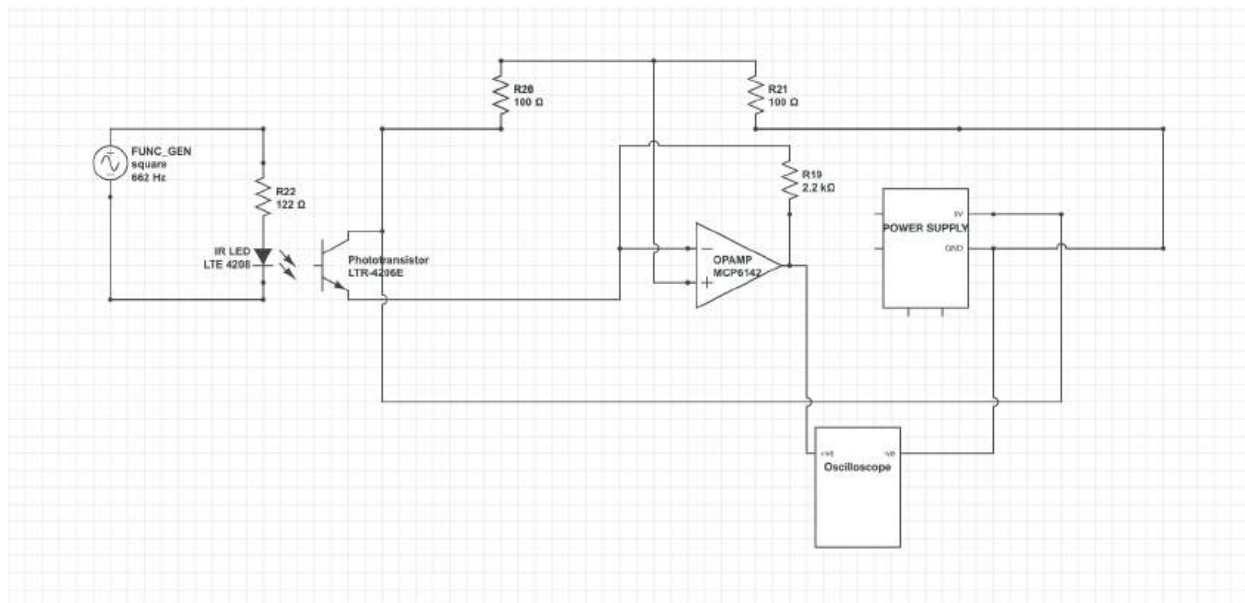
### 2.2.2B

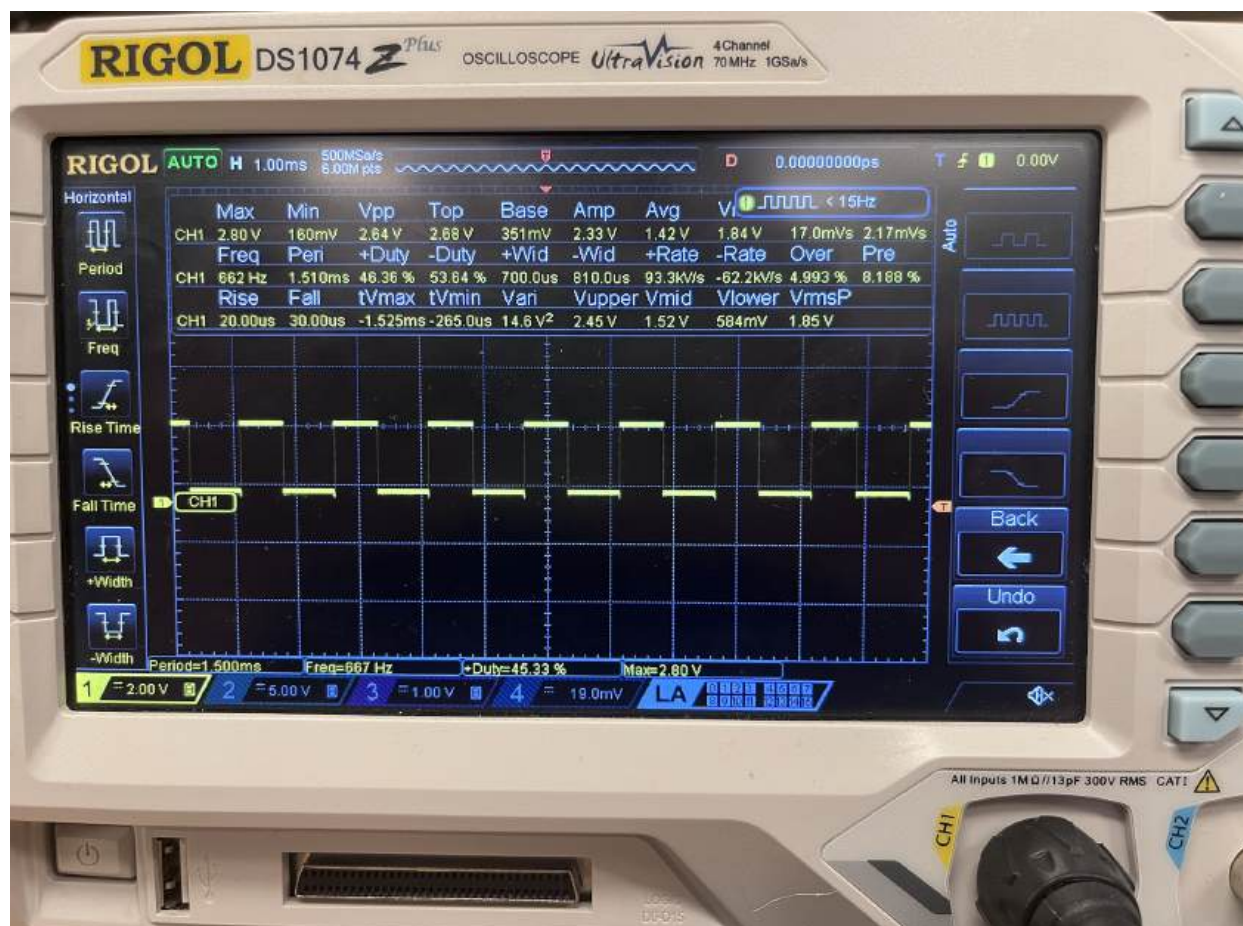


[DEMO VIDEO](#)

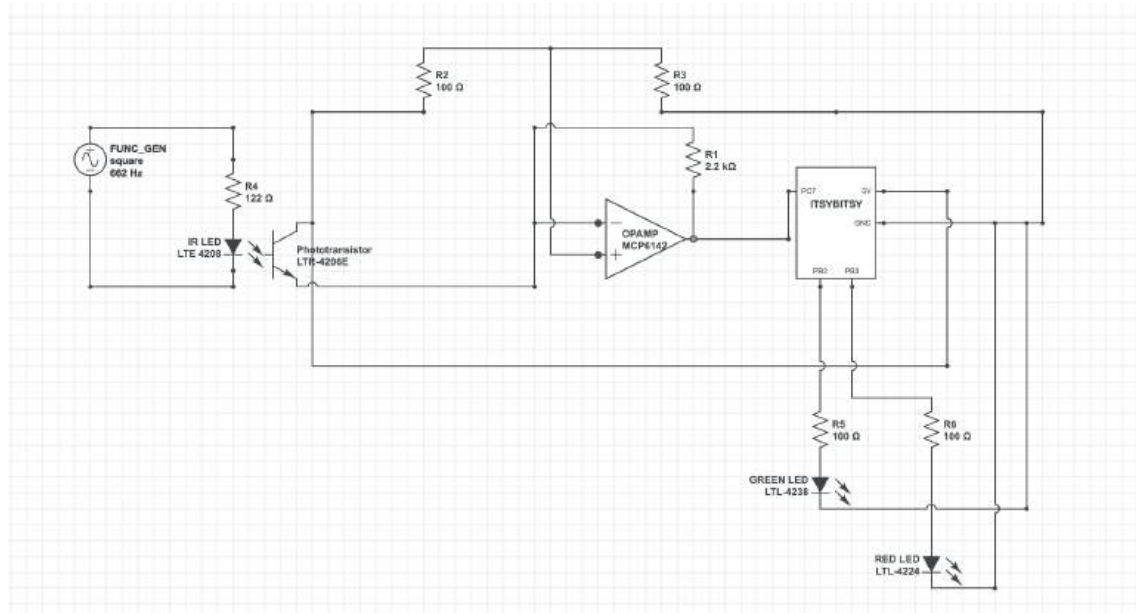


### 2.3.1



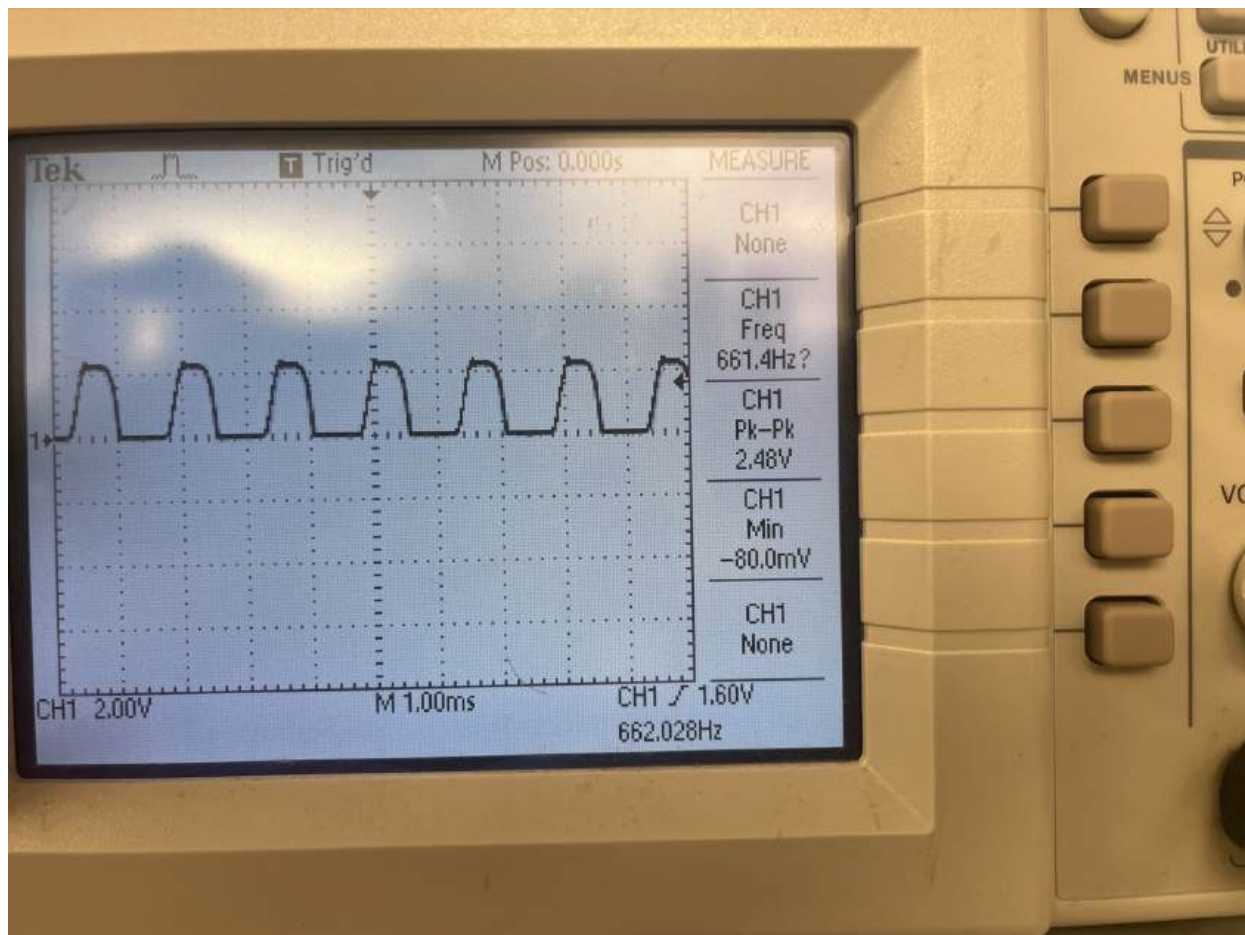


### 2.3.2

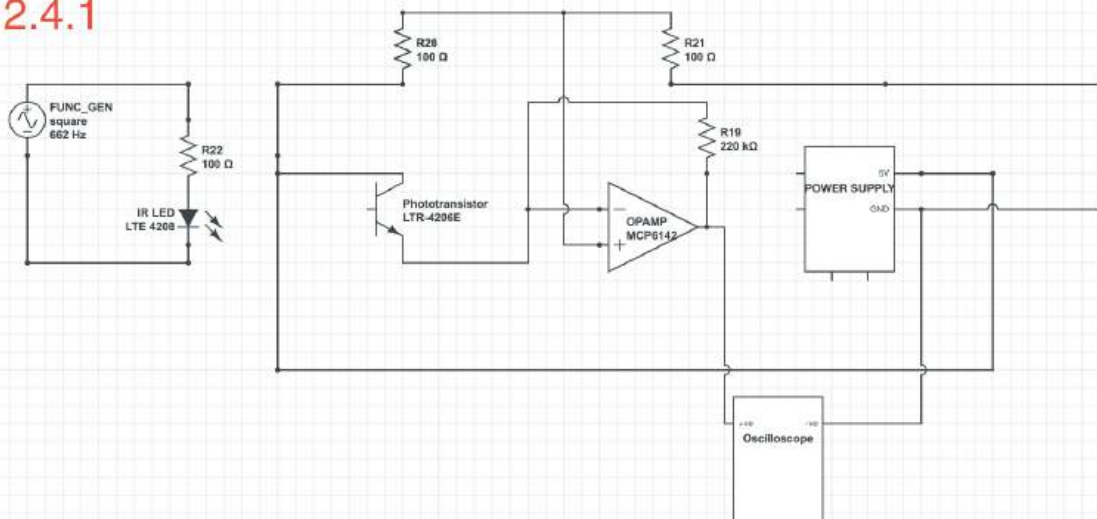


code

## 2.4.1



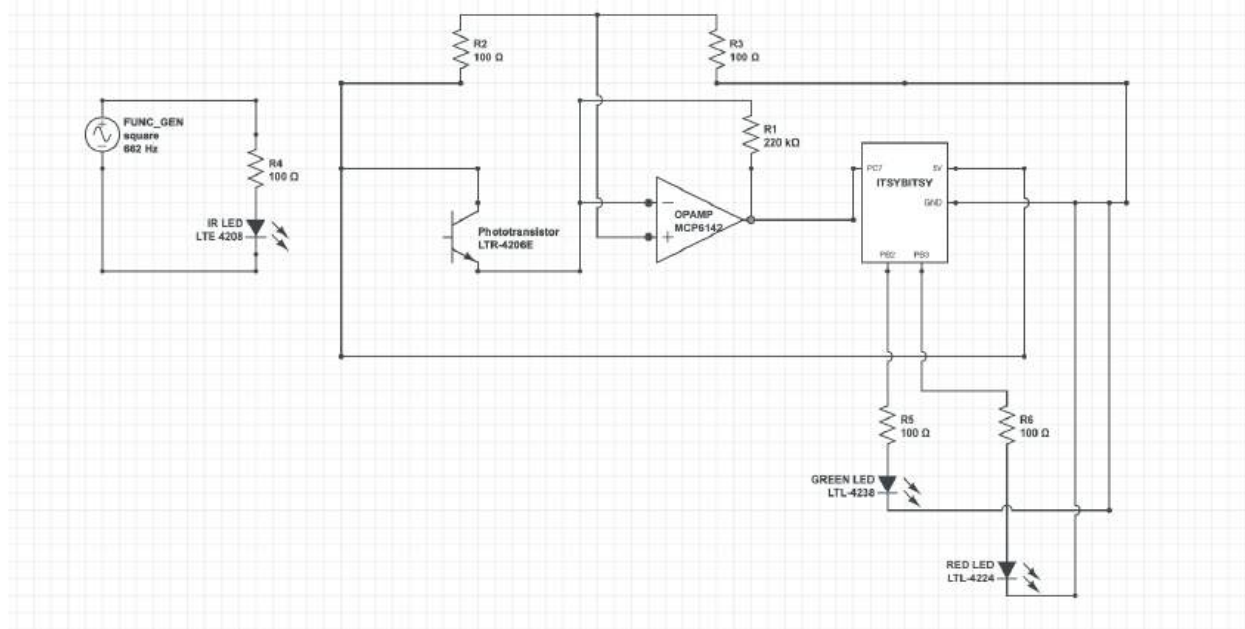
## 2.4.1





## 2.4.2

### 2.4.2



## 2.5

SECTION	HOURS
2.1	2
2.2	2
2.3	5
2.4	7