Datasheet

FS8205

Dual N-Channel Enhancement Mode Power MOSFET



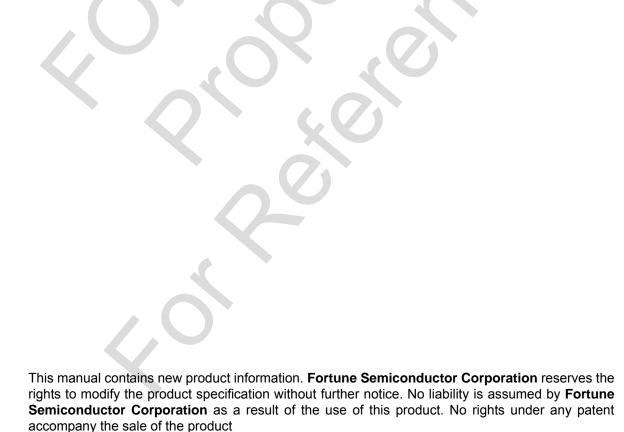


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1. Features

1.1 Low on-resistance

1.1.1
$$R_{DS(ON)} = 27 \text{ m}\Omega$$
 MAX. $(V_{GS} = 4.5V, I_D = 4A)$

1.1.2
$$R_{DS(ON)} = 36 \text{ m}\Omega$$
 MAX. $(V_{GS} = 2.5V, I_D = 3A)$

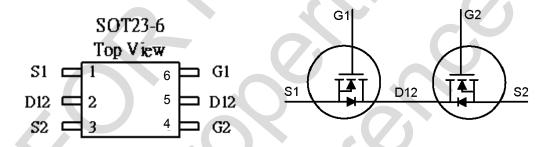
2. Applications

■ Li-ion battery management applications

3. Ordering Information

Product Number	Description	Package Type	Quantity/Reel
FS8205	SOT23-6 package version	SOT23-6	3,000

4. Pin Assignment



5. Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
VDS	Drain-Source Voltage	20	V
VGS	Gate-Source Voltage	±12	V
ID @TA = 25°C	Continuous Drain Current3	6	Α
ID @TA = 70°C	Continuous Drain Current3	5	Α
IDM	Pulsed Drain Current1	25	Α
PD @TA = 25°C	Total Power Dissipation	1	W
	Linear Derating Factor	0.008	W/°C
TSTG	Storage Temperature Range	-55 to 150	$^{\circ}$ C
TJ	Operating Junction Temperature Range	-55 to 150	$^{\circ}\!\mathbb{C}$

6. Thermal Data

Symbol	Parameter	Value	Unit	
Rthj-a	Thermal Resistance Junction-ambient3	Max. 125	°C/W	

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Electrical Characteristics 7.

Electrical Characteristics $@T_i = 25^{\circ}C$ (unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
Static Character	ristics					
BV _{DSS}	Drain-Source Breakdown Voltage	$V_{GS} = 0V, I_D = 250uA$	20	-	-	٧
Δ BV _{DSS} / Δ T _j	Breakdown Voltage Temperature Coefficient	Reference to 25°C, I _D =1mA	-	0.1	-	V/°C
R _{DS(ON)}	Static Drain-Source On-Resistance ²	V_{GS} = 4.5V, I_{D} = 4A	-	23	27	mΩ
		$V_{GS} = 2.5V, I_D = 3A$	-	28	36	mΩ
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = 250uA$	0.5	-	1.0	V
1	Drain-Source Leakage Current (T _j = 25℃)	V _{DS} =16V, V _{GS} = 0V	-	-	1	uA
I _{DSS}	Drain-Source Leakage Current (T _j = 70℃)	V _{DS} =16V, V _{GS} = 0V	-	-	25	uA
I _{GSS}	Gate-Source Leakage	V _{GS} = ±10V	-	-	±10	uA

Source-Drain Diode

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
Is	Continuous Source Current (Body Diode)	$V_D = V_G = 0V, V_S = 1.2V$	1	-	0.83	Α
V _{SD}	Forward On Voltage ²	$T_j = 25^{\circ}C$, $I_S = 1.25A$, $V_{GS} = 0V$	-	-	1.2	V

Notes:

- Pulse width limited by Max. junction temperature.
 Pulse width ≤ 300us, duty cycle ≤ 2%.
- 3. Surface mounted on 1 in² copper pad of FR4 board ; 208℃/W when mounted on Min. copper pad.

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9. Typical Characteristics

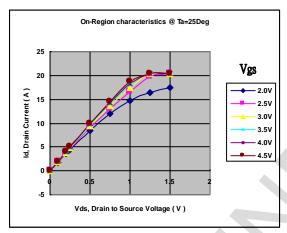


Fig 1. Typical Output Characteristics

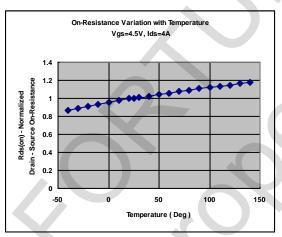


Fig 3. Normalized On-Resistance

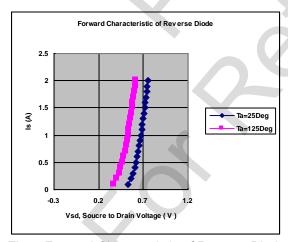


Fig 5. Forward Characteristic of Reverse Diode

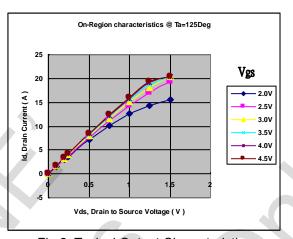


Fig 2. Typical Output Characteristics

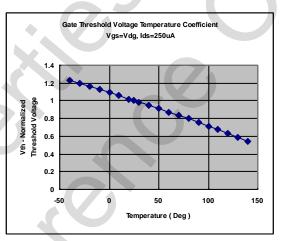
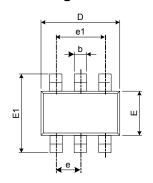


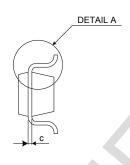
Fig 4. Gate Threshold Variation with Temperature

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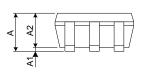


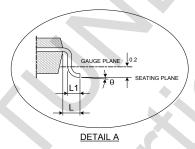
10. Package Information





		Uni	t : mm
SYMBOL	MIN.	TYP.	MAX.
Α	1.050	-	1.250
A1	0.000	-	0.100
A2	1.050	-	1.150
b	0.300	-	0.400
С	0.100	-	0.200
D	2.820	-	3.020
E	1.500	-	1.700
E1	2.650	-	2.950
е		0.950 TYP	
e1	1.800	-	2.000
L	0.700REF		
L1	0.300	-	0.600
θ	0°	-	8°





11. Revision History

Version	Date	Page	Description
1.0	2009/08/17	-	Version 1.0 released
1.1	2010/01/26	3	Rds25 TYP 28mohm MAX 36mohm
			Rds45 TYP 22mohm MAX 26mohm
1.2	2010/06/02	3	Rds45 TYP 23mohm MAX 27mohm
1.3	2010/06/10	4	IDSS Test Conditions : VDS=16V VGS=0V
1.4	2010/08/31	3	Revise Pin Assignment

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