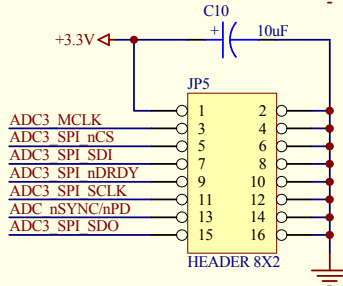
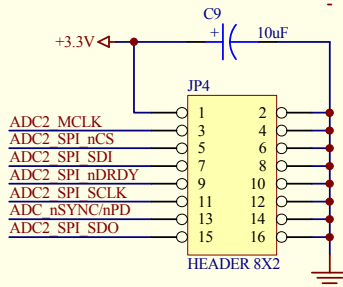
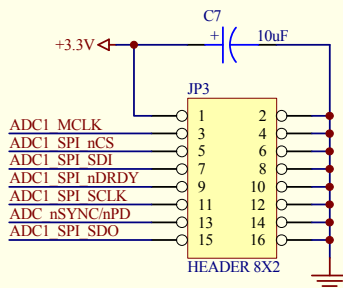
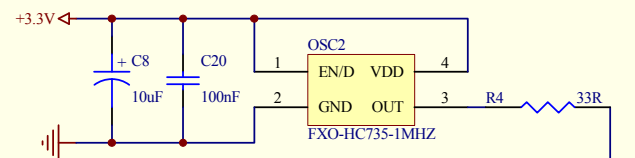
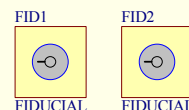
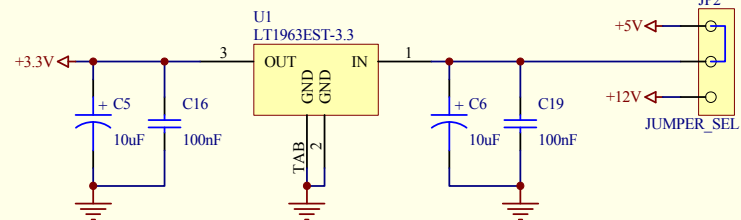
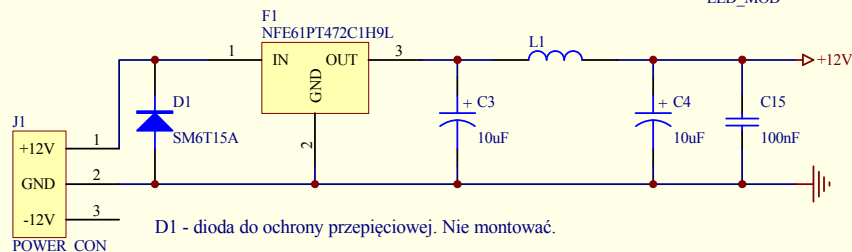
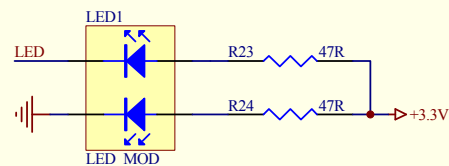
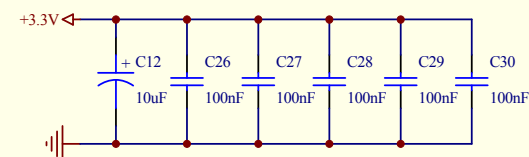
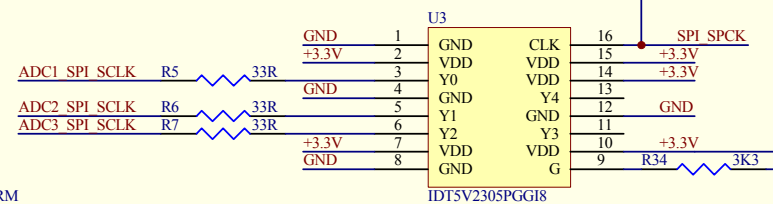
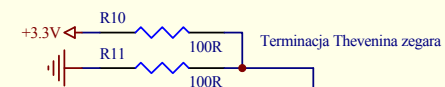
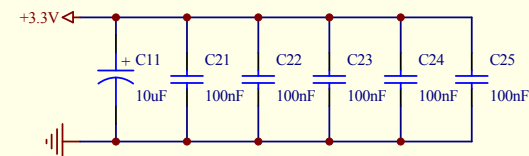
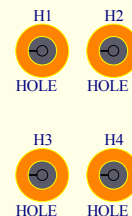
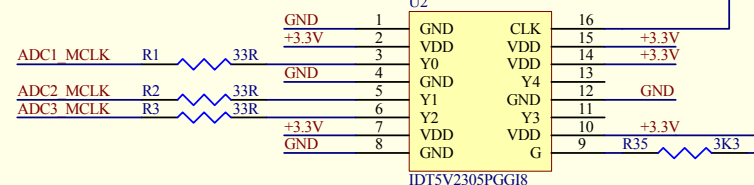
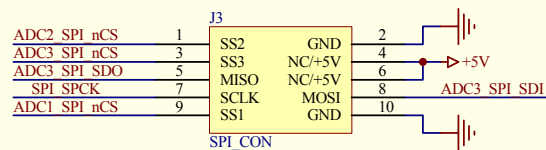
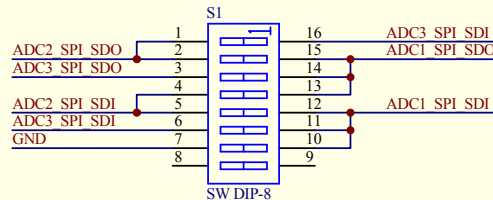
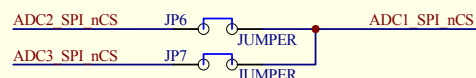


Rev1.3
Strona 1/2



1. Konfiguracja Daisy-Chain

- | | |
|--------------------------|--------------------------|
| JP6 - "zwarcie" | JP6 - "rozzwarcie" |
| JP7 - "zwarcie" | JP7 - "rozzwarcie" |
| S1.1-16 - "zwarcie" | S1.1-16 - "rozzwarcie" |
| S1.2-15 - "rozzwarcie" | S1.2-15 - "zwarcie" |
| S1.3-14 - "rozzwarcie" | S1.3-14 - "zwarcie" |
| S1.4-13 - "zwarcie" | S1.4-13 - "rozzwarcie" |
| S1.5-12 - "rozzwarcie" | S1.5-12 - "zwarcie" |
| S1.6-11 - "rozzwarcie" | S1.6-11 - "zwarcie" |
| S1.7-10 - "zwarcie" | S1.7-10 - "rozzwarcie" |
| S1.8-9 - "bez znaczenia" | S1.8-9 - "bez znaczenia" |

ARM
ARM.Sch

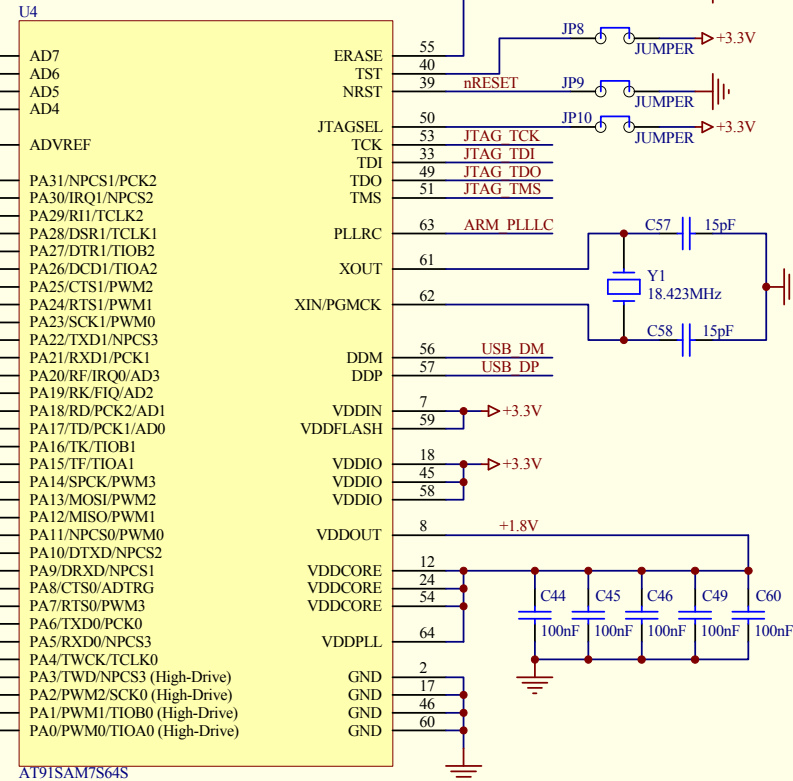
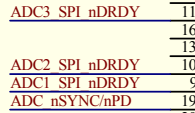
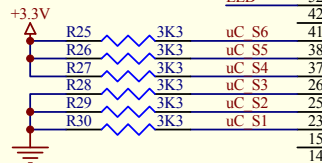
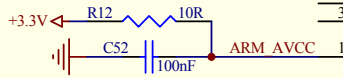
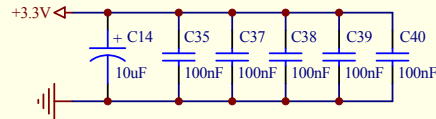
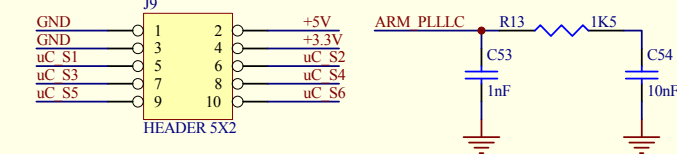
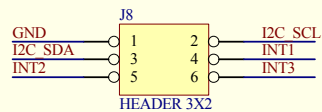
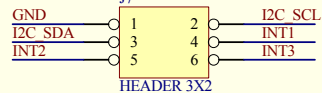
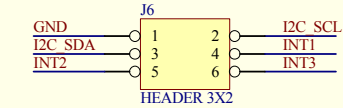
Układ konwertera SPI <-> USB

Rev1.3

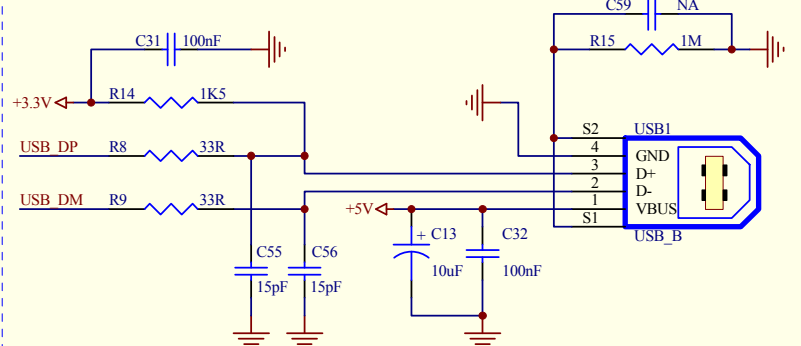
Strona 2/2

Uwagi:

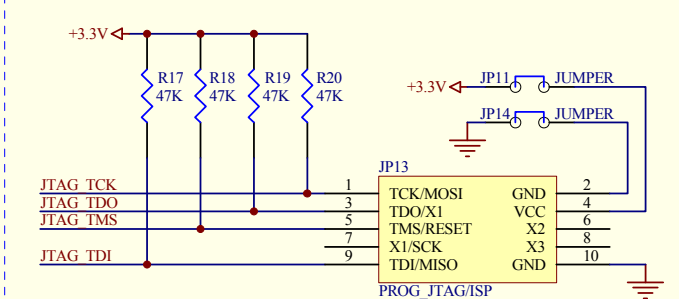
1. Wszystkie rezystory: obudowa 0603
2. Kondensatory filtrujące napięcia zasilania:
 < 10uF dielektryk X7R, NPO
 = 10uF dielektryk X5R
 > 10uF low ESR tantal
3. NA - nie montować.



USB (12Mbps, Full Speed)



JTAG



UART

