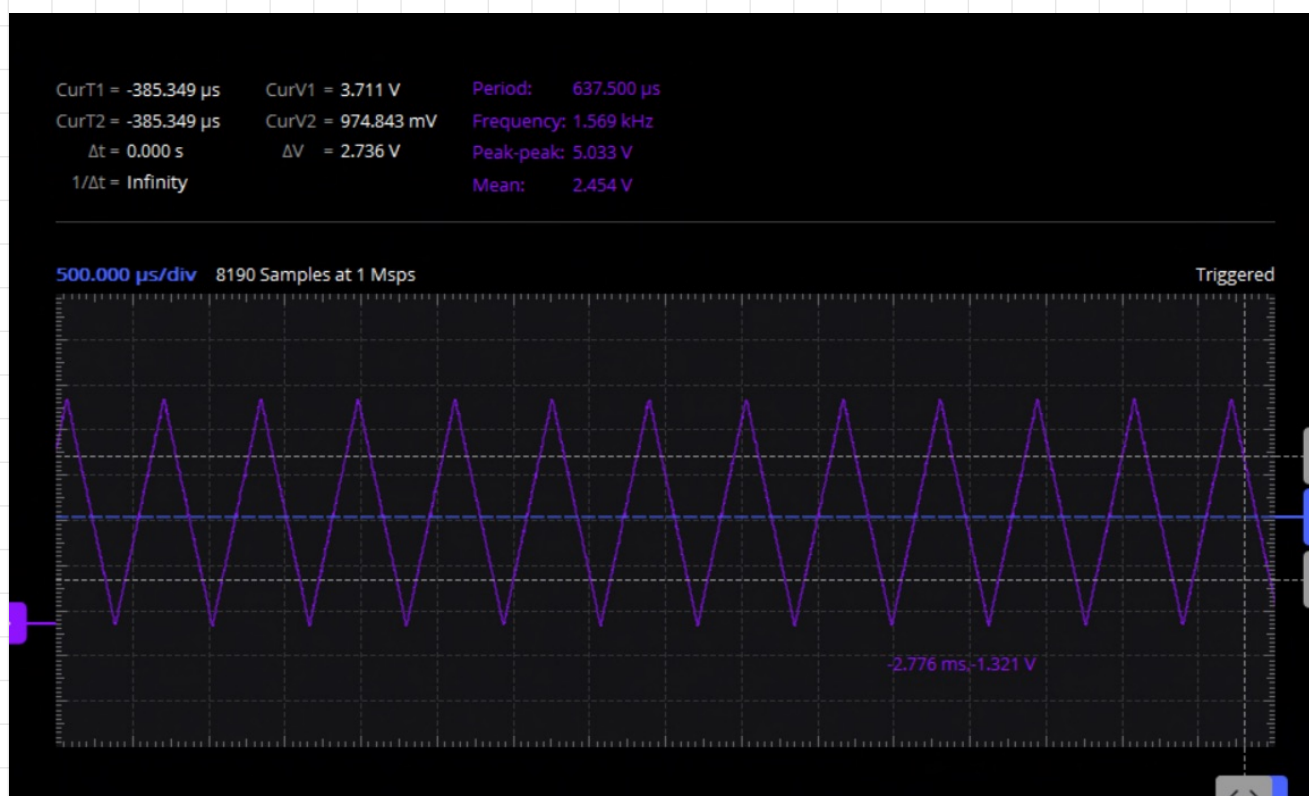


$$f = 1000 \text{ Hz} \rightarrow T = \frac{1}{1000} = 1 \text{ ms}$$

$$\frac{1 \text{ ms}}{256} = 3.9 \text{ } \mu\text{s} \rightarrow \text{update linearly every } 3.9 \text{ } \mu\text{s}$$

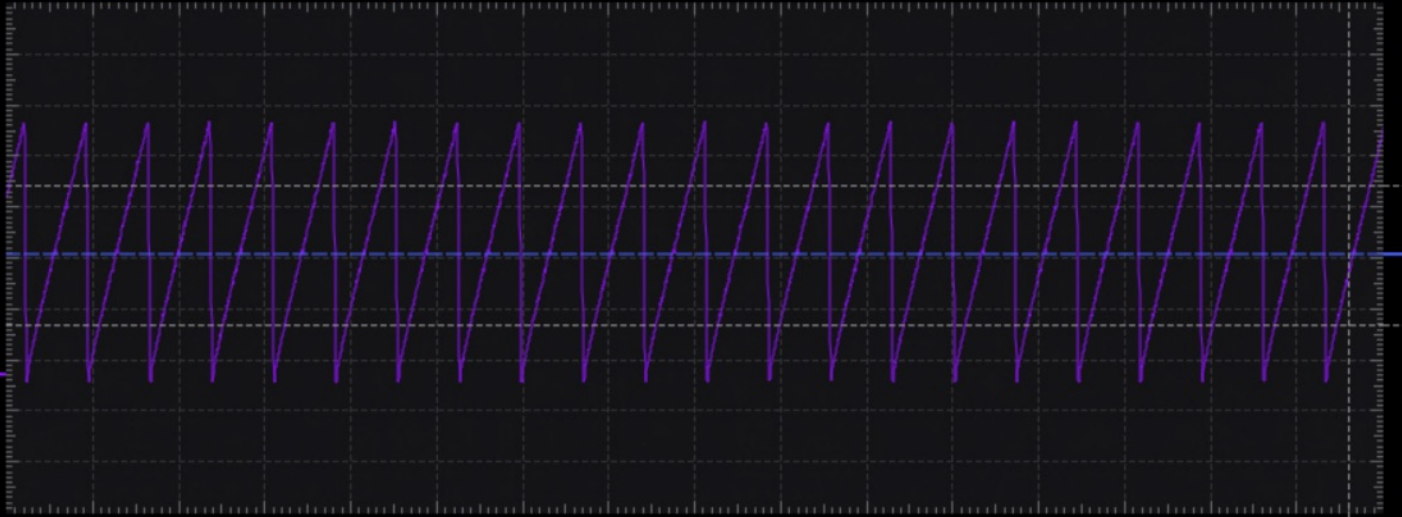
- initialize a variable to hold the DAC level
- Make the clock count to almost 4 μs before it proceeds to incrementing DAC
- Increment DAC Level by 1 until it reaches 255, then reset back to 0
- Repeat



CurT1 = -385.349 μ s CurV1 = 3.711 V Period: 359.773 μ s
CurT2 = -385.349 μ s CurV2 = 974.843 mV Frequency: 2.780 kHz
 Δt = 0.000 s ΔV = 2.736 V Peak-peak: 5.129 V
1/ Δt = Infinity Mean: 2.510 V

500.000 μ s/div 8190 Samples at 1 Msps

Triggered



CurT1 = 3.419 ms CurV1 = 3.711 V Period: 1.950 ms
CurT2 = 3.419 ms CurV2 = 974.843 mV Frequency: 512.821 Hz
 Δt = 0.000 s ΔV = 2.736 V Peak-peak: 5.011 V
1/ Δt = Infinity Mean: 2.411 V

1.000 ms/div 1600 Samples at 100 ksps

Triggered



CurT1 = 3.419 ms CurV1 = 3.711 V Period: 1.012 ms
CurT2 = 3.419 ms CurV2 = 974.843 mV Frequency: 987.776 Hz
 $\Delta t = 0.000$ s $\Delta V = 2.736$ V Peak-peak: 5.028 V
1/ $\Delta t = \text{Infinity}$ Mean: 2.482 V

1.000 ms/div 1600 Samples at 100 ksps

Triggered

