

Course Code	Course Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECC402	Micro-controllers	3	-	--	3	-	--	3

Course Code	Course Name	Examination Scheme							
		Theory Marks				Exam Duration (in Hrs.)	Term Work	Practical And Oral	Total
		Internal assessment			End Sem. Exam				
		Test1	Test2	Avg. of Test 1 and Test 2					
ECC402	Micro-controllers	20	20	20	80	03	-	-	100

Course Pre-requisites:

1. ECC303 - Digital System Design

Course objectives:

1. To develop background knowledge of Computer and its memory System.
2. To understand architecture of 8051 and ARM7 core.
3. To write programs for 8051 microcontrollers.
4. To understand design of Microcontroller Applications.

Course outcomes:

After successful completion of the course student will be able to:

1. Understand Computer and its memory System,
2. Understand the detailed architecture of 8051 and ARM7 Core.
3. Write programs for 8051 microcontrollers.
4. Design an applications using microcontroller.

Module No.	Unit No.	Topics	Hrs
1		Overview of Microprocessor based System	5
	1.1	Overview of microcomputer systems and their building blocks, Memory Interfacing, Steps taken by the microprocessor to fetch and executes an instruction from the memory	
	1.2	Concepts of Program counter register, Reset, Stack and stack pointer , Subroutine, Interrupts and Direct Memory Access	
	1.3	Concept of RISC & CISC Architecture	
	1.4	Harvard & Von Neumann Architecture	
2		The Memory Systems	4
	2.1	Classification of Memory : Primary and Secondary	
	2.2	Types of Semiconductor memories	
	2.3	Cache Memory	
	2.4	Virtual Memory Concept with Memory Management Unit with Segmentation and Paging (Address Translation Mechanism)	
3		8051 Microcontroller	8
	3.1	Comparison between Microprocessor and Microcontroller	
	3.2	Features, architecture and pin configuration	
	3.3	CPU timing and machine cycle	
	3.4	Input / Output ports	
	3.5	Memory organization	
	3.6	Counters and timers	
	3.7	Interrupts	
	3.8	Serial data input and output	
4		8051 Assembly Language Programming and Interfacing	9
	4.1	Addressing modes	
	4.2	Instruction set	
	4.3	Need of Assembler & Cross Assemble, Assembler Directives	
	4.4	Programs related to: arithmetic, logical, delay subroutine , input, output, timer, counters, port, serial communication, and interrupts	
	4.5	Interfacing with LEDs, Relay and Keys	
5		ARM7	8
	5.1	Introduction & Features of ARM 7	
	5.2	Concept of Cortex-A, Cortex-R and Cortex-M	
	5.3	Architectural inheritance, Pipelining	
	5.4	Programmer's model	
	5.5	Brief introduction to exceptions and interrupts handling	
	5.6	Instruction set: Data processing, Data Transfer, Control flow	
6		Study 8 bit microcontroller Applications	5
	6.1	Understanding features of NXP 89v51RD2, Atmega 328P and PIC16F886	
	6.2	Selecting a microcontroller for an application	
	6.3	Study of 89v51 based Clock Using I2C RTC and Seven Segment Display	
	6.4	PIC16F886 Speed Control of DC Motor.	
	6.5	Atmega 328P based remote temperature monitoring with LCD display	
Total			39

Text Books:

1. Douglas V Hall, SSSP Rao "Microprocessors & Interfacing", McGraw Hill
2. Carl Hamacher, Zvonko Vranesic and Safwat Zaky, "Computer Organization", Fifth Edition, Tata McGraw-Hill
3. Shibu K. V "Introduction to embedded systems" McGraw Hill.
4. M. A. Mazidi, J. G. Mazidi and R. D. Mckinlay, "The 8051 Microcontroller & Embedded systems", Pearson Publications, Second Edition 2006.
5. C. Kenneth J. Ayala and D. V. Gadre, "The 8051 Microcontroller & Embedded system using assembly & 'C' ", Cengage Learning, Edition 2010.
6. Steve Furber, "ARM System on chip Architecture", Pearson, 2nd edition.

Reference books:

1. "MCS@51 Microcontroller, Family User's Manual" Intel
2. "PIC16F882/883/884/886/887 Data Sheet", Microchip.
3. ATmega328P 8-bit AVR Microcontroller with 32K Bytes In-System Programmable Flash datasheet, Atmel
4. P89V51RB2/RC2/RD2 8-bit 80C51 5 V low power 16/32/64 kB flash microcontroller, Data Sheet NXP founded by Philips
5. James A. Langbridge, "Professional Embedded Arm Development", Wrox, John Wiley Brand & Sons Inc., Edition 2014

NPTEL/ Swayam Course:

1. Course: Microprocessors and Microcontrollers By Prof. Santanu Chattopadhyay (IIT Kharagpur); https://swayam.gov.in/nd1_noc20_ee42/preview

Internal Assessment (20-Marks):

Internal Assessment (IA) consists of two class tests of 20 marks each. IA-1 is to be conducted on approximately 40% of the syllabus completed and IA-2 will be based on remaining contents (approximately 40% syllabus but excluding contents covered in IA-1). Duration of each test shall be one hour. Average of the two tests will be considered as IA marks.

End Semester Examination (80-Marks):

Weightage to each of the modules in end-semester examination will be proportional to number of respective lecture hours mentioned in the curriculum.

1. Question paper will comprise of **total 06** questions, each carrying **20 marks**.
2. **Question No: 01** will be **compulsory** and based on entire syllabus wherein 4 to 5 sub-questions will be asked.
3. Remaining questions will be mixed in nature and randomly selected from all the modules.
4. Weightage of each module will be proportional to number of respective lecture hours as mentioned in the syllabus.
5. **Total 04 questions** need to be solved.