REGIVEATED POWER SUPPLY: All most all electronic.

Com equipments contain a circuit Called power.

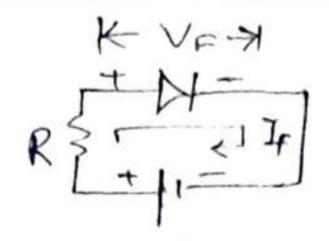
Supply to change the air mains into d.c.

air mains to the Regulator of Regulator of Contains of Co

- Ree i) TRANSformer: Steps up or Steps down the au Nain vallage according to sequisement of d.C. to the Circuit. Transformer also provides isolation
- (ii) RECTIFIER: The function of the suchifier is to Convert the ac. Voltage into cl-c Voltage.
- III) FILTER: The de. autput of seechifier may Contain supplies. These supplies are sumaved with the help of filters.
- (iv) REGILLATOR: The function of the segulator is to provide Constant dic vallage or segulated dic vallage is verspective of the change in load or input vallage.

- DIODE RATINGIS:

of Electronics & Communication France



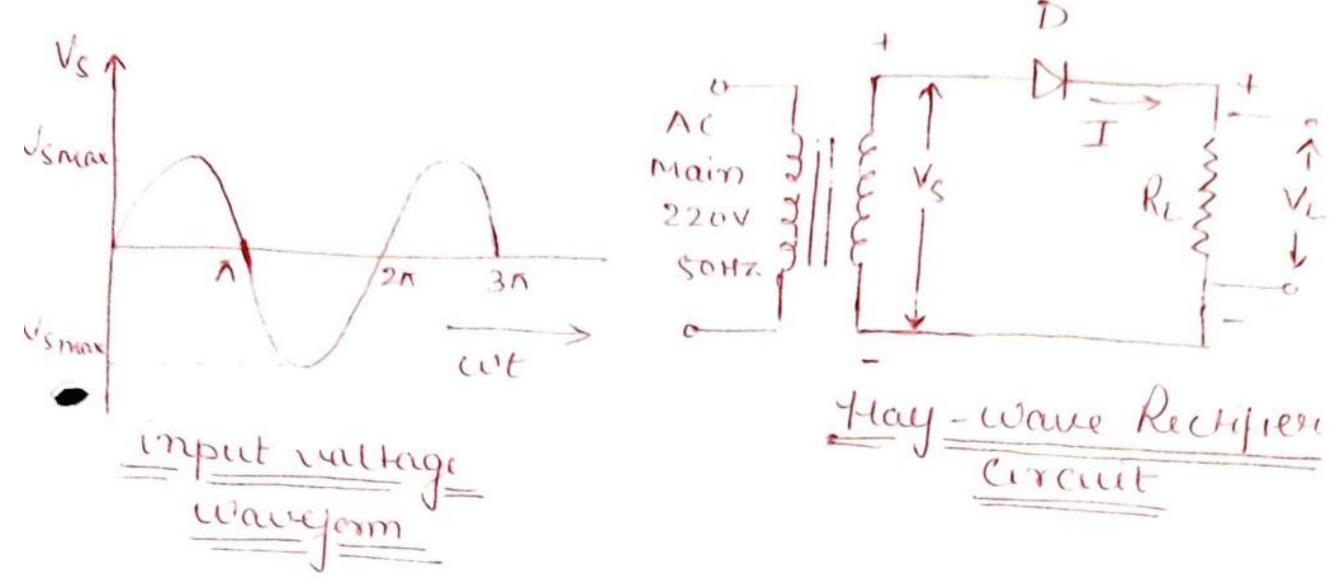
- 1) Forward Voltage drop: It is the anode to Cathode Vallage measured across a farword biased diode $V_F = 0.3$. for germanium and $0.7 \, \text{V}$ for Silicon
- 2) Maximum forward Current: defined as the maximum Value of forward Current that can be allowed to pass through a forward biased diode without damaging it.
- 3) Average forward current: defined as the maximum average rectified current which can flow through a forward biased diode without damaging it
- 4) Reverse Saturation current: défined as the current flouring through a diode in reverse biased State due to runosity charge Carvière.
 - => nA yor silicon and MA for Gurmanium
- Power dissipation: defined as the maximum power a diode can dissipate without damaging itself

 Vr = forward Vallage across diode

IF = forward Current.

) Peak Inverse Valtage: It is maximum reverse Valtage which can be applied across a diode without damaging it.

Junction Temperature [Timer)]: Maximum temperatwer a junction is allowed to aperate at, without getting damaged into unidirectional pulsating vallage, using me-half cycles of the applied untrage, the athen that Cycles being Suppressed because it conducts only in one clinection.



(IRCUIT: The cliede is Connected in Series with the Secondary of the tounsformer and the Load Resistance Re, the primary of the tounsformer us being Connected to the a.c. Supply mains.

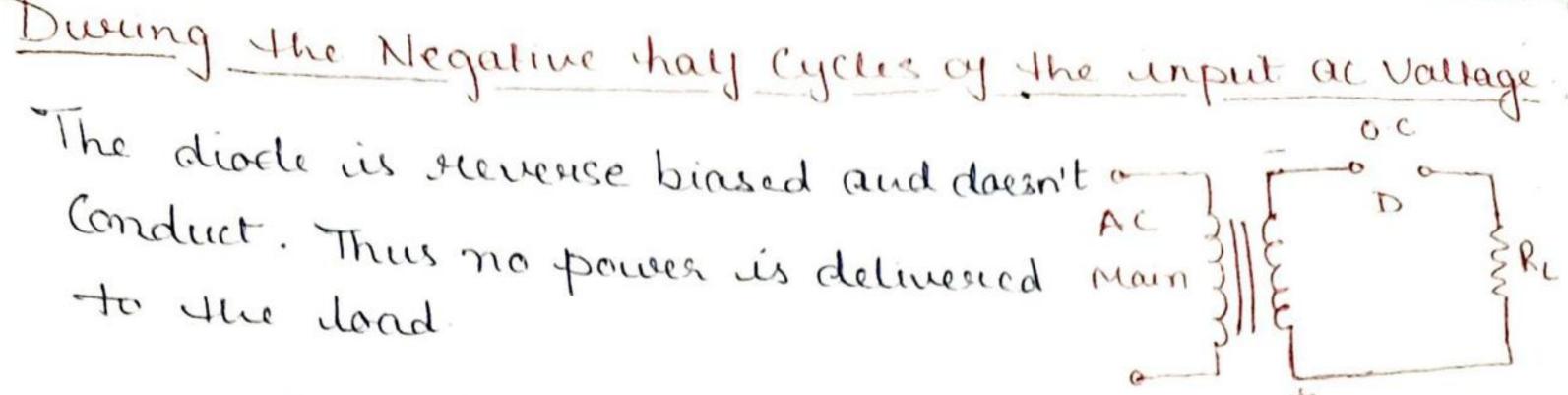
a) During Positive hay Cycle of the input ac vallage

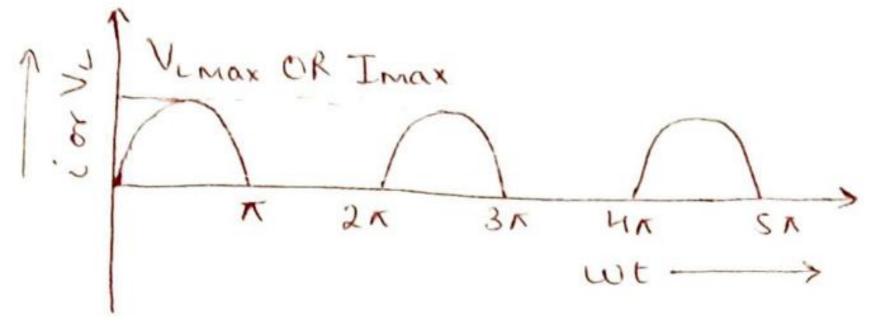
The diode is forward biased

and Conducts Current. If the Ac 3 Reverse waim 3 Reverse is assumed to be Zero,

the input voltage is directly explied to the load resistence Re.

The waveforms of the output Current and vallage are by the same shape as that by the input of all vallage.





Rectified Output Vallage/Cuvaent

ANALYSIS OF HALF-WAVE RECTIFIER

4) Peak Inverse Voltage (PIV): During negative half-Cycles ay the input valtage, the diade is reverse biased, no current ylaws through the load Resistant Re and whale by the input valtage appears across

Thus, the Maxm voltage, that appears across the diade is equal to the peak value by the Secondary vallage.

i.e PIV = Vernar

Peak Current: Current ylowing through diode (or loadke)
$$i = \begin{cases} I_{max} S_{in} wt & for 0 \le wt \le \overline{\Lambda} \\ 0 & for \overline{\Lambda} \le wt \le 2\overline{\Lambda} \end{cases}$$

Where peak value of Current flowing through dode or (load Re) is where,

Inax - beak current

Vallage across Secondary

ey transformer.

RF -> forward Resistance ay RL -> Load Resistance.

$$\int_{\alpha} dt = \int_{\alpha} \int_{\alpha} (d c w t)$$

$$= \int_{\alpha} \int_{\alpha} \int_{\alpha} I_{max} Sin w t d(w t) + \int_{\alpha}^{2\pi} o d(w t)$$

$$= \int_{\alpha} \int_{\alpha} I_{max} \left[-cas w t \right]_{\alpha}^{2\pi}$$

$$= \int_{\alpha} \int_{\alpha} I_{max} \left[-cas w t \right]_{\alpha}^{2\pi}$$

$$= \int_{\alpha} \int_{\alpha} I_{max} \left[-cas w t \right]_{\alpha}^{2\pi}$$

20 Hostel An.

Substituting the value of Imax

S: Jmax = Vsmax }
$$\left\{ \frac{1}{R_L + R_F} \right\}$$

11) De aupert Vallage (across load)

5) RMS Value of Current Howing through diode or load $I_{rms}^2 = \frac{1}{2\pi} \int_{0.27}^{2\pi} i^2 d(\omega t) \qquad \begin{cases} \sin^2 \omega = 1 - \cos 2\omega \end{cases}$

$$\frac{1}{2\pi} = \frac{1}{2\pi} \times \frac{1}{2} \Rightarrow \frac{1}{2\pi} = \frac{1}{2} = \frac$$

6) RMS value og Output Voltage: Rms voltage across The doad

7) FORM factor: defined on the evation of Rms to average Value

$$K_{J} = \frac{I_{rms}}{I_{dc}} = \frac{V_{smax}/2(R_{f} + R_{L})}{V_{smax}/\sqrt{R_{c} + R_{L}}}$$

$$V_{Smax}/\bar{\Lambda}(R_{c}+R_{c})$$

$$K_{5} = \frac{\pi}{2}$$
or
$$K_{5} = 1.57$$

(8) PEAK factor: defined as vatio of Peak value of oms value

$$\frac{K_{p} = I_{max}}{I_{rms}} = \frac{V_{smax}/(R_{f} + R_{e})}{V_{smax}/2(R_{f} + R_{e})} \Rightarrow \frac{1}{K_{p} = 2}$$

10) Rectification Officiency is defined as the station de autput powen to the ac imput powen

de power delivered to the doad de = Ide Re Pale = (Iniax) RL

$$P_{\alpha l} = I_{rms}^{2} (R_{F} + R_{l})$$

$$= I_{max}^{2} (R_{F} + R_{l})$$

$$= I_{rms}^{2} (R_{F} + R_{l})$$

Value of the ac Components of Voltage or Cuvient present in the output from the rectifier to the clivict or average value of the autput Voltage or Cuvient Cuvient,"

Ripple yactor,
$$\gamma = \frac{\pi}{3}$$
 and $\gamma = \sqrt{\frac{1}{2}}$ and $\gamma = \sqrt{\frac{1}{3}}$ and $\gamma = \sqrt{\frac{1}{3}}$

12) Transformen Utilisation factor: is defined as
the reation of power delivered to the load
and ac reating by the transformen Secondary

TUF = Pac = Iacker
Pac (rated) Vsrms Irms

'Vsmax = Jmax (R+Ri)

Advantages and Disadvantages of a Hay-wave Rectifier:

Advantages:

1) Simple Circuit and law Coest.

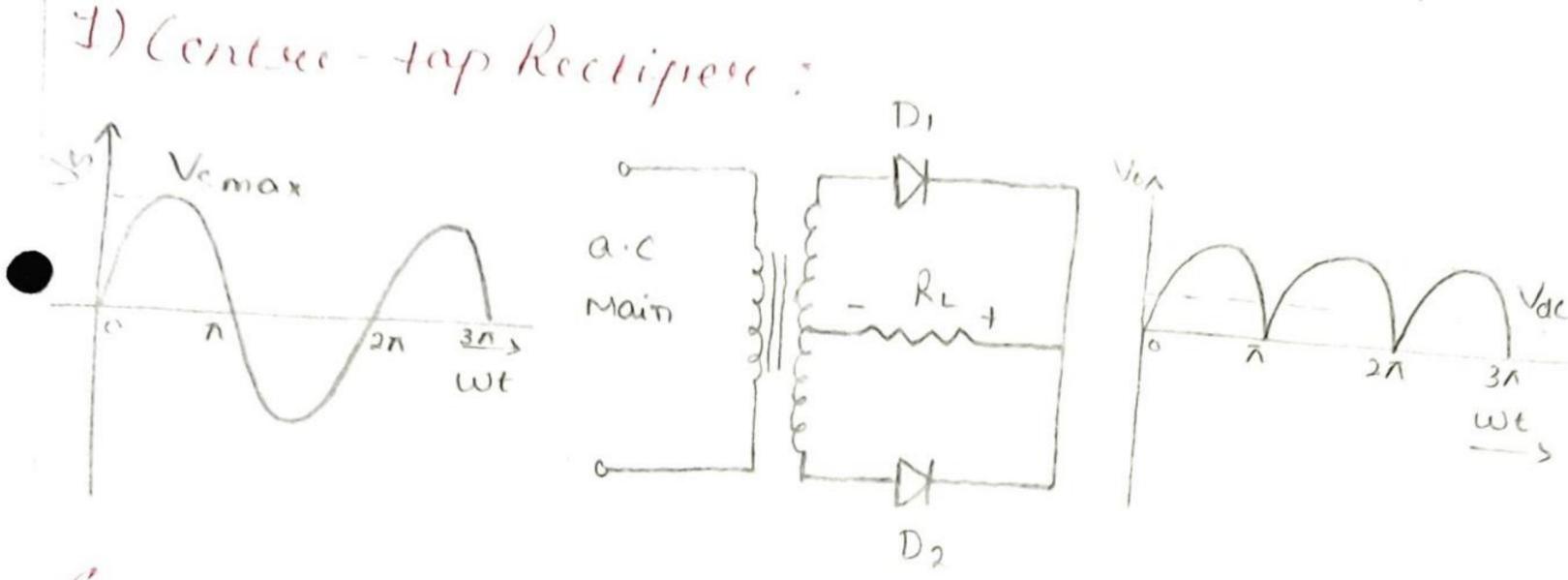
Disadvantages

or

- I) Ripple factor is high .. an elaborate filtering is required to give steady de autput.
- 2) Pawer autput is low, because power is delivered only hay the time
- 3) Rechticotion Efficiency in quite low
- 4) Transformer Utilization factor is low

both hay Cycles oy the input are utilized. Alternate hay Cycles are invested to give uniderectional load Current.

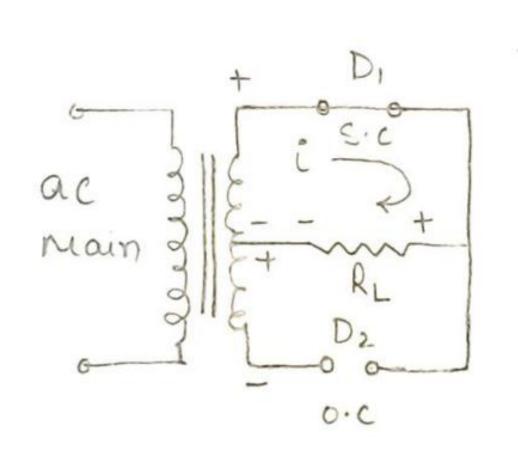
There are two types of full-wave suctifier Circuits 1) Centre - tap Rectifier 2) Brudge seectifier



Construction: The ac input is applied through a transformer, anodes (P-type) of Diodes D1 and D2 are Connected to the apposite ends af the Secondary windings and Cathodes are connected to each other and also through the load resistance Re to the Centre of the transformer.

During Positive thay Cycle:
The Diode D1 is yor wand bias as anode by D4 is positive wat Cathode and anode by Diode D2 is negative w.s. t Cathode ie

D2 is reverse biased



. only De Conducts and Current flows from Cathode

to anode of Diode Di, through load resistor Ri.

Diving regative they cycle

Do us forward biased and Do is

reverse biased

Do Conduct and Current flows

Through the load resistor Re and bottom

of the transformer Secondary.

Thus the direction of flow Luveent through the load Resistance Re remains the Same during both & halves of the input Supply vallage.

Peak Invense Vallage: During any half Cycle of the Supply one of Diode Conduct and other remain viewerse biased (o.c.) i.e. forward biased deade Conduct and offers almost zero resistance. So whole of the voltage Vsmax of they winding is developed across the load resistance Re

i. The vallage aveoss the non-conducting diode is the Sum of vallage aveoss the half of the transformer winding and the vallage aveoss the load resistance RL

PIV = Vsmax + Vsmax

PIV = 2 Vs mox

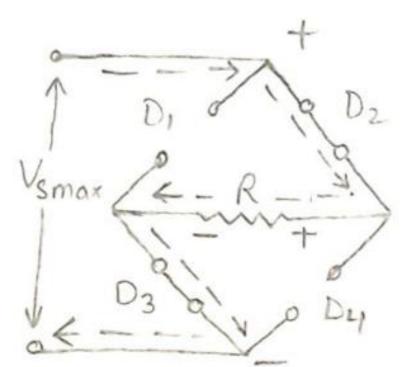
Odd Ser

will be

Vm Nain all Vsm D2D3 D1D1 Vo D2

During positive thay Cycle (first thay (yell ay input Supply)

Diode D2 and D3 are yorward biased and current ylow through them, enters the load at positive terminal and leaves the load at negative terminal.



The clicele D, and Dy are reverse biased and no current flow through them.

During negative thay Cycle (Second hay Cycle of input Supply)

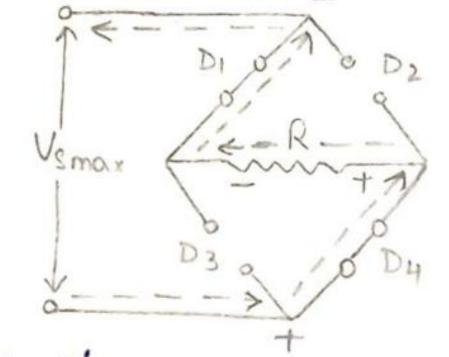
Diode Di and Dy are forward biased and current flow through them, enter the load at positive terminal and haves at regative terminal

365

Sin

tan

eu



Diased and no current ylows through them.

Peak Inverse Voltage: The entire vallage of the transformer Secondary winding, Vsmax is developed accuses the devad Resistance Re. The Same vallage (Vsmax) art across each of the mon-conducting diodes.

1) Peak Current: Hotal Current ylowing through the load Resistance Re

2) Output Current:

1 Sin

v Rai

rudh:

htar

3) De output Valtage:

4) RMS Value of Convert: rms or effective value of convert. Howing through load he

$$I_{rms}^2 = I_{max}^2 = I_{max}$$

5) RMS Value of Output Voltage:

6) Form factor:

$$K_f = \overline{\Lambda}$$

$$2\sqrt{2}$$
=) $K_f = 1.11$

7) Peak jactor:

8) Output frequency: The jull-wave rectifier invents each negative thay Cycle, so a jull-wave autput has twice as many Cycles as the Sine-wave input

M= Pole =
$$\frac{\frac{14}{\pi^2} J_{\text{max}} R_{\ell}}{\frac{1}{2} J_{\text{max}} (R_{\ell} + R_{f})}$$

10) Ripple factor: