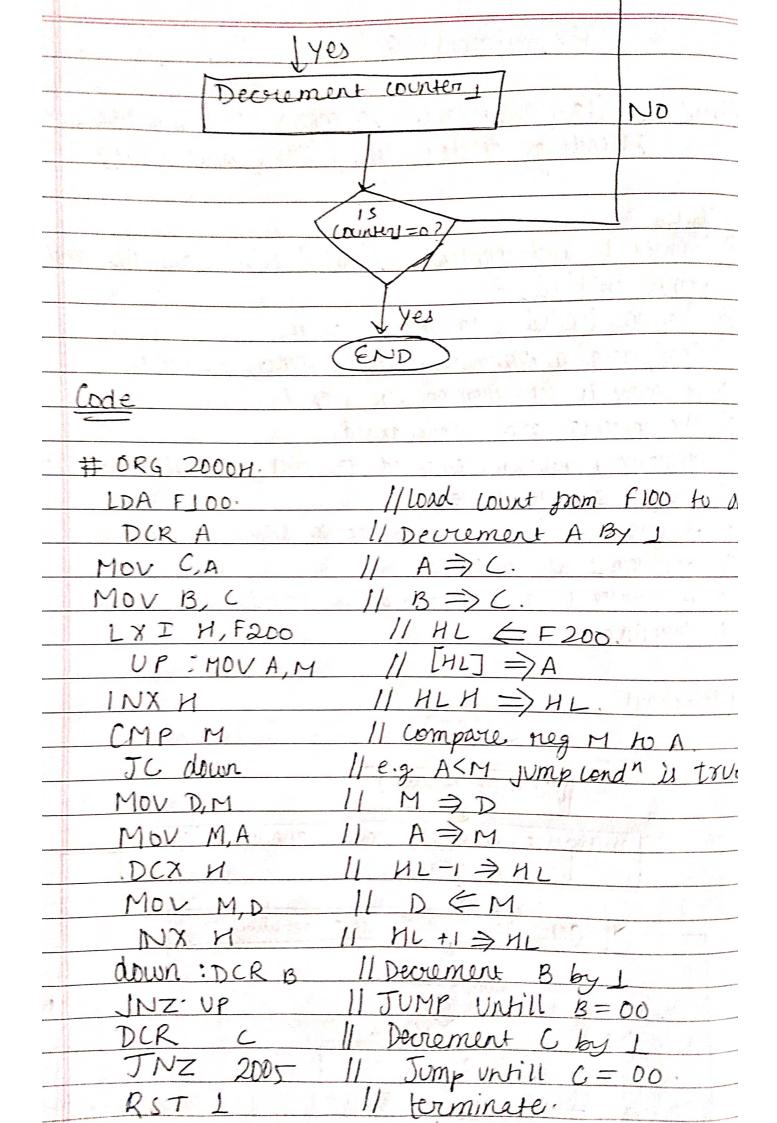
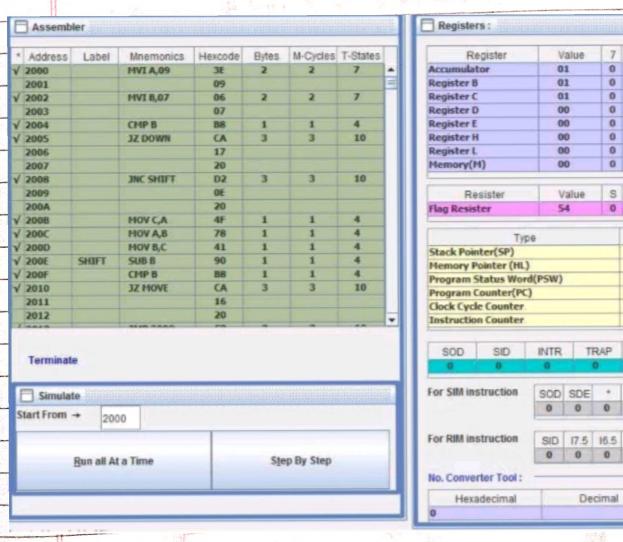
Name: Diksheet Agarwal Scholar ID: 1912059 Class: - Computer Science and Engineening Section: -Mark State of the state of the

A STREET, SQUARE, SQUA		
	Experiment - 6	
	Nime - Illinite a perpoxam to avenue a number	
	Aim: - White a program to overlange a number is aslerding order using 8085 and verity.	<i>Y</i> (
	usulary of all using soss wer verify.	
	1200444	
	O Counter 1 and counter 2 loaded with counters of	ad mamori
		w number
	points initialised @ The no. loaded into accumulation.	# 1
	6) Common a commutation and memory pointer	
	3 Compare accumulator and memory pointer.	
-	G it cavery is set then go son step 7.	
	(5) The consents one interchanged.	lovation
	6 memory pointer invemented to point next memory	producti
ner 1	(3) Counter 2 decremented	-
illi.	(8) if counter 2 is not zero, go to step 2.	
	(3) (DOUGOT I DECEMBER )	
	(6) it counter 1 is not zero, go to step 1	
	(1) Terminate.	
	Flowthout (START)	
	mitialise counter I	
	Initialise memory pointer and initialise	
-	mitialise money points	
	ARTA ARTA	
	get the number into accumulator	A.10
) př	The state of the s	
	acc, > monory	No.
	No Interchange convers.	
		1
	Deviement counter 2	
	Invenent memory pointer to point rext	
	memory tocation	
		36.71
-	NO (15 0?)	
	LOUNTEN Z	

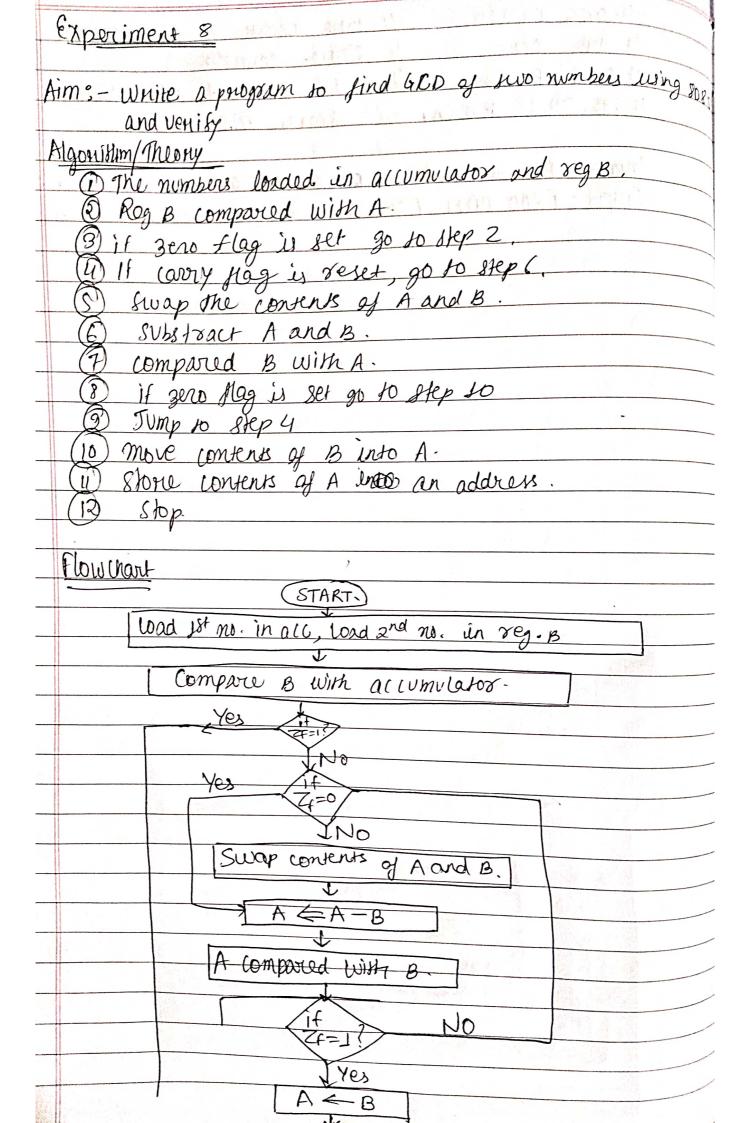


#ORG FLOOM 11 stone country of the address # DB 04 11 store country. # ORG FROOM 11 store now address. # DB, DD, LC, BB, AA // Shorte nos.

Input: F100-044, F200-004, F201-CCH, F202-EEH, F203-AAH Output: F200-AAM, F201-BBM, F202-CCH, F203-DDM



Register	• JOHN	nawa:	ned m	Biolick.	William .	MERK!	enjed	PERM	a sa		
Re	gister	V	alue	7	6	5 4	3	2	1	0	
Accumulat			01		0	0 0	0	_			
Register B		01		0	0	0 0	0	0	0	1	
Register C		0	01		0	0 0	0	-	0 1	1	
Register D		00 00 00 00		0		0 0	0		0	0	
Register E				0	0	0 0	0	0	0		
Register H				0	0	0 0	0	-	0		
Register L				0	0	0 0	0		0		
Memory(M	0	00			0 0	0	0	0	0		
Res	sister	V	alue	S	Z	* AC		Р	. (	CY	
Flag Resist	er	5	4	0	1	0 1	0	1	0	0	
Program Counter(PC) Clock Cycle Counter Instruction Counter						0009 244 38					
-											
SOD	SID	INTR	T	RAP	R7	.5	R6.5		R5.5	9	
0	0	0		_	- 0				-		
For SIM ins	truction	SOD	SDE		R7.5	MSE	M7.5	M6.5	5 M	5.	
		0	0	0	0	0	0	0		0	
For RIM instruction		SID	17.5	16.5	15.5	IE	M7.5	M6.5	5 M	5.5	
		0	0	0	0	0	0	0		0	
No. Conver	ter Tool :										
	decimal	Decimal			Binary						
Hexa	decimal				0						



Yes Storie Ainto memory Wde : #ORG 2000H. MVI.A, 09 // Load Host no. in reg A. MVI B, 07 // Load Second no. in reg B. CMP B 11 Compare 13 to A. Jz down 11 True if A=B. JNC Shift 11 True it A7B. MOVA,B //  $A \in B$ . MOVB, G //  $C \Leftarrow B$ . Shitt: SUB B // A-B=>A. CMPB 11 COMPARE B to A. JZ move 11 True if A=B. JMP 2008 | Jump untill A=B. move: Mov A,B 11 B=>A. down: STA F200 // A => [Addrew] RST 1 // Terminate. Mput = A - 09H, B-67H. OUTPUT: A-01 H, F200-01H.

