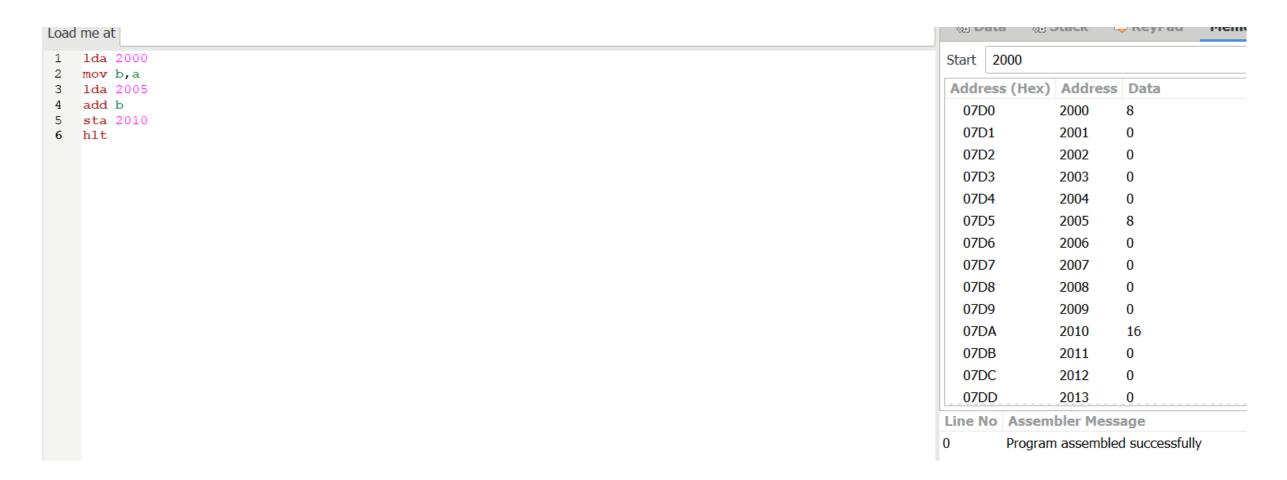
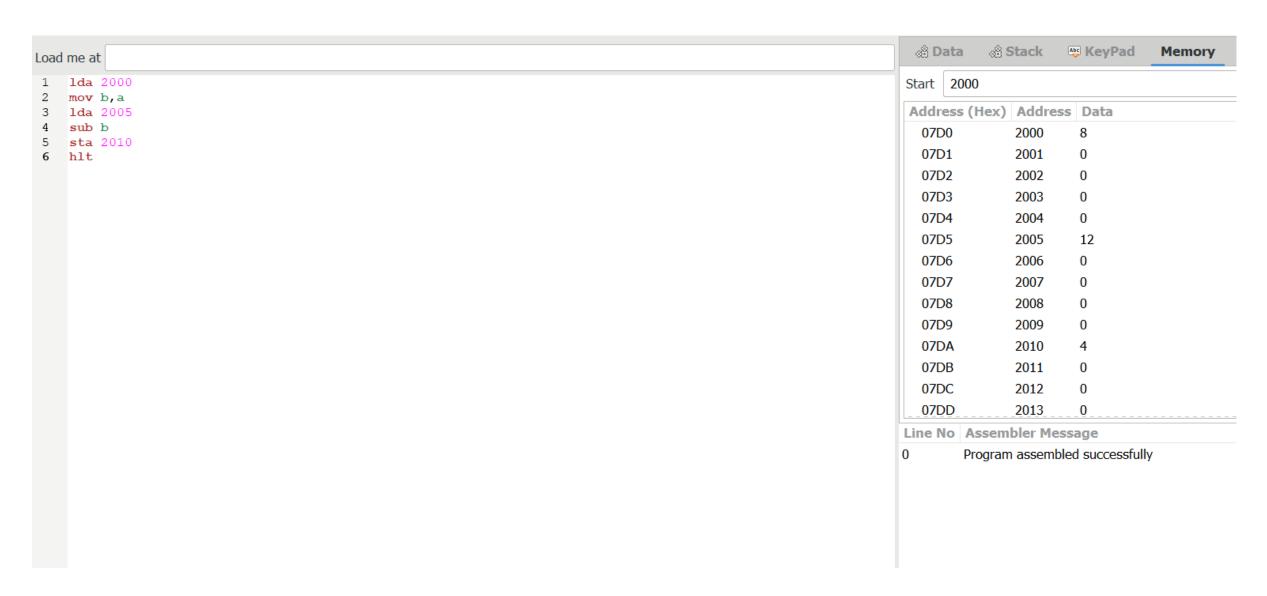
COMPUTER ARCHITECTURE

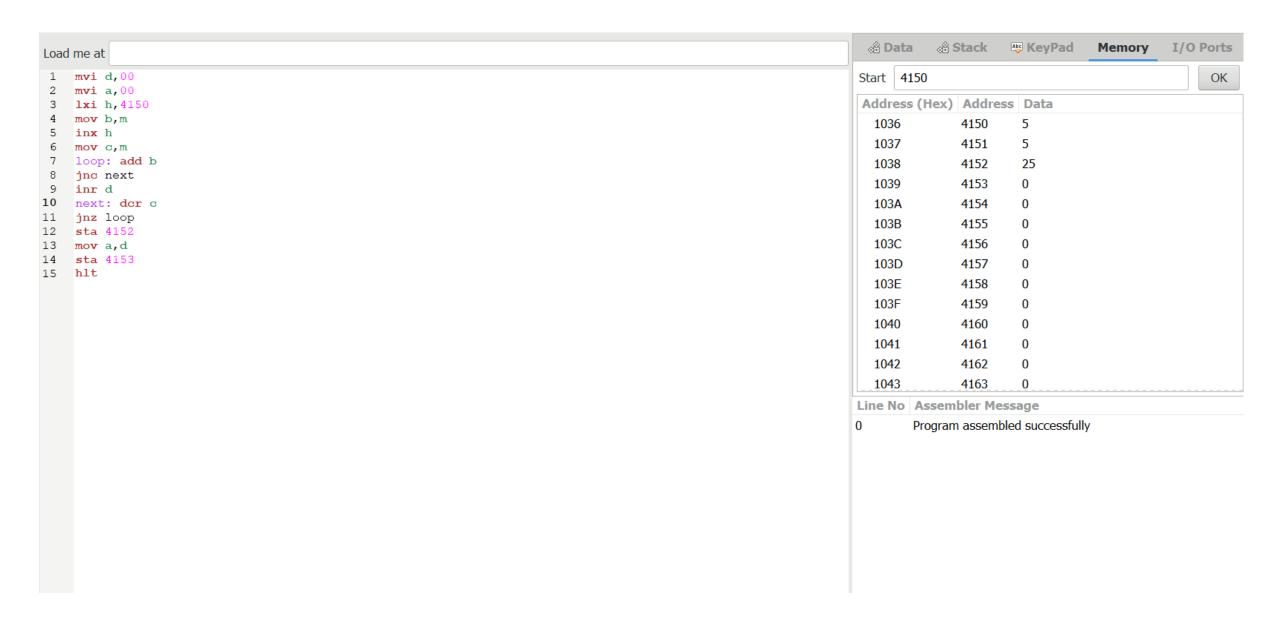
1.8BIT ADDITION



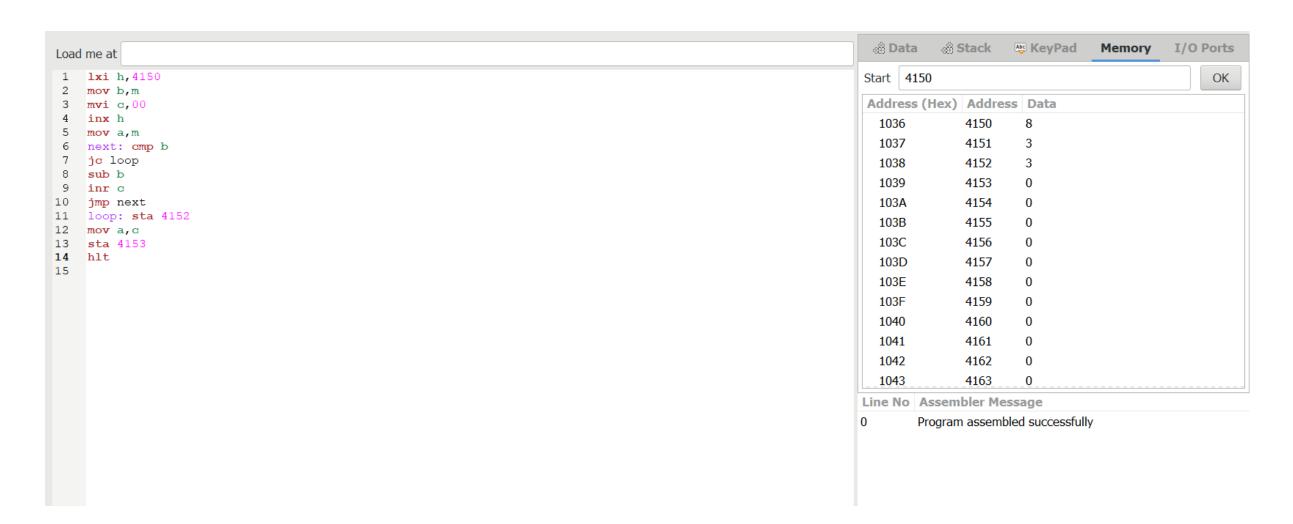
2.8BIT SUBTRACTION



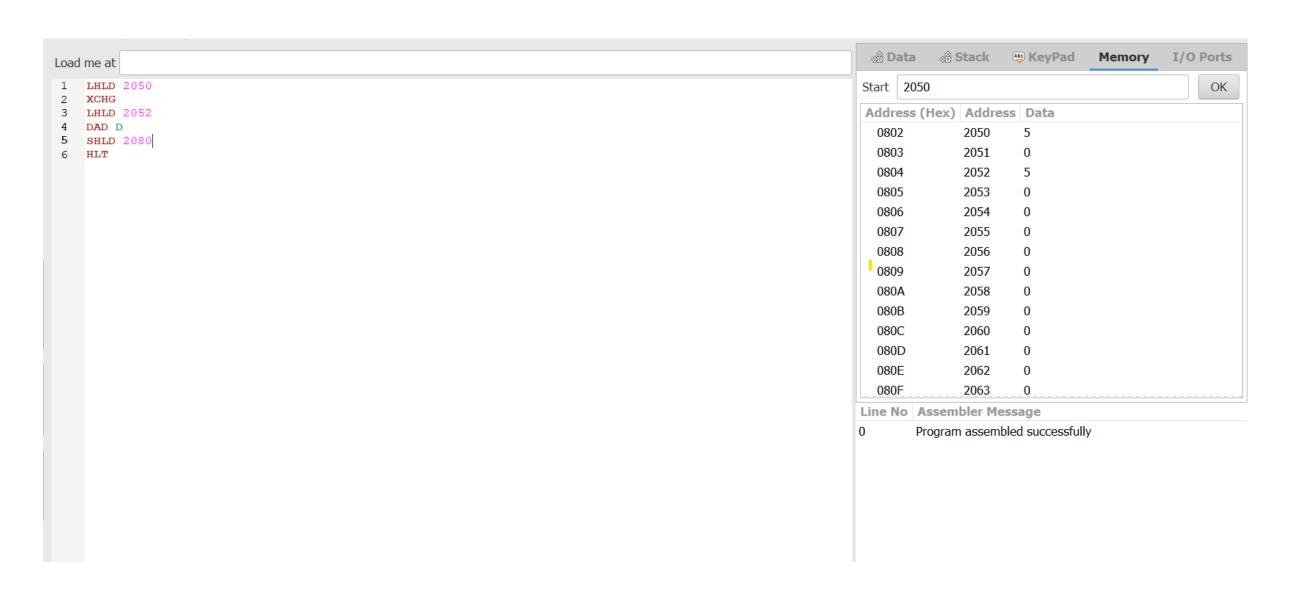
3.8BIT MULTIPLICATION



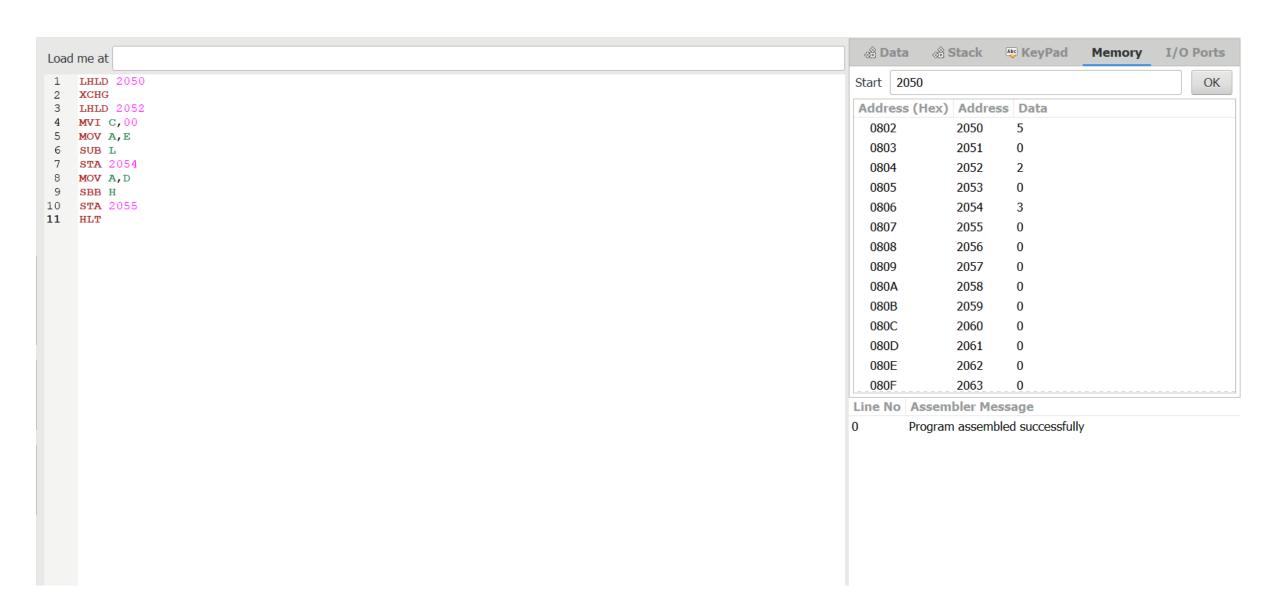
4.8BIT DIVISION



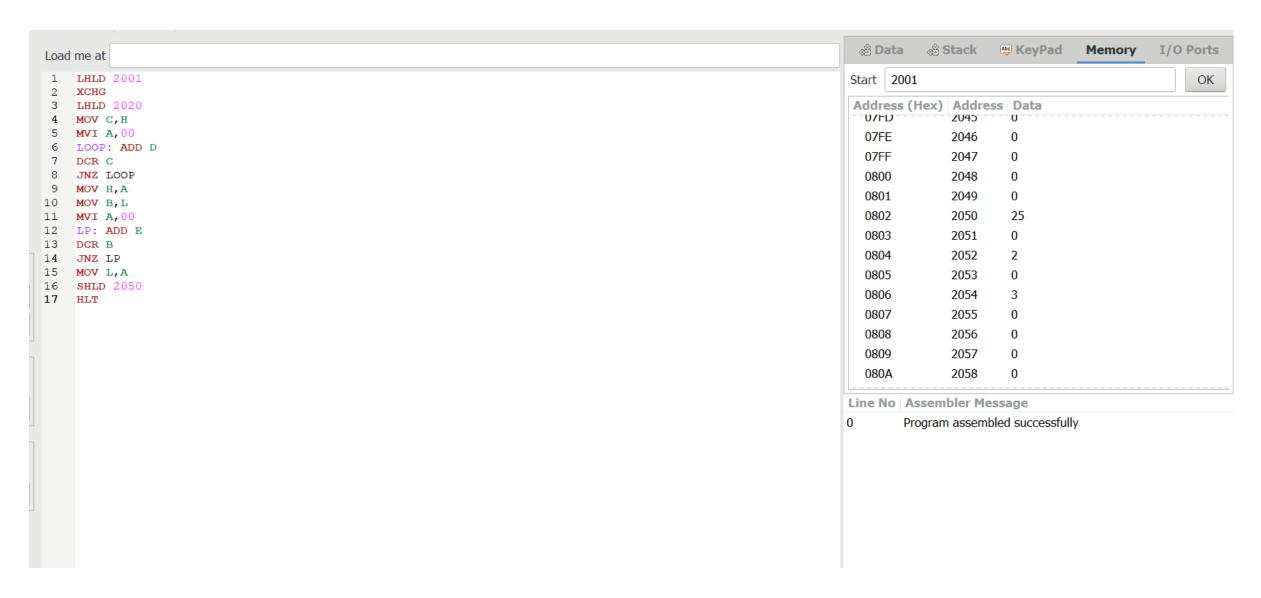
5.16BIT ADDITION



6.16BIT SUBTRACTION



7.16BIT MULTIPLICATION



8.16BIT DIVISION

