VNR VJIET Name of the Laboratory:	Name of the Experim futallation Experiment No:	
Embedded System & Design. Keil U vision Software of the following are the steps to be keil Software (installation).	Installation Process.	
1) Google Keil-com		

2) Go to download - Product Downloads -> MDK - ARM 3) In MDK - ARM page, Fill in details, company: VNRVJIET, JobTitle: Student

Last Column: LPC 1768 (Device

4) Submit the details. 5) Click on setup dile "MDK538A. EXE" (908.615 KMB)

6) Once, the exe file is downloaded double click on the file for setup and run. Follow the instructions prompted during installation. In software

1 Package Installer opens @ Select . IC EPC 1768 (Nxp)

3 In device window, nxp LPC 1700 series LPC 17xx goto > APC 1768

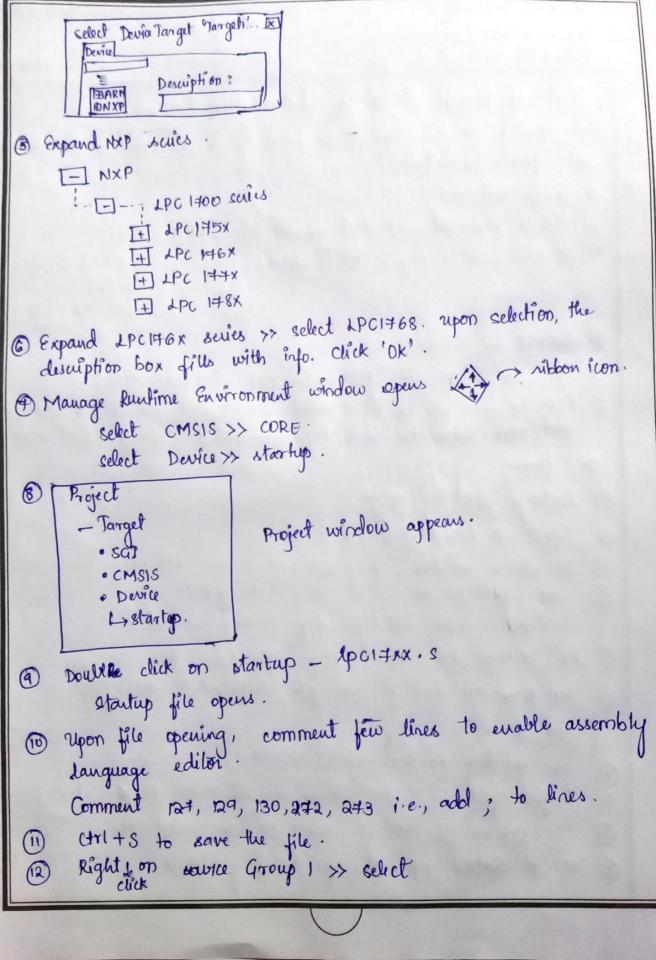
17 packages on right get displayed schol keil: LPC 1700_dfp package -> install.

15 The package has to display "upto date" then close 6 Keil Morsion desktop icon gets displayed on desktop.

Project Creation

O Close window, Random project appears @ Project folder, right dick - new Il vision project . (·uv proj.

@ Soon a window peps out for selecting device.



" Ald new item to Group 'Source Group 1' window pops.
Add new item. e più (·c) c po più (·cy) nsmfile (·s)
select (-5) ASM file is assembly language Name the file
(B) Vuity file name Sa! to confirm water
10 Now start writing the instructions
AREA AUGN=2
13 save program.
@ Right click on Jarget 7> Select options (Alt+F7).
window opens -, options for Target 'target i'
1 Now Goto Debug panel >> select use signulator >> click Ok.
(18) Go to projects click on translate option for compilation (18) Now link all header files to the source file to cuate object file>>> select Build project in Project Panel-
file >> select Build project in Project Panel-
50 Start/Stop debug their run the program.
Note: To edit a file come out of debug made, edit, translate
Note: To edit a file come out of debug made, edit, translate and build the project and debug and run.

kluite an assembly program to add two numbers, Paggiam 3 : Addition of a numbers. AREA ADDITION, LODE, READONLY, ARIGN = 2 EMTRY EXPORT __ main - maio MOV RO, #10 MOV RI, #15 ADD RI, RO STOP B STOP END . en register window O[P: 0 X 000 000 0 A Ro R1 0x0000000 0F After execution. 01000 000 15 RI

VNR VJIET

Name of the Laboratory: Embedded System Design daboratory. Experiment No: 01 Date: 17/02/2023

Name of the Experiment: ARTHMETIC OPERATIONS .

Write a foregram to perform arithemetic operations in assembly language on two soperands.

Program:

AREA ARITHMETIC, CODE, READONLY

ENTRY

EXPORT __main

NUMI DCD 0x0000000B

NUM 2 DCD 0x 0000000E

- main

LDR RO, NUMI

LDR RI, NUM2

ADDS R2, R1, R0

BUBS R3, R1, RO

MUL Ry, Ri, Ro

UDIV Ro, Ri, Ro

STOP B STOP

END.

output:

0x0000000 B Ru

R1 0 x 000 0000E

R, 0x 00000019

0 x 000 000 03 R3

0 X 00000009A Ru

R5 0 x 000 000 1 x PSR 0 x 21 00000

0

0

0

- 0 0

Disabled - ISR

```
Write a foregram to see address of register stored in address
     memory plane.
       AREA Add, code, READONLY
       ENTRY
       EXPORT _- main
     moun
       LDR Ro, = 123
       LDR R, = 456
       mul R2, R1, Ro
       lda Rq, = PROD
       str R2, [R4]
       AREA Slove, DATA READWRITE
  PROD DCD OXO
      END
 Output:
on Memory 1 plane.
 Address : Ry
 0000 DB18.
   Ro 1 0x 000 000 7B
```

Ry, Re have same address spaces. (Re stored in Ry).

R1 ; 0x 000 00 1 C8

R2 : 0x000 0DB18.