## **COMPUTER ORGANIZATION AND ARCHITECTURE**

NAME: VAYYA VISHNUPRIYA

REG: AP22110010390

CLASS: CSE-F

## LAB TASK 3

1) Write a program in assembly language to perform subtraction of 8-bit data. org 100h num1 db 24h num2 db 21h start: mov al, num1 sub al,num2 mov bl,al mov ah,al and ah,0f0h shr ah,4 add ah,30h cmp ah,39h jle print\_first\_digit

```
print_first_digit:
 mov dl,ah
 mov ah,02h
 int 21h
mov ah,bl
and ah,0Fh
add ah,30h
cmp ah,39h
jle print_second_digit
add ah,7
print_second_digit:
 mov dl,ah
 mov ah,02h
 int 21h
mov ah,4Ch
int 21h
```

**OUTPUT:** 

add ah,7



2) Write an assembly language program to perform subtraction of 16-bit data.

org 100h

num1 dw 5678h

num2 dw 2024h

start:

mov ax, num1

sub ax, num2

mov bx, ax

mov ah, bh

shr ah, 4

add ah, 30h

cmp ah, 39h

jle print\_high\_nibble

```
add ah, 7
print_high_nibble:
mov dl, ah
mov ah, 02h
int 21h
mov ah, bh
and ah, 0fh
add ah, 30h
cmp ah, 39h
jle print_low_nibble
add ah, 7
print_low_nibble:
mov dl, ah
mov ah, 02h
int 21h
mov ah, bl
shr ah, 4
add ah, 30h
cmp ah, 39h
jle print_high_nibble2
add ah, 7
print_high_nibble2:
mov dl, ah
mov ah, 02h
```

mov ah, bl

and ah, 0fh

add ah, 30h

cmp ah, 39h

jle print\_low\_nibble2

add ah, 7

print\_low\_nibble2:

mov dl, ah

mov ah, 02h

int 21h

mov ah, 4ch

int 21h

## **OUTPUT:**



## GITHUB LINK: https://github.com/vishnupriyavayya/COA-LAB-TASK-3

