

Saragadam R V Vishwanath | EE10B035

Indian Institute of Technology Madras



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EDUCATION

Program	Institution	%/CGPA	Year of completion
B Tech (Hons) in Electrical Engineering with minor in Operations Research.	Indian Institute of Technology Madras, Chennai	9.39/10.0	2014
XII	Narayana Junior College, S.R.Nagar, Hyderabad	98.4 %	2010
X	Chinmaya Vidyalaya, Hyderabad	92.0 %	2008

SCHOLASTIC ACHIEVEMENTS

- IITJEE 2010 All India Rank 155
- Recipient of CBSE merit scholarship for professional studies-AIEEE (226th rank in AIEEE 2010)
- State 9th position in XII board exam
- Qualified for KVPY fellowship program
- Qualified for first round of Physics and Chemistry Olympiad
- 14th rank in NSTSE exam, 2009

COURSE WORK

- Electric and magnetic circuits
- Networks and systems.
- Digital systems
- Analog and Digital signal processing
- EM Fields
- Electrical machines
- Computer Organization and Architecture
- Probability, statistics and stochastic processes
- Analog circuits
- Control systems
- Solid state devices
- Introduction to social networks

LABS

- CAD Lab for Electrical Engineering
- Digital Circuits Lab
- Microprocessor lab

SKILLS

- Computer Languages : C, C++, Python, VHDL, Verilog, Assembly(ARM, 8051)
- Computer Tools and Platforms: AutoCAD, MS-Office, Adobe Flash, Autodesk 3DS-Max, Xilinx, Keil uVision, ModelSim, PSPICE.
- Operating Systems known: Microsoft Windows, Linux (Redhat, Debian).

PROJECTS

- Creating low cost tactile graphics for visually impaired (Tactograph). (1 Year)
 - Project selected for **IEEE AIYEHUM** and **EPICS** competition.
 - Under the guidance of **Dr. Anil Prabhakar**, Professor, Department of Electrical Engineering IITMadras and **Dr.Nitin Chandrachoodan**, Department of Electrical Engineering IITMadras.
 - **Patent pending**(Patent Application No. 4091/CHE/ 2011“Tactograph”Date of Entry 28th November 2011 in the names of Indian Institute of Technology- Madras and Chetana Charitable Trust).

- Project funded by IC&SR IITMadras.

PROFESSIONAL EXPERIENCE

- Intern at Sasken Communication Technologies Ltd.
 - Ported Mozilla's new mobile operating system Boot to Gecko(B2G) to a low cost android mobile
 - Involved cross compiling Gecko render engine and Linux kernel for ARM platform.

POSITIONS OF RESPONSIBILITY

- Coordinator, Design team, Saarang 2012
- Coordinator, Design team, Shaastra 2011
- Snet ambassador, Saraswathi hostel, IITMadras
- Coordinator, Electronics workshop, Shaastra 2013
- Advisory committee member, NSS IITM 2013

CO-CURRICULAR/EXTRA-CURRICULAR ACTIVITIES

- Programming
 - Outstanding student award, NIIT school curriculum. Won first prize in the School zone
 - Wrote programs in python and C for ASCII image generation, Simple USB interrupt window, A simple drawing utility.
 - Created A powerful and fast image processing library using Python and scipy , which includes implementation of Canny edge detection, Edge follow algorithm, image registration using FFT, and image registration using SIFT and RANSAC algorithm.
- Electronics hobbyist
 - Created libusb based USB device using Microchip USB stack
 - Low cost servo motor based 2D plotter
 - Automatic water flow cutoff valve for overhead tank
 - Electronic stethoscope on MSP430 platform
- Participate actively in fine arts.
 - Won second prize in Horlicks whizz kids competition
 - Best props award in School
- Part of NSS IITMadras(2010-2013)

OBJECTIVE

To develop an aptitude for research by obtaining an internship in an environment that helps further my interest in control engineering and signal processing.