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Post silicon Validation &Debug, Power & Performance  [gouthamkini20@gmail.com](mailto:gouthamkini20@gmail.com)

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# Profile

* 2 years of industry experience in Post Silicon function validation, Debugging, Power and Performance execution and Triage of Intel Gen Platforms.
* 1 Year of Experience in Windows Performance & Responsiveness Execution, Triage and Debug.
* 1 Year of Experience in Performance & Power ingredient level debug and performance optimization for Intel Mobile platforms and Power optimization for Desktop platforms using NI DAQ/USB DAQ.
* Hands on experience in Debug & Triage for BIOS and SoC Platform issues and to find out the regression Change in the Build which causing the issue
* Currently working on the Performance optimization of Customer Platforms & Desktop Platforms.
* Worked on System Responsiveness Debug and analysis of subphases.
* Have worked with different Intel teams and Supported to root cause issues and working on fixes.
* Worked with leads/Platform Domain owners for designing validation & Debug Experiments for various platforms.

# Skill Set

System Engineer System behavioral analysis for different system states

Power and Performance KPI measurement & enablement, Execution & Triage,Reporting Dashboards. System Responsiveness Responsiveness use cases and sub phases validation,Analysis.

Post Silicon Validation Functional Validation, Board bring up, Power Management flows

Ingredients/Subsystems C States, D States, P States, Basics of Schematics and NI DAQ Configuration file preparation.

Tools NI DAQ tools and Windows ADK

Programming C,Python(entry level),HTML,Data structures.

# Education

* Graduation (B.Tech) , Information Science and Engineering ,Canara Engineering College Mangalore,(2015- 2019)- 7.14/10

# Professional Experience

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| Power and Performance Engineer | Wipro Technologies(Intel Client  Location),Bangalore | May’20-current |
| System Validation and Debug Engineer | WiproTechnologies,(Intel Client Location) | June’19-May’20 |

Roles and Responsibilities in key positions

* Power and Performance Engineer (Present Role)
  + Currently owning the Performance optimization compliance for desktops and customer platforms.
  + Optimizing the Power and Performance targets based on industry standard benchmarks
  + Run Power and Performance KPI’s and own execution /experiment
  + The analysis of the workloads and their power dissipation to drive the power optimization, efforts to achieve best in-class power/performance on SoC and presenting platform power data of all imitating workloads to customer based on BKC Release and Helping Engineers to Debug the issues of Power Dissipation
  + Debugging the Responsiveness and Performance Issues to root cause the failures.
  + Setting Up NI DAQ and Configuration file for different Platforms.
  + Interactions with Stake holders for status update and to mentor the team.

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* POST Silicon Validation, Platform Validation & Debug.
  + Good Experience in Functional testing of SoC and Platform Validation
  + Direct Experience & Specialized in Intel Power Management testing.(System SleepStates, CPU Sleep states,Device Power states)
  + Analyzing windows system power consumption using windows performance analyzer
  + Adhered to quality processes and timelines.

# Innovations/Optimization on Job

* Played pivotal role for Intel client to achieve Intel's highest honor for execution excellence award by meeting PNP goals while pulling in schedule by 9 weeks.

# Special Interests

* System Power & performance Optimization and improvement
* Volunteering & Leadership

Personal Details

DOB: 20th Oct 1997

Languages known: English,Kannada,Konkani.

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