Personal Information

Name and Surname: Vishwanath Martur

Address: Behind APMC Sindagi
586128

Date of Birth: 19.03.1999

Phone number: 9206121393
9986265490

E-mail: vishwamartur@gmail.com

Linkedin: http://linkedin.com/in/vishwa-martur/

Education

| 06.2018 - 11.2021 | **SPVVS GPP BCA, Rani Channamma University, Belagavi, India**
Bachelor of Computer Application |
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| 06.2015 - 07.2018 | **PES PU COLLEGE, SINDAGI , BIJAPUR - 586128**
SCIENCE |
| 06.2012 - 07.2015 | **SSSLS HS MUDDENAHALLI**
HIGH SCHOOL |

Languages

English -

Kannada -

Hindi -

Personality

Communicative

Creativity

Punctuality

Organized

Work Experience in Harman Connected Services (Date of joining-07/01/2022)

Domain knowledge in Power Management and doing post-silicon validation.

Knowledge about Board bring-up, Flashing IFWI, and Installing BKC OS based on the client's requirement. Basic knowledge about BIOS, and OS settings post-installation as per the config sheet.

Worked with Tools like dediprog, wimager, intel cycling tool, Etc...

practical skillset and some theoretical knowledge about how different OSs work. Supported OS for STEP is Windows 2019 and Red Hat Linux 7 +. CENT OS,

TESTER 1--POST SILICON VALIDATION ENGINEER

* Hands-on domain knowledge: Power Management on Different Alder Lake projects
* Good knowledge of BIOS flow on I/p and O/P systems.
* Good knowledge of Manual And Automation.
* Good knowledge of PM flows, NPM flows, and Power Management tools.
* I have Good knowledge In System States (Global System Sates G0 --CS, S1, S2, S3, S4, S5, WR)
* I have good knowledge Of PM Tools Like (System Cycling Utility, Power And Thermal Analysis Tool, and System Scope Tool).
* I have good knowledge Of NPM Tools Like (3D Mark, Furmark, Iometer, Pass Mark Burnin, and MemRunner).
* I have Good knowledge In Automation Tools Like (Team City)
* Good at finding issues Like BSODs, Soft Hangs, Hard Hangs, and cater (Catastrophic Error), Debugging PC10 using XDP, Debugging SLP_S0, Socwatch log & Sleepstudy Log collection, Windbg Live debug session enablement, Remote session.
* Good at finding the bug and reporting it to bug-tracking software.
* Collaborated with development team members to establish immediate resolution of identified issues.
* Consolidating and sharing the validation and bug replication report with the client.

Working PSG INDIA FPGA Engeenring

Intel aglix cards, **Firmware** bios flashing, network maintenance, QSFP, and DAC cables support

* Good knowledge about BIOS and BMC flashing using Dediprog software and OS settings post-installation as per the config sheet.
* Good knowledge of PythonSV- usage of ITP commands, hands-on debugging, and PVT deployment as per client Request
* Worked with Tools like Dediprog, Winimager, USB Blaster Etc.
* Experience on Archer City, Wilson City Platforms, Lookout Ridge Boards, Willow Ridge boards, Cotton Ridge Boards
* Good knowledge of BIOS flow
* Experience in isolating, debugging, and root causing the issues and closely working with clients until closure.
* Practical skillset and theoretical knowledge of how different OS works, Good knowledge of FM85 cards, SPR silicon, Intel aglix cards, bios flashing, network maintenance QSFP, and DAC cables support
* Good knowledge of the process of installing Windows 10, CentOS, Ubuntu, and VMware
• Good knowledge of Linux, and CentOS commands.

Experienced in designing, developing, and optimizing software applications in C for embedded systems.

Hands-on experience in working with Linux-based environments, including Debian and Yocto/Open Embedded platforms.

Experienced with Linux kernel modules and network drivers.

Experienced in Make files and the entire build process.

Collaborate with cross-functional teams to define software requirements and specifications.

Conduct debugging and troubleshooting of software and hardware issues.

Experienced in using GIT or equivalent code version control system.

Perform system-level integration and testing of embedded software components.

Ensure compliance with coding standards and best practices.

Stay up-to-date with emerging technologies and trends in embedded systems