CSCI 516 - Fundamental Concepts in Computing and Machine Organization Homework Assignment 6

Due on 11/22/2022 (Tuesday), 11:55PM

Requirement

- · You need to do your assignment independently.
- You need to submit ".pdf" file which contains your solutions to D2L

Questions

Given the following assembly instructions, answer the questions.

loop:

LDUR X9, [X0, #0] LSL X9, X9, #2 STUR X9, [X0, #0] ADD X10, X10, #8 CMP X1, X10 B.GT loop

X0 stores the base address of an array, X1 indicate the number of iterations for this loop.

Questions:

- 1. Assume there is no forwarding or branch prediction. How many stalls are in the pipeline in the previous code $\overline{\text{(one iteration)}}$. List all the stalls between the execution of the instructions.
- 2. Assume there is forwarding, no branch prediction.
- a. Unroll the loop two times.
- b. Reorder the code and see how many cycles are needed to complete the unrolled code.

1. There are 5 stalls in the pipeline

- the LDUR instruction takes 1 Clock cycle to excecute so it stalls the pipeline for 1 clock cycle.
- the LSL instruction takes I clock cycle to excecute so it, stalls the pipeline for I clock cycle.

- the STUR instruction takes I clock cycle to execute so it stalls the Pipeline for I clock cycle.
- the ADD instruction takes I clock cycle to execute so it stalls the pipeline for I clock cycle.
- the CMP instruction takes I clock cycle to execute so it stalls the pipeline for I clock cycle.
- 2. If the loop is unrolled 2 times it will take 10 cycles to complete the unenrolled code.
 - The first instruction, LDUR ×9, [x0,#0], loads the value at memory address x0 into register ×9.
 - The second instruction, LSL X9, X9, #2, Shifts the value in register X9 to the left by 2 bits.
 - The Hird instruction, STUR X9, [X0, #0], stores the value in register X9 back into memory at address X0.
 - The fourth instruction, ADD X10, X10, 748, adds to the Value in register X10.
 - The fifth instruction, CMP XI, X10, compares the Value in register XID.