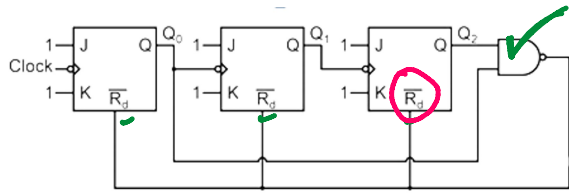


Async mod-5 up

The circuit shown consists of J-K flip-flops, each with an active low asynchronous reset. The counter corresponding to this circuit is \_\_\_\_\_



2D  
AK(1)

QA

QB

✓ Async  
✓ UP ✓

down X

mod-5 (0-4)

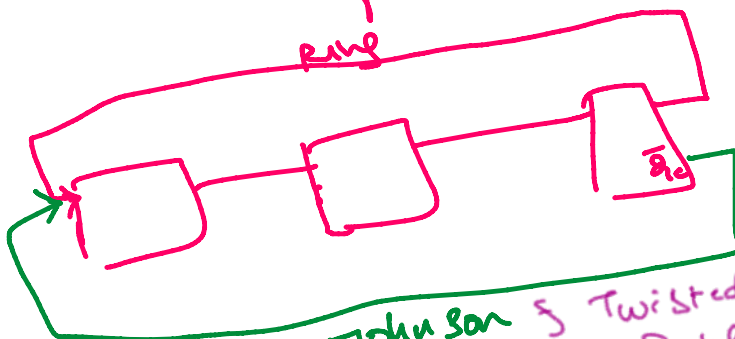
000 ✓  
001 ✓  
010 ✓  
011 ✓  
100 ✓  
101 → Reset  
110  
111

Q0 Q1 Q2  
0 0 0  
0 0 1  
1 0 0  
1 0 1  
1 1 0  
1 1 1

	ANCS	NAMS
00	0	1
01	0	1
10	0	1
11	1	0

Ring counter

Shift Reg



Johnson's Twisted Ring Counter

Ring  
0000  
0100  
0010  
0001

Johnson  
1000  
1100  
1110  
1111  
0111