# SUDHARSHAN.V | EE08B049
## INDIAN INSTITUTE OF TECHNOLOGY MADRAS

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## EDUCATION & SCHOLASTIC ACHIEVEMENTS

| Program | Institution | %/CGPA | Completion Year |
|---|---|---|---|
| **B.Tech**:Electrical Engineering **Minor**:Physics **M.Tech**:VLSI and Microelectronics | IIT Madras, Chennai | **9.20** Rank: **2**(of 20) | 2013 |
| 12th Grade | P.S.B.B. K.K.Nagar, Chennai | 95.0 | 2008 |
| 10th Grade | P.S.B.B. K.K.Nagar, Chennai | 90.8 | 2006 |

- Recipient of **Central Scheme Sector Scholarship** sponsored by the **MHRD** for excelling in XII CBSE examinations
- **Selected** to represent **Electrical Engineering dept. IIT Madras** in the **Indo-German Winter Academy** conducted in December 2011 by IIT Delhi-FAU Erlangen. Presented a Technical Talk on **Resistive Memory Devices**
- **Selected** to the **International Olympiad For Informatics Training Camp**. Ranked in **top 25** at the Indian National Olympiad For Informatics(**INOI**)
- **First** in the Physics Talent Test in January 2008 conducted by The Physics Society, Chennai among **618** students

## INTERNSHIPS

- ### AMERICAN EXPRESS INDIA, GURGAON                                      MAY - JULY 2012
  - Leveraged the **Longitudinal Behaviour** of **Early Transactions** towards predicting **Short & Long Term Risk** - Model **2 times better risk** indication for early defaulters and **50 bp lower** for good customers than existing methods
  - Developed a system that extracts **risky/credit-worthy behaviour** and flags events across different markets worldwide
- ### ITTIAM TECHNOLOGIES PVT LTD, BANGALORE                              MAY - JUNE 2011
  - **Implemented** a model for **Low Density Parity Check**(LDPC) error correcting codes - based on the IEEE 802.11n standard
  - Disadvantages of the simple LDPC Encoder- Decoder realized. Proposed a suitable **Hardware structure** for implementing the LDPC Encoder and Decoder on chip
- ### AGENT TECHNOLOGIES PVT LTD, BANGALORE                               MAY - JUNE 2010
  - **Image Processing** using Basic Graph Theory. Modular library built in `C++` - used in extracting License Plate Details. Hit Rate of **70%**

## POSITIONS OF RESPONSIBILITY

- ### CORE, WEB AND MOBILE OPERATIONS, SHAASTRA                          MAY-OCTOBER 2011
  - **Headed a team of 20 coordinators** to completely **rebuild** Shaastra Website in 2 months with **custom backend** - improved ease of use for Event Coordinators and Participants
  - **Laid the Foundations** for an in-house **Enterprise Resource Planning**(ERP) system for the **entire Shaastra Team**
  - **Initiated and Built** the Backend for **User Registration for Events Using BarCode** to speed up, and **document the participation registration** online for **analytics**; Built **Shaastra Android application** for the **first time**
- ### CORE, ELECTION ENGINEERING TEAM
  - **Lead the Election Engineering Team** for the Student Body Elections 2011. Implemented a **Secure Web Based Voting** for a 5000-strong electorate across 20 polling stations
- ### COORDINATOR, WEB OPERATIONS
  - **Super Coordinator**, Shaastra 2010 - Headed a team of 3 coordinators to build the **Shaastra UserPortal** handling over **3000 registrations** and **all online participation** before Shaastra
  - **Coordinator**, Shaastra 2009 & Saarang 2010 - Built utilities for Hospitality and Facilities Teams and involved in inter-departmental requests and services.

## EXTRA CURRICULAR ACTIVITIES

- Simulation Championship- **Won** $2^{nd}$ **and** $3^{rd}$ in Shaastra 2010, Shaastra 2009 respectively
- **Won** $3^{rd}$ **in Online Programming Contest**, Exebit 2009, technical festival by CS Dept of IIT Madras
- Participated in LM Solo Vocal Lit-Soc 2010. Trained in Vocal Carnatic Music
- Selected for **NSO Cricket** along with 20 others in total