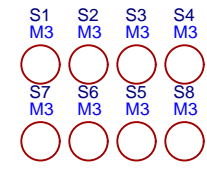
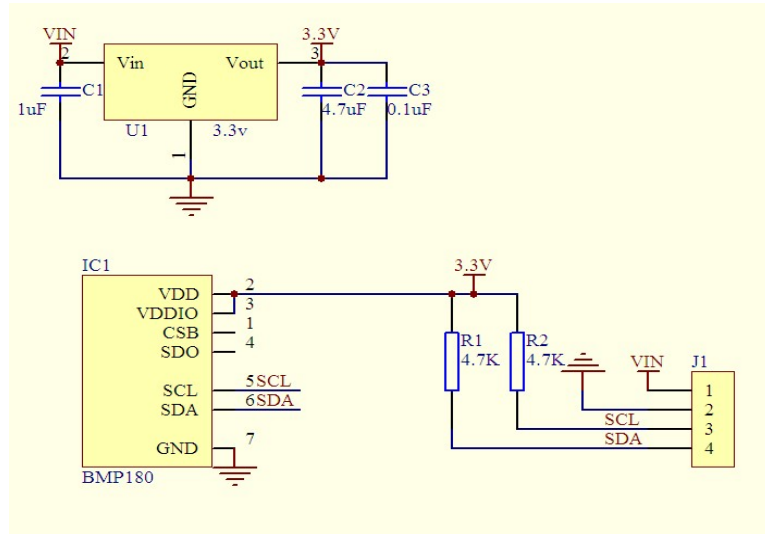
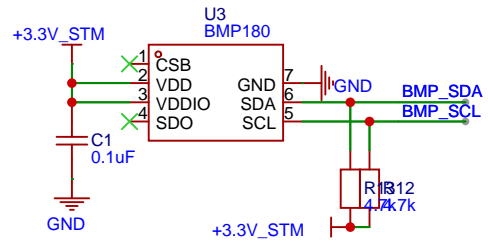
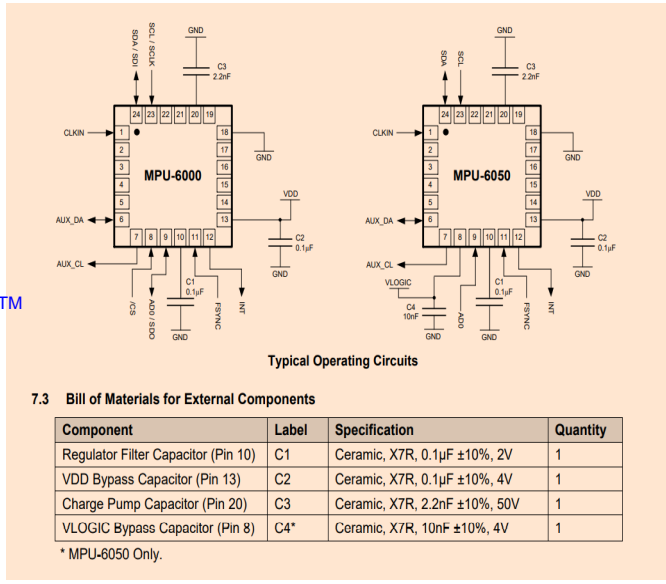
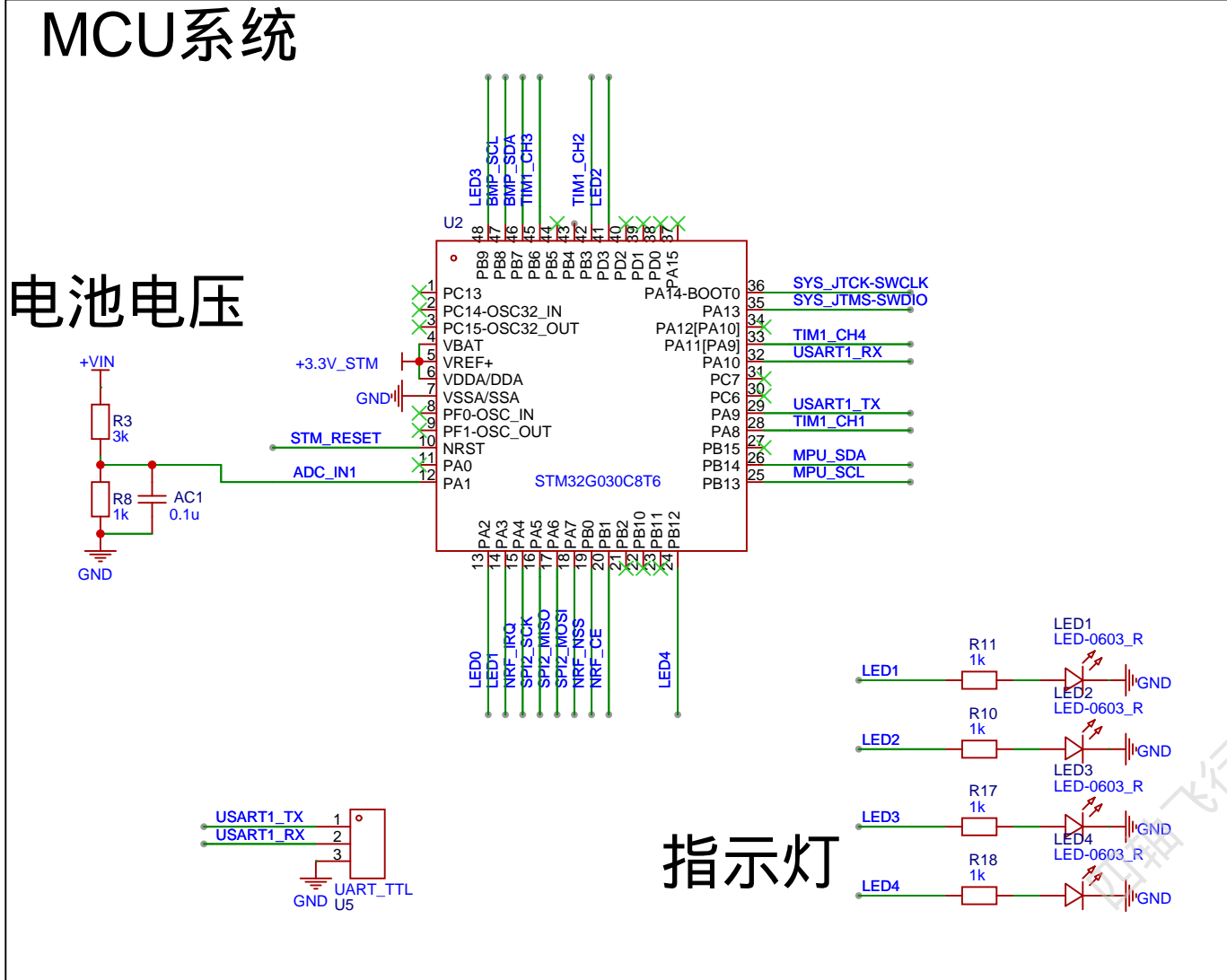
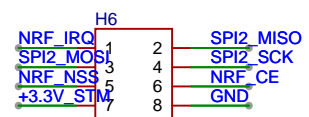
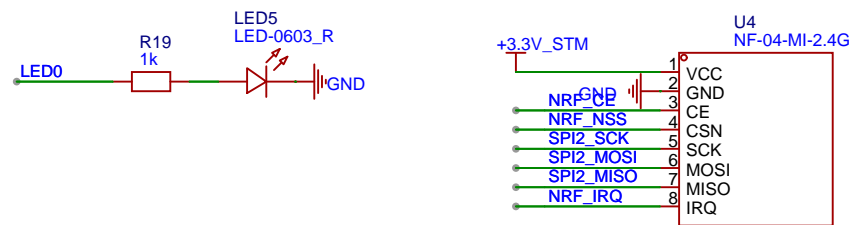


[illegible]

The schematic shows the STM\_RESET pin connected to one terminal of a switch labeled SW1. The other terminal of the switch is connected to a green trace that leads to the SKRKAEE020 component. A capacitor labeled AC8 with a value of 0.1uF is connected between this green trace and ground (GND).

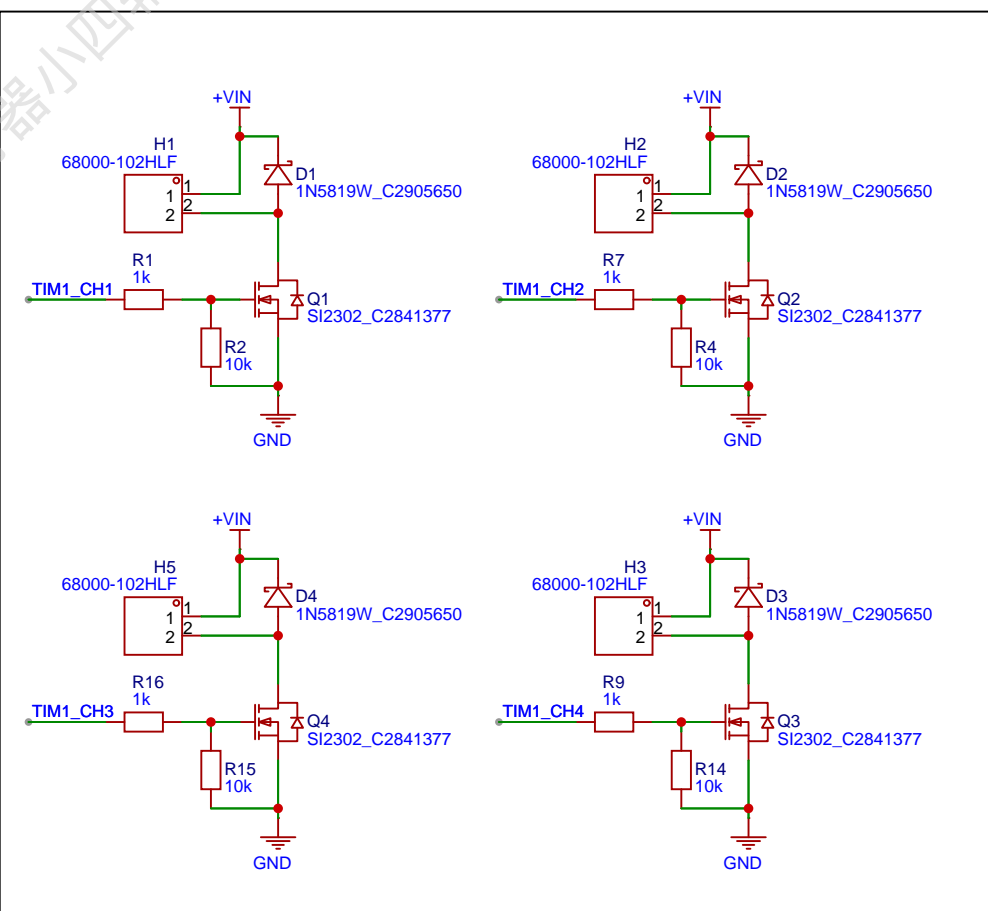
[illegible]

指示灯



# 电源

# 电源



G030 BOOT  
boot\_lock bit默认是0      nboot\_sel bit为1

Boot mode configuration					Selected boot area
BOOT_LOCK bit	nBOOT1 bit	BOOT0 pin	nBOOT_SEL bit	nBOOT0 bit	
0	x	0	0	x	Main Flash memory
0	1	1	0	x	System memory
0	0	1	0	x	Embedded SRAM
0	x	x	1	1	Main Flash memory
0	1	x	1	0	System memory
0	0	x	1	0	Embedded SRAM
1	x	x	x	x	Main Flash memory forced

Boot mode configuration					Selected boot area
BOOT_LOCK bit	nBOOT1 bit	BOOT0 pin	nBOOT_SEL bit	nBOOT0 bit	
0	x	0	0	x	Main Flash memory
0	1	1	0	x	System memory
0	0	1	0	x	Embedded SRAM
0	x	x	1	1	Main Flash memory
0	1	x	1	0	System memory
0	0	x	1	0	Embedded SRAM
1	x	x	x	x	Main Flash memory forced