

### **QUADRUPLE 3-STATE BUFFERS OE LOW**

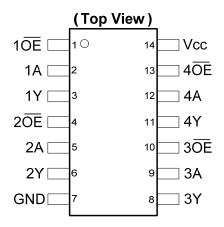
### **Description**

The 74HC125 provides provides four independent buffer gates with 3-state outputs. Each buffer has a separate enable pin that if driven with a high logic level places the corresponding output in the high impedance state. The device is designed for operation with a power supply range of 2.0V to 6.0V.

#### **Features**

- Wide Supply Voltage Range from 2.0V to 6.0V
- Sinks or sources 4mA at  $V_{CC}$  = 4.5V
- CMOS low power consumption
- Schmitt Trigger Action at All Inputs
- ESD Protection Exceeds JESD 22
  - 200-V Machine Model (A115-A)
  - 2000-V Human Body Model (A114-A)
  - Exceeds 1000-V Charged Device Model (C101C)
- Range of Package Options SO-14 and TSSOP-14
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

### **Pin Assignments**



SO-14 / TSSOP-14

### **Applications**

- General Purpose Logic
- Wide array of products such as:
  - PCs, Networking, Notebooks, Netbooks
  - Computer Peripherals, Hard Drives, CD/DVD ROM
  - TV, DVD, DVR, Set Top Box

Notes:

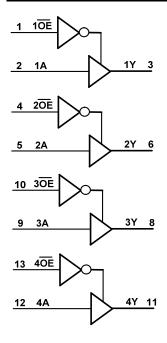
- No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
   See http://www.diodes.com for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
   Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.



# **Pin Descriptions**

Pin Number	Pin Name	Function
1	1 <del>0E</del>	Data Enable Input (active low)
2	1A	Data Input
3	1Y	Data Output
4	2 <del>0E</del>	Data Enable Input (active low)
5	2A	Data Input
6	2Y	Data Output
7	GND	Ground
8	3Y	Data Output
9	3A	Data Input
10	3 <del>OE</del>	Data Enable Input (active low)
11	4Y	Data Outp
12	4A	Data Input
13	4OE	Data Enable Input (active low)
14	Vcc	Supply Voltage

### **Logic Diagram**



### **Function Table**

Inp	Output	
ŌE	Α	Y
L	Н	Н
L	L	L
Н	Х	Z



# Absolute Maximum Ratings (Note 4) (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Symbol	Description	Rating	Unit
ESD HBM	Human Body Model ESD Protection	2	KV
ESD CDM	Charged Device Model ESD Protection	1	KV
ESD MM	Machine Model ESD Protection	200	V
Vcc	Supply Voltage Range	-0.5 to +7.0	V
VI	Input Voltage Range note 3)	-0.5 to +7.0	V
I <sub>IK</sub>	Input Clamp Current V <sub>I</sub> < -0.5V or Vi > V <sub>CC</sub> +0.5V	±20	mA
lok	Output Clamp Current V <sub>O</sub> < -0.5V or V <sub>O</sub> > V <sub>CC</sub> +0.5V	±20	mA
I <sub>O</sub>	Continuous Output Current -0.5V < V <sub>O</sub> V <sub>CC</sub> +0.5V	+/- 25	mA
Icc	Continuous Current Through V <sub>CC</sub>	50	mA
I <sub>GND</sub>	Continuous Current Through GND	-50	mA
T <sub>J</sub> Operating Junction Temperature		-40 to +150	°C
T <sub>STG</sub> Storage Temperature		-65 to +150	°C
P <sub>TOT</sub>	Total Power Dissipation	500	mW

Notes:

# Recommended Operating Conditions (Note 6) (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Symbol	Parameter	Conditions	Min	Max	Unit
V <sub>CC</sub>	Supply Voltage		2.0	6.0	V
VI	Input Voltage		0	V <sub>CC</sub>	V
Vo	Output Voltage		0	Vcc	V
		$V_{CC} = 2.0V$		625	
Δt/ΔV	Input Transition Rise or Fall Rate	$V_{CC} = 4.5V$		140	ns/V
		V <sub>CC</sub> = 6.0V		85	
T <sub>A</sub>	Operating Free-Air Temperature	_	-40	+125	°C

Note: 6. Unused inputs should be held at  $V_{\text{CC}}$  or Ground.

<sup>4.</sup> Stresses beyond the absolute maximum may result in immediate failure or reduced reliability. These are stress values and device operation should be within recommend values.

<sup>5.</sup> Input Voltage cannot exceed  $V_{\text{CC}}$  to the extent the Maximum clamp current is exceeded.



# **Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Cumbal	Parameter	Test Conditions	V	T <sub>A</sub> = -40°0	C to +85°C	T <sub>A</sub> = -40°C	to +125°C	Unit
Symbol	Parameter	rest Conditions	Vcc	Min	Max	Min	Max	Unit
	Library Laurent Laurent		2.0V	1.5		1.5		
$V_{IH}$	High-level Input Voltage		4.5V	3.15		3.15		V
	Voltago		6.0V	4.2		4.2		
	Low lovel lanut		2.0V		0.5		0.5	
$V_{IL}$	Low-level Input voltage		4.5V		1.35		1.35	V
	romage		6.0V		1.8		1.8	
		$I_{OH} = -20 \mu A$	2.0V	1.9		1.9		
	High-level Output Voltage	I <sub>OH</sub> = -20μA	4.5V	4.4		4.4		V
$V_{OH}$		I <sub>OH</sub> = -20μA	6.0V	5.9		5.9		
		$I_{OH} = -4.0$ mA	4.5V	3.84		3.7		
		I <sub>OH</sub> = -5.2mA	6.0V	5.34		5.2		
		I <sub>OL</sub> = 20μA	2.0V		0.1		0.1	
		I <sub>OL</sub> = 20μA	4.5V		0.1		0.1	
$V_{OL}$	Low-level Output Voltage	$I_{OL} = 20\mu A$	6.0V		0.1		0.1	V
	Voltago	I <sub>OL</sub> = 4mA	4.5V		0.33		0.44	1
	I <sub>OL</sub> = 5.2mA	6.0V		0.33		0.44		
l <sub>OZ</sub>	Z State Leakage Current	V <sub>O</sub> = 0 to 6.0V V <sub>I</sub> = GND or 6.0V	6.0V		± 5.0		± 10	μΑ
IĮ	Input Current	V <sub>I</sub> = GND to 5.5V	6.0V		± 1		± 1	μΑ
Icc	Supply Current	$V_I = GND \text{ or } V_{CC}, I_O = 0$	6.0V		20		40	μΑ

# **Switching Characteristics**

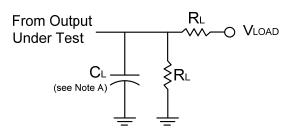
Symbol	Parameter	Test	V		T <sub>A</sub> = +25°C		-40°C to +85°C	-40°C to +125°C	Unit
Symbol	Parameter	Conditions	Vcc	Min	Тур.	Max	Max	Max	Unit
	Danasastian	Figure 1	2.0V	_	30	100	125	150	
t <sub>PD</sub>	Propagation Delay A <sub>N</sub> to Y <sub>N</sub>	Figure 1 $C_L = 50 \text{ pF}$	4.5V	_	11	20	25	30	ns
	Delay AN to TN	CL = 50 pr	6.0V	_	9	17	21	26	
		Figure 1	2.0V	_	41	125	155	190	
$t_{EN}$	Enable Time	Figure 1 $C_L = 50 \text{ pF}$	4.5V	_	15	25	31	38	ns
	OE <sub>N</sub> to Y <sub>N</sub>	CL = 50 pr	6.0V	_	12	21	26	32	
		Figure 1	2.0V	_	41	125	155	190	
t <sub>DIS</sub>	<u>Dis</u> able Time	Figure 1 $C_L = 50 \text{ pF}$	4.5V	_	15	25	31	38	ns
	OE to Y <sub>N</sub>	CL = 50 pr	6.0V	_	12	21	26	32	
		Fig	2.0V	_	14	60	75	90	
t <sub>t</sub>	Transition time	Figure 1 $C_L = 50 \text{ pF}$	4.5V	_	5	12	15	18	ns
İ	s <sub>t</sub>		6.0V	_	4	10	13	15	

# Operating Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Parameter		Test Conditions	V <sub>CC</sub> = 6V Typ	Unit
C <sub>pd</sub>	Power Dissipation Capacitance per Gate	f = 1MHz	22	pF
Cı	Input Capacitance	$V_I = V_{CC} - \text{or GND}$	4	pF



### **Parameter Measurement Information**



TEST	Condition
t <sub>PLZ</sub> (see Notes D and E)	Vload
t <sub>PZL</sub> (see Notes D and F)	Vload

V	Inputs		V	V		В	<b>V</b> Δ
V <sub>CC</sub>	VI	t <sub>r</sub> /t <sub>f</sub>	V <sub>M</sub>	V <sub>M</sub> V <sub>LOAD</sub>	CL	KL	VΔ
2.0V to 6.0V	V <sub>CC</sub>	≤3ns	V <sub>CC</sub> /2	2 X V <sub>CC</sub>	15,50 pF	2 ΚΩ	10% of V <sub>CC</sub>

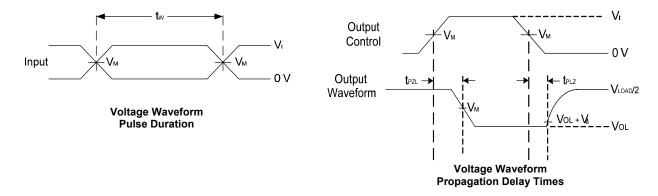


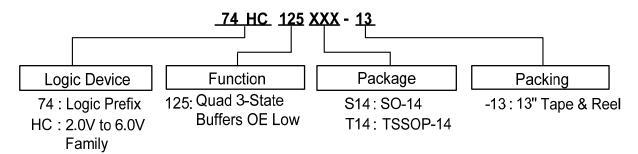
Figure 1 Load Circuit and Voltage Waveforms

Notes: A. Includes test lead and test apparatus capacitance.

- B. All pulses are supplied at pulse repetition rate ≤ 1 MHz.
- C. The inputs are measured one at a time with one transition per measurement.
- D. For the 3 state device  $t_{PLZ}$  and  $t_{PZL}$  are the same as  $t_{PD}$ .
- E.  $t_{\text{PZL}}$  is measured at  $V_{\text{M}}$ .
- D.  $t_{PLZ}$  is measured at  $V_{OL}$  + $V_{\Delta}$ .



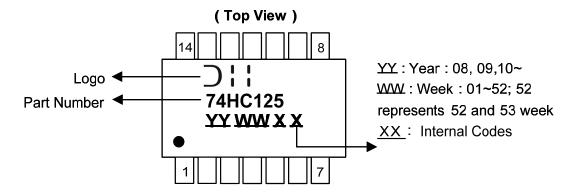
# **Ordering Information**



	Davisa	Device Package Code		7" Tape and Reel		
	Device	Package Code	Packaging	Quantity	Part Number Suffix	
Pb Green	74HC125S14-13	S14	SO-14	2500/Tape & Reel	-13	
Pb Green	74HC125T14-13	T14	TSSOP-14	2500/Tape & Reel	-13	

### **Marking Information**

#### (1) SO-14, TSSOP-14



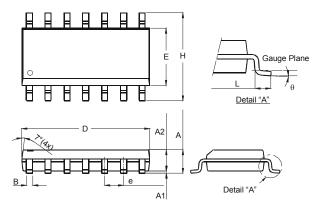
Part Number	Package
74HC125S14	SO-14
74HC125T14	TSSOP-14



# Package Outline Dimensions (All dimensions in mm.)

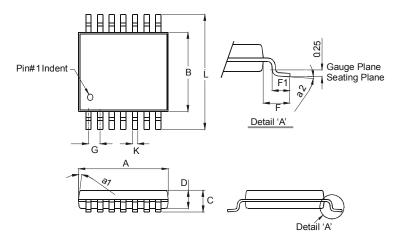
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.

### Package Type: SO-14



	SO-14				
Dim	Min	Max			
Α	1.47	1.73			
<b>A</b> 1	0.10	0.25			
A2	1.45	Тур			
В	0.33	0.51			
D	8.53	8.74			
Е	3.80	3.99			
е	1.27	Тур			
Н	5.80	6.20			
L	0.38	1.27			
θ	0°	8°			
All Dimensions in mm					

### Package Type: TSSOP-14



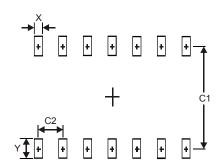
TSSOP-14		
Dim	Min	Max
a1	7° (4X)	
a2	0°	8°
Α	4.9	5.10
В	4.30	4.50
С		1.2
D	8.0	1.05
F	1.00 Typ	
F1	0.45	0.75
G	0.65 Typ	
K	0.19	0.30
L	6.40 Typ	
All Dimensions in mm		



# **Suggested Pad Layout**

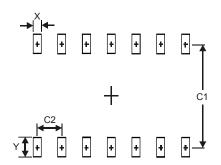
Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for latest version.

#### Package Type: SO-14



Dimensions	Value (in mm)
Х	0.60
Y	1.50
C1	5.4
C2	1.27

#### Package Type: TSSOP-14



Dimensions	Value (in mm)
X	0.45
Υ	1.45
C1	5.9
C2	0.65



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