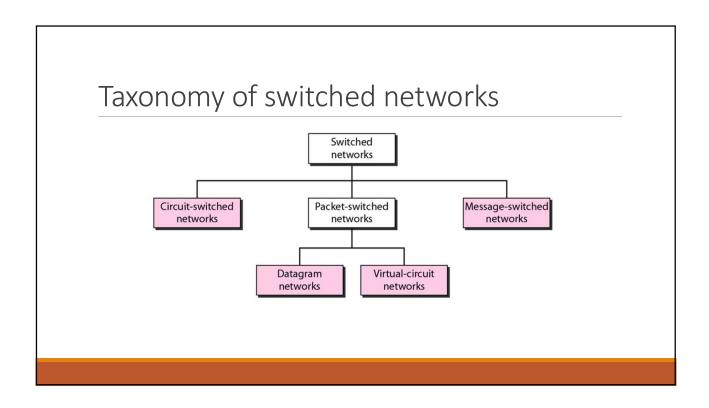
Computer Networks

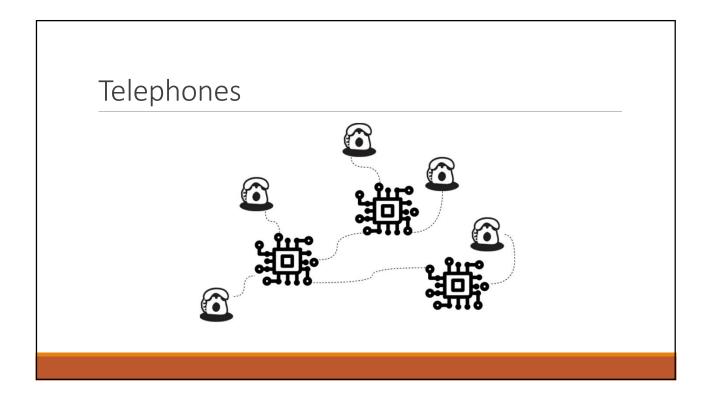
DR. AMRIT PAL VIT CHENNAI

Network

- ■Data Communication
- ■Network Topology
- □LAN/MAN/WAN
- □OSI Model
- ☐TCP/IP Model

How to connect nodes to make one-to-one communication possible?





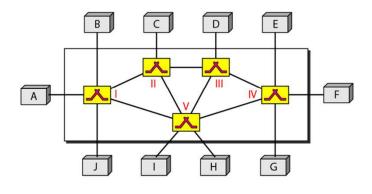
Circuit Switching

- □end-to-end circuit
- ☐ Links and Circuits
- ☐FDM (frequency division multiplexing)
- ☐TDM (time-division multiplexing)

Disadvantages

- ☐ Resource Utilization
- □Connection Establishment Time

Switched network



An Example

Suppose that all links in the network use TDM with 12 slots per second and have bit rate 1.536 Mbps.

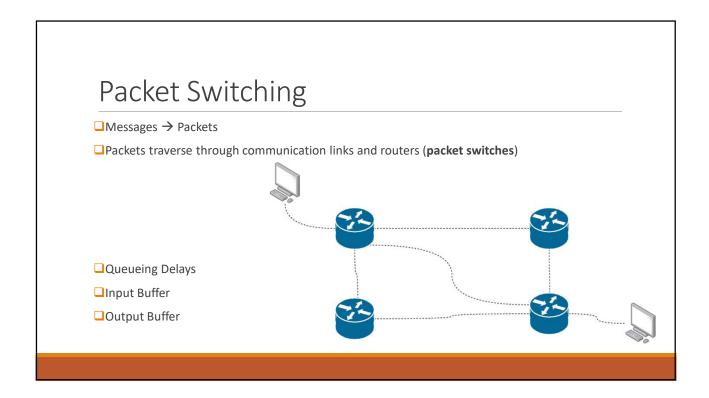
An end-to-end circuit establishment time =500 msec

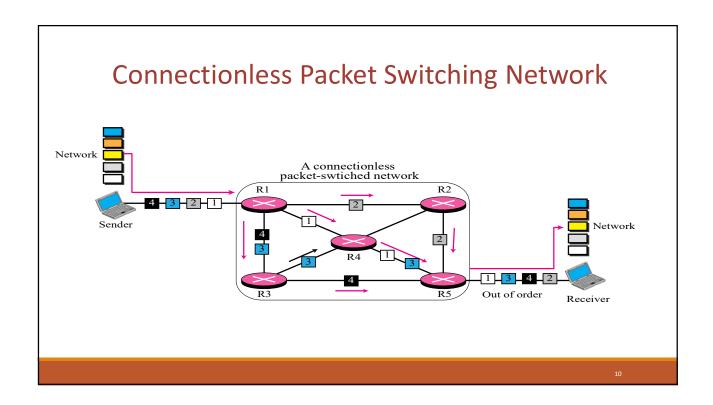
How long does it take to send a file of 640Kbits?

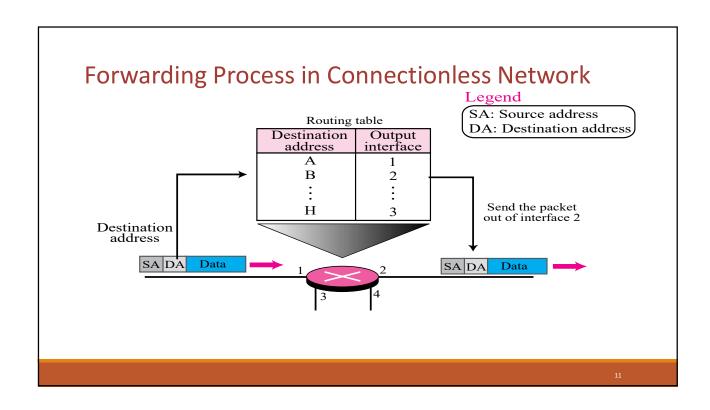
Each circuit has a transmission rate= (1.536 Mbps)/12 = 128 Kbps,

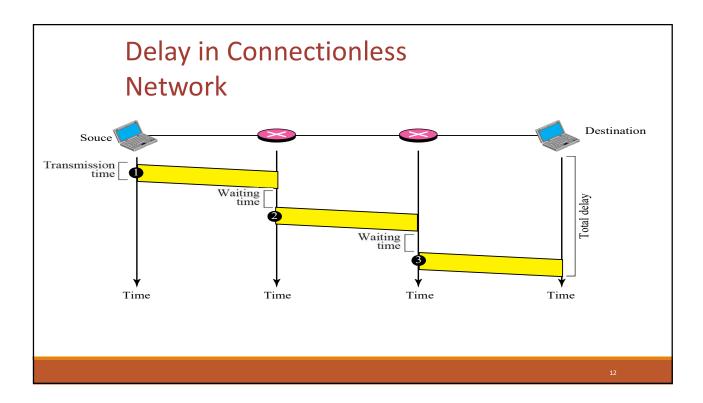
Time to transmit the file= (640 Kbits)/(128 Kbps) = 5 seconds.

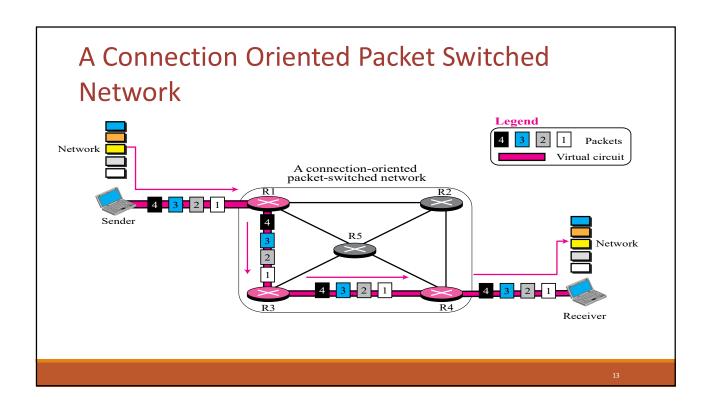
Total time- 5.5sec

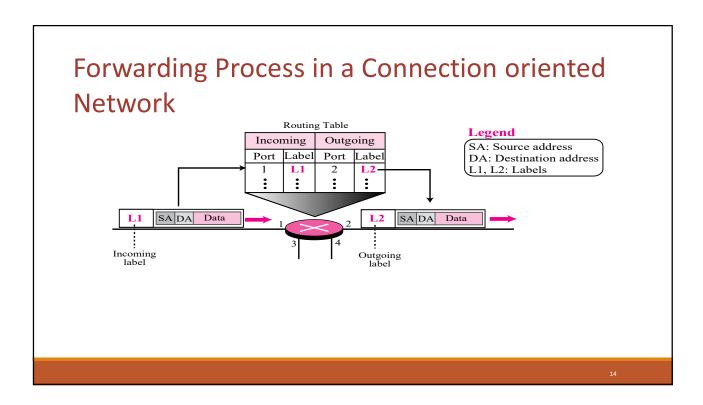


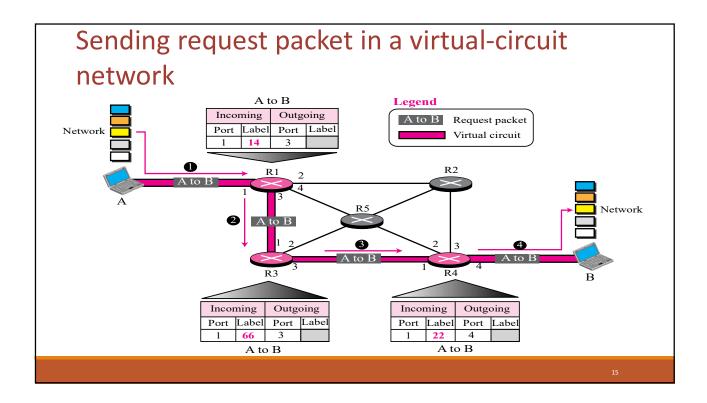


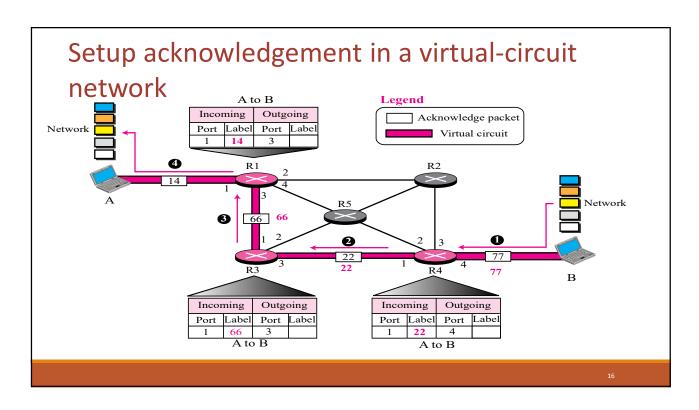


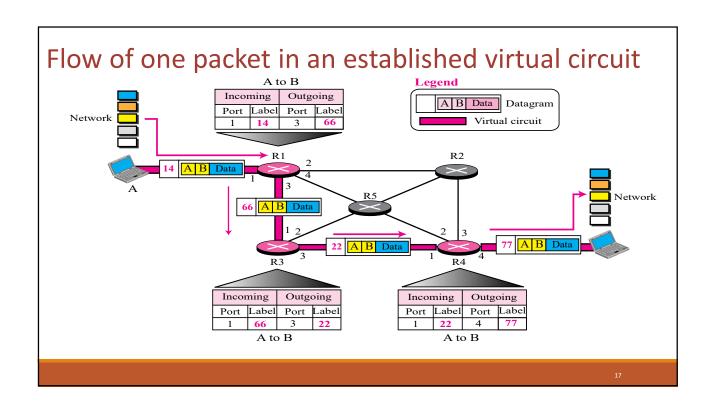


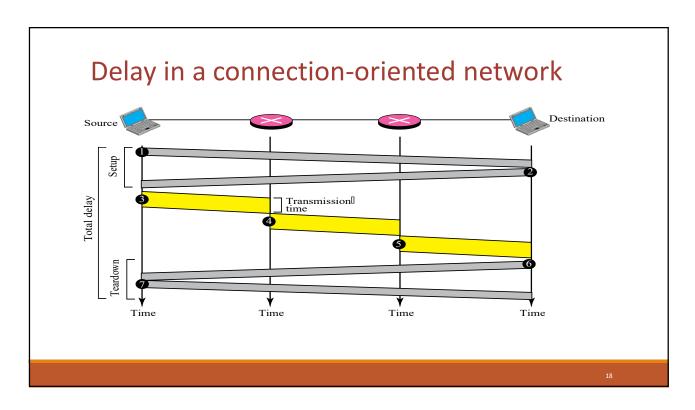






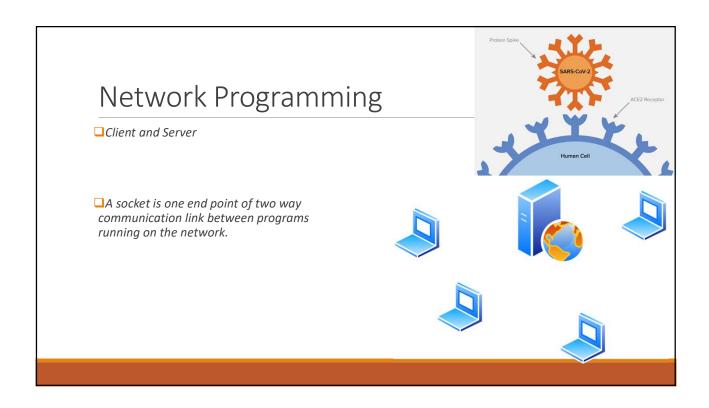


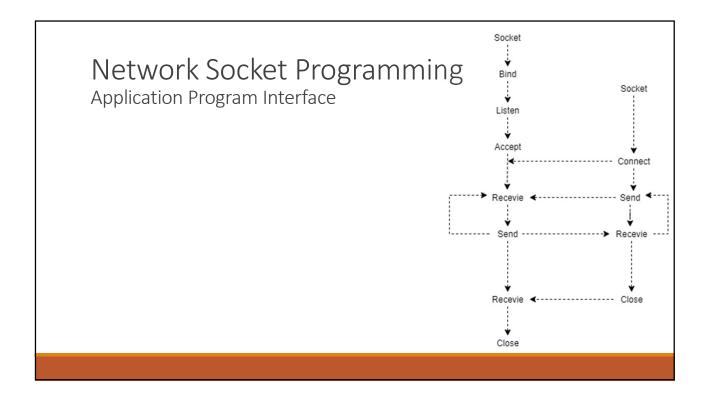


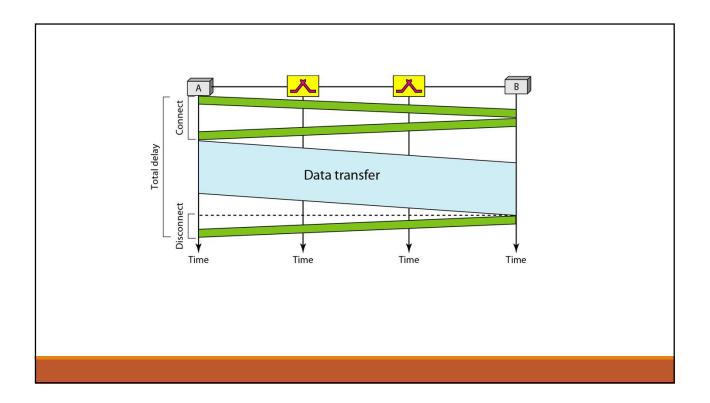


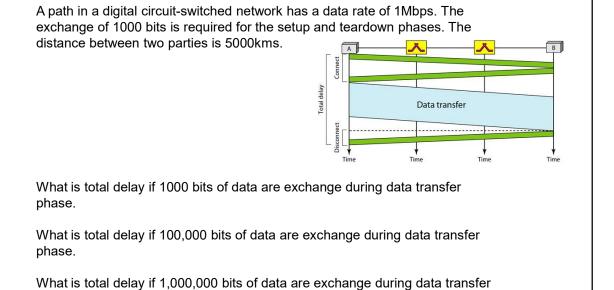
Comparison of Circuit Switching and Packet Switching

Parameter	Circuit Switching	Packet Switching
Routing scheme	Route selected during call setup	Each packet routed independently
Multiplexing scheme	Circuit multiplexing	Packet multiplexing shared media access networks
Addressing scheme	Hierarchical numbering plan	Hierarchical address space
Information representation	Analog voice or PCM coded voice	Binary information
End terminal	Telephone, modem	Computer
Transmission system	Analog and digital data over different transmission media	Digital data over different transmission media
Traffic	Real time interactive	Heavy traffic
Application	Telephone network for bi- directional, real time transfer of voice signals	Internet for datagram and reliable stream service between computers





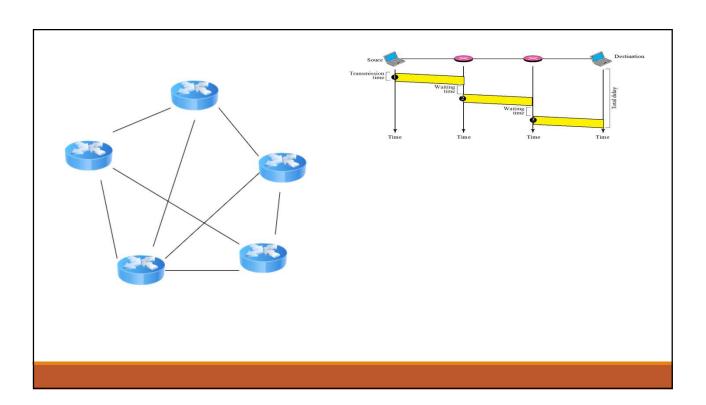




What is total delay if 50000 bits of data are exchange during data transfer

phase.

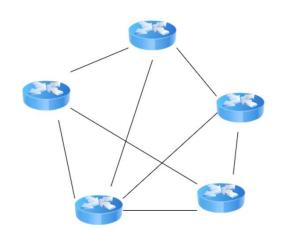
Transmission delay = Data unit size / BW Propagation delay= Distance / speed Waiting time=



Datag	ram Path Length	Visited Switches
1	3200Km	1,3,5
2	11,700 Km	1,2,5
3	12,200 Km	1,2,3,5
4	10,200 Km	1,4,5
5	10.700 Km	1.4.3.5

The delay for each switch (including waiting and processing) is 3,10, 20, 7, and 20 ms. Assuming that the propagation speed is 2 x 10^8 m,

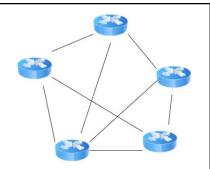
Find the order the datagram arrival



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Thank you