Final Assessment Test (FAT) - APRIL ATAX 2023

-1		OSLIFATI - APRIL	ATAX 2021	
Programme Course Trile	H Tech MICROPROCESSORS AND	Semester	Winter Semester 2022-23	
	MICROCOSTROLLERS	Course Code	BECE204L	
Faculty Nume	Prof. S Siyakumar	Slot	AI+TAI	
		Class Nisr	C112022235001100	
Lime	3 Hours	Max. Marks	100	
	Anana	A (4 X 5 Marks)		
a) Read To b) Store th	slock diagram for the following to components, and briefly describe the emperature from analog sensor tepes to temperature at external RAM local	take to be carried to block diagram, atedly after a speci- ation		1.
	the temperature value at a LCD dis- te function of the following purs of 8			[5
(b) MN 3 (c) NMI (d) REAL (c) RD)Y			
hits and st A000h. As	membly language program in 8086 i used in memory location 2000h and sume the result is not exceeding FFF	2002h respectively TH. *	Store the result in location	[5
formula f	ARM amenibly language program to mn : Iil 2, where n = 10	compute the sum of	of su numbers usual the	L
Diverse (Answer	(5 X 10 Marks) All questions		
6. Describe i	be architecture of Programmable Inte	(Val Timer (N254))	n detail with a neat aketch.	(1)
Describe the architecture of 8051 microcontroller with next a block diagram. Write an 8051 assembly language program to count the number of 1's and 0's in an 8-bit number.				[10
RI	oven in the memory location 4311. S	tore the number of	I's in R1 and number of 0's in	[1]
With a tic processus	ut diagram, discuss the ARM regist for the CPSR register values given in	ter set in detril in n Figure-1	id write the status of the ARM	110
1 0	1 0 1		1 1 0 1 0 0 1 1	
9		cure-1		
	values stored in the registers at	for construct the	Collinson in a 1234	
Appending M	6X00000020_R2=0X00000300_ (c.R2_LSL=)	R3-0X0000000)	R4-0X00000000	110
1 0 1	1 0 1	eure-1	1 1 0 1 0 0 1 1	

STADITION REASON RELIGIOUS

PART-C (2 X 15 Marks) Answer All questions

- [66] (ii) Write an 80% (assembly language program to general, a surface waygor from state period on pm P2/4. Use timer 0 in model. Assume the arestal frequency to 11 0502 MHz. C Marks).
 - (ii) Witheran 80(5) assembly language program to transfer [SLNSI] serially a) a vision band robe with 8 bit data. I stop bit and do this community. (8 Marks):
- SC The 8051 microcontroller is interfaced with 4x4 keypod and 1 CD as shown in Figure 2. Answer (15) the following:
 - (i) Write the configuration for PO, P1, P2 and P3, (3 marks)
 - (iii) Write the look-up table for the keypad information stored in the ROM location; starting from 400H+3 market
 - (iii) Write the steps how 8051 will alentify the key pressed? (3 marks)
 - (iv) Write the LCD initialize subroutine that can display the key pressed in 2nd line 3nd proofon.
 (3 marks)
 - (v) Write the LCD command subroutine and data subroutine to display the key pressed (3 marks).

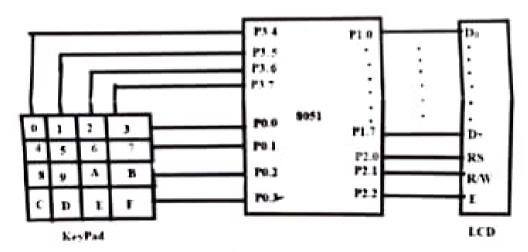


Figure-2