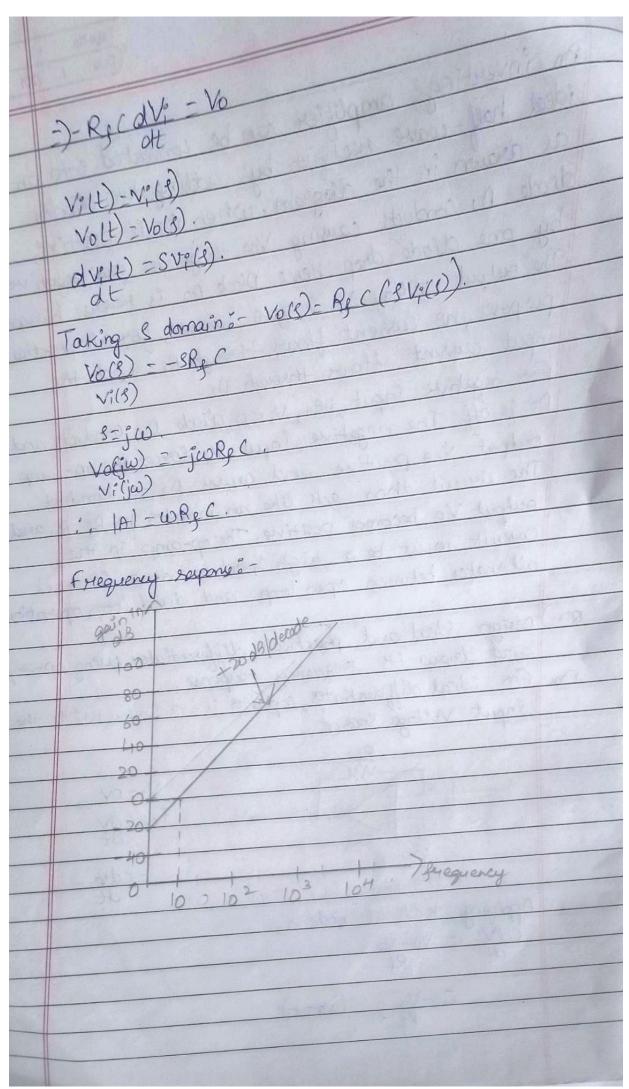
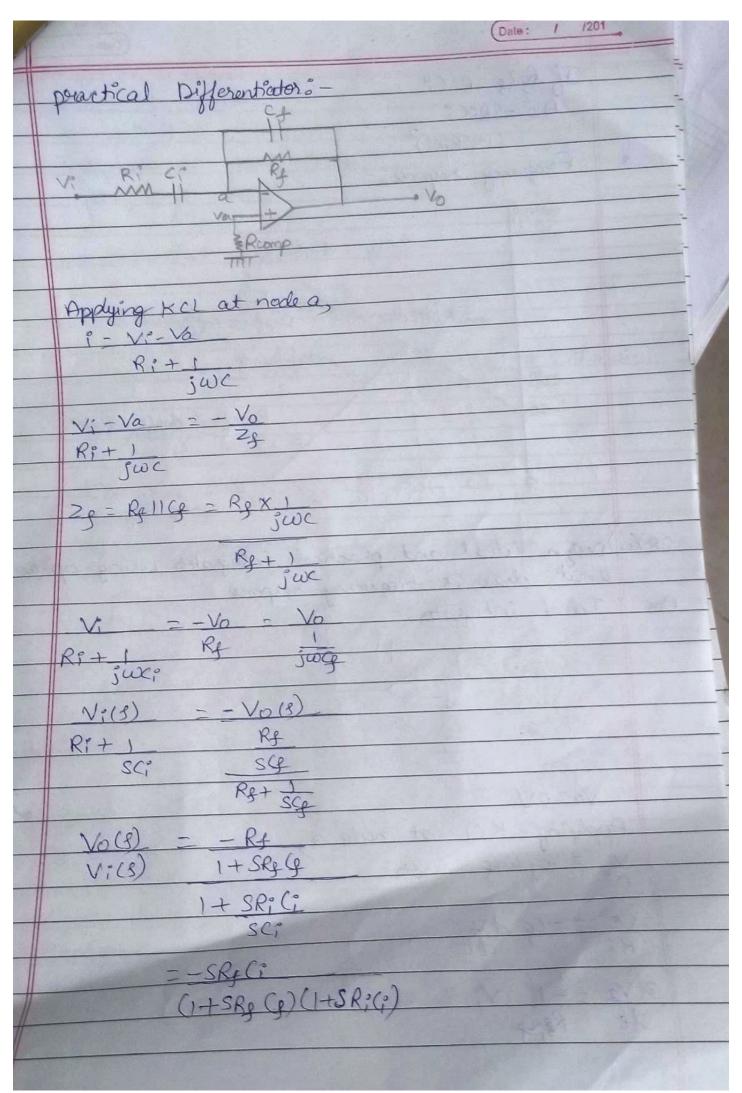


An inverting amplifier can be converted proto an ideal half-wave rectifier by adding two diodes as drawn in the diagram, when it is positive, diade DI conducts causing von to go to pos negative by one diode drop. Hence Diode Do is reverse biased. The output voltage Vo is zero, because, for all practical purposes, no consent flows through Rg and the input current flows through D, For negative input, i.e., V: <0 diade D, conducts and Di is off. The negative input V: forces the op- amp output VoA positive and causes Do to conduct The correct then acts like an invertor for Rg-R, and output Vo becomes positive. The op-amp in the Circuit must be a high speed op- amp since it alternates between open loop and doed loop operations 27.) Design ideal and practical differentiator using op-amp and draw its suguency response m. For ideal differentiates, capacites is in Series with input Voltage Source Va= OV Applying KCL at node a,





The Role - Role

D - SRici

LI+SRici)

Forequency serponse o 2 Tdool Differentator +2008/decade Prortical different aton 105 prequercy 102 103 104 28 % Design ideal and practical integrator using op-amp and draw it ferequency response. Ideal integrator of Va=OV Applying KCI at node a, Ri -- Cg d Vo dt Raig

