Mod -7 (10 m)

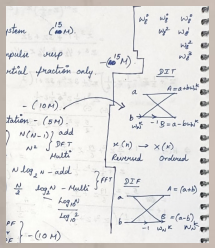
What is VLIW? Draw the architecture of VLIW.

What are the 2 types of processors & difference between them (5m) fixed Hosting point Compare

Mod 6 (15 m)

Direct form I or II (5m) Structure

Parallel / cascade (↓) (10 m)



Mod 5 (15 m)

IIR → Butterworth < LPF / HPF (10 m)

→ Bilinear transform (5 m) HC(s) → HC(z)

$\frac{2}{T}\left[\frac{z-1}{z+1}\right]$

Mod 4 (15 m)

FIR LPF / HPF → Hamm / Hann window (15 m) given data, value of M/n (Integ) Symm prop - L hopital HC(ej0) HC(n)

Mod 3 (15 m)

DFT / DIF — FFT (10 m) no sin/cos (0 to 0)

Computation (5 m) (complex no addition & multiplication)

Mod 2 (15 m)

Finding ROC, stability, h(n), impulse response H(z) → H(n) of system

Inv z-transform (partial fraction only)

Mod 1

Types of signals & system (Linear / non, periodic / variant / inv (10 m)

Linear & circular conv. (5 m)

1) rev, shift, scaling
even/odd, power/energy, periodic/non periodic
linear, causal, stable, time variant,
static & dynamic
convolution

2) ROC (causal, stability), inv-Z
(H(z) → H(n))

3) DIFT - mag, phase (H(jω))

4) ideal condition for filter H(n) - windowing
linear phase signal

5) IIR - mag response, normalized mag response, order,
poles, n(s), digital - bilinear

6) FIR - novel
FIR - all