	L-c circuits
	consider an electric circuit containing an inductance
par.	L'and a capacitance C: II a is the charge the
A college	capacitor peate and i the current in the circuit
	at any time t, then
	(i) the voltage drops across the inductance
	= L di = m L dq, : i = dq
	$= \frac{1}{dt} = \frac{1}{dt$
	(ii) the voltage drop due to capacitance = 9
	considera a minima is and a the they
	since There is no applied emf is the drawit, we have
	The Ld ² g + 9 =0 side makes another
	at a
	$\frac{1}{2} \frac{d^2q}{dt^2} + \frac{1}{2} \frac{1}{2} = 0 \text{Taking } \frac{1}{Lc} = \omega^2$
	at² L
	=) dq+wq=0 come (Similar to eany
	dt ² Free oscillations)
	L-c with an emf 12 partition of
a e	
	In the previous case, along with inductance L and
	capacilance c an emf Eo cosnt is also applied
	to the circuit, the sun of voltage drops is the circuit
	equals to an applied emf.
	L'd2q + 2 = Eo cosnt
The second	dt ² Colla
	La residentiales



