

# SPR5801S1

# Firmware Release

# Notes

---

LG only

---

Version 0x10



# Contents

<b>About this Document</b>	<b>3</b>
<b>1. Overview</b>	<b>4</b>
<b>2. Release Notes for SPR5801S1 Firmware</b>	<b>5</b>
<b>3. FW Branch History</b>	<b>8</b>
<b>4. Issues Fixed in Rev 0x10</b>	<b>9</b>



## About this Document

This document contains information that is proprietary and confidential to Gyrfalcon Technology Inc. and is intended for the specific use of the recipient, for the purpose of evaluating or using Gyrfalcon Technology's products and/or IP. This document is provided to the recipient with the expressed understanding that the recipient will not divulge its contents to other parties or otherwise misappropriate the information contained herein.

U.S. Patents Pending. The Gyrfalcon Technology logo is a registered trademark of Gyrfalcon Technology Inc.

Copyright (c) 2018 Gyrfalcon Technology Inc. All rights reserved. All information is subject to change without notice.

### Contact Gyrfalcon

Silicon Valley HQ  
Gyrfalcon Technology, Inc.  
1900 McCarthy Boulevard, Suite 208  
Milpitas, CA 95035 USA  
[www.gyrfalcontech.ai](http://www.gyrfalcontech.ai)



# 1. Overview

The GTI Lightspeed® chip series consists of low-power and high-performance AI accelerator chips that leverage model compression and quantization techniques. SPR5801S1 chip series is equipped with native USB interface.

## 2. Release Notes for SPR5801S1 Firmware

SPR5801S1 firmware is released as part of SDK package under GTISDK/Firmware folder. The Firmware release includes I2C code download and USB connection and control transfer, EP in and EP out.

Version	Notes	Date
0x01	Initial release.	02/26/2019
0x02	Modified the data in and data out functions. <ul style="list-style-type: none"> <li>Fixed the USB connection issue which was caused by improper flag of USB configuration.</li> <li>Fixed issue which does not respond data transfer CMD properly.</li> <li>Fixed the data in and out EP not starting issue. The EP could not start because it was not set correctly.</li> <li>Further simplified data transfer handler to reduce turnaround time. To finish the transfer preparation partially before receiving event, hence, save some time after receiving the event.</li> </ul>	03/06/2019
0x03	Improved the data transfer performance. <ul style="list-style-type: none"> <li>Increased the transfer data block size when guaranteeing stability. To prepare more space for the transfer to simplify the process.</li> <li>Assign higher priority for data transfer. The data transfer is processed as soon as possible.</li> </ul>	04/03/2019
0x04	Optimized the power management functions. <ul style="list-style-type: none"> <li>Implemented the power management commands. The API can use the command to enter low power mode.</li> <li>Fine tuned the timing for entrance and exit of low power states to guarantee system stability.</li> </ul>	04/04/2019
0x05	Optimized firmware size to speed up the I2C loading. <ul style="list-style-type: none"> <li>Reduced the code size for current FW implementation. Modify the code such that the bin file to be small.</li> </ul>	04/06/2019

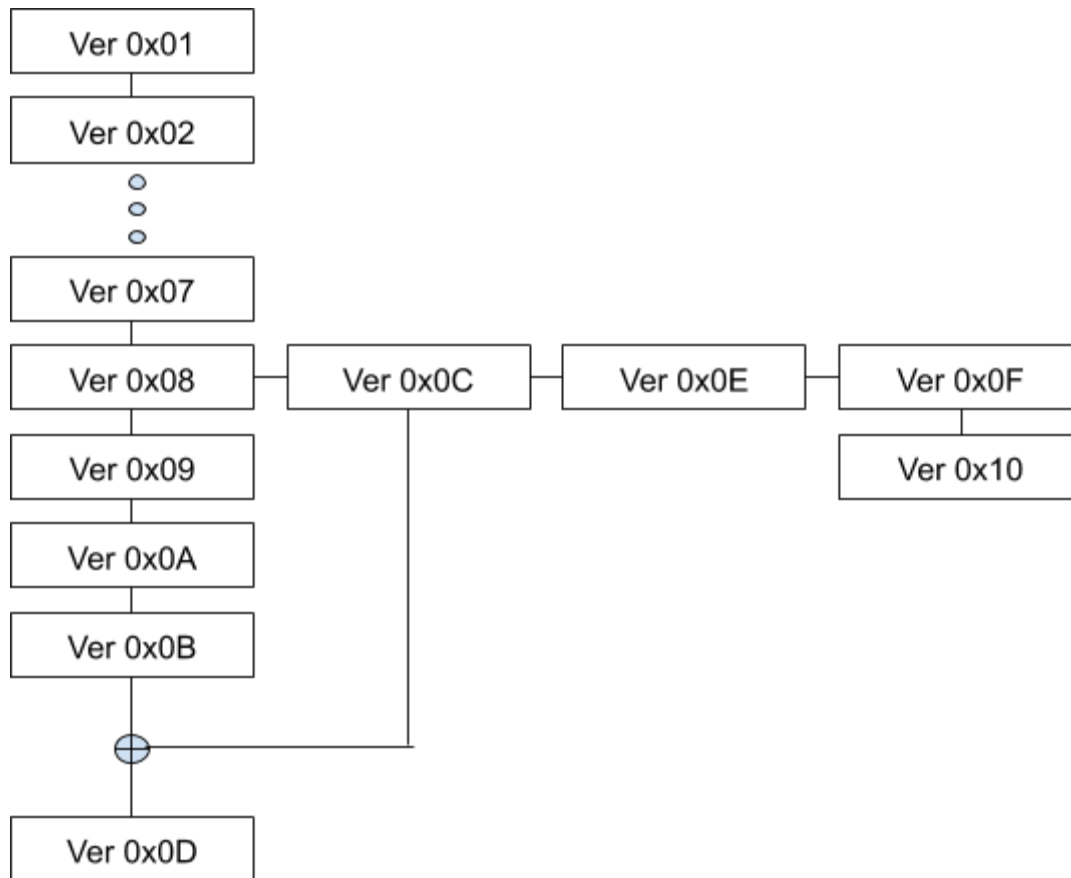


0x06	<p>Fixed I2C 100kHz read firmware version issue.</p> <ul style="list-style-type: none"> <li>• Previous release (rev 05) reduced code size and commented out the code to handle the STOP of the command 08 00 00 00. This fix just added the code back.</li> </ul>	04/12/2019
0x07	<p>Fixed the learning mode hang issue.</p> <ul style="list-style-type: none"> <li>• To stop the DMA properly, the firmware added code to make the next memory invalid DMA descriptor. The DMA will stop when hitting the invalid descriptor.</li> </ul>	04/16/2019
0x08	<p>Fixed the issue that I2C stopped working after code download and demo execution.</p> <ul style="list-style-type: none"> <li>• The code was modified such that the executing I2C related functions won't interfere with other functions.</li> </ul>	04/21/2019
0x09	To improve the data transfer by increasing the data size.	04/29/2019
0x0A	To increase the DMA transfer block size	05/14/2019
0x0B	To clean up code.	06/12/2019
0x0C	<ul style="list-style-type: none"> <li>• Added the CRC calculation function for the downloaded code.</li> <li>• Added zero stuffing to have no impact in case code overwrite happen due to race condition in handling the STOP ISR.</li> <li>• This version of Firmware was built based on revision 0x08.</li> </ul>	06/14/2019
0x0D	<ul style="list-style-type: none"> <li>• Added the CRC calculation function for the downloaded code (same change from 0x0C).</li> <li>• Added zero stuffing to have no impact in case code overwrite happen due to race condition in handling the STOP ISR (same change from 0x0C).</li> <li>• Re-use the ROM code to reduce the downloaded code size. The FW reset SPR5801S1 when it receives warm reset or USB2 connection request.</li> <li>• This version of Firmware was built based on revision 0x0B.</li> </ul>	06/26/2019

0x0E	<ul style="list-style-type: none"><li>• The FW reset SPR5801S1 when it receives warm reset or USB2 connection request.</li><li>• This version of Firmware was built based on revision 0x0C.</li></ul>	07/02/2019
0x0F	<ul style="list-style-type: none"><li>• Fixed USB enumeration issue by initializing the memory properly.</li><li>• This version of Firmware was built based on revision 0x0E.</li></ul>	07/15/2019
0x10	<ul style="list-style-type: none"><li>• Fixed DMA Bulk transfer issue</li><li>• This version of Firmware was built based on revision 0x0F.</li></ul>	07/17/2019



### 3. FW Branch History





## 4. Issues Fixed in Rev 0x10

**Problem Description :**

The DMA process of bulk transfer can be hung in case incorrect TRB is assigned.

**Analysis :**

Incorrect TRB can be assigned if the size of the requested data from host is too small for FW to have enough time for DMA event processing generated after DMA handling the data.

**Fix :**

FW ensures the TRB is assigned correctly in this case.

**Side Effects:**

No side effects are expected.