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LE12.2

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LE12.2.1 Compiling a Procedure

3/4 points (ungraded)

You are given the following listing of a C program and its translation to Beta assembly code:

```
int ones(int x) {
    int lowbit = x & 1;    // low bit of x
    int rest = x >> 1;     // shift other bits right
    if (x == 0) return 0;
    else return ones(rest)+lowbit
}
```

```
[01]  ones:  PUSH(LP)
[02]          PUSH(BP)
[03]          MOVE(SP, BP)
[04]          ALLOCATE(2)
[05]          PUSH(R1)
[06]          LD(BP, -12, R0)
[07]          ANDC(R0, 1, R1)
[08]          ST(R1, 0, BP)
[09]          SHRC(R0, 1, R1)
[10]          ST(R1, 4, BP)
[11]          BEQ(R0, lab1)
[12]          LD(BP, 4, R1)
[13]          PUSH(R1)
[14]          BR(ones, LP)
[15]          DEALLOCATE(1)
[16]          LD(BP, 0, R1)
[17]          ADD(R0, R1, R0)
[18]  lab1:  POP(R1)
[19]          MOVE(BP, SP)
[20]          POP(BP)
[21]          POP(LP)
[22]          JMP(LP)
```

(A) Which line(s) of assembly language form the entry sequence? Include code responsible for allocating local variables and saving the values of registers used in the body of the procedure. Check all the applicable lines.

☒ [01] ones:PUSH(LP)

☒ [02] PUSH(BP)

☒ [03] MOVE(SP, BP)

☒ [04] ALLOCATE(2)

☒ [05] PUSH(R1)

☐ [06] LD(BP, -12, R0)

☐ [07] ANDC(R0, 1, R1)

☐ [08] ST(R1, 0, BP)

☐ [09] SHRC(R0, 1, R1)

☐ [10] ST(R1, 4, BP)

☐ [11] BEQ(R0, lab1)

☐ [12] LD(BP, 4, R1)

☐ [13] PUSH(R1)

☐ [14] BR(ones, LP)☐ [15] DEALLOCATE(1)☐ [16] LD(BP, 0, R1)☐ [17] ADD(R0, R1, R0)☐ [18] lab1:POP(R1)☐ [19] MOVE(BP, SP)☐ [20] POP(BP)☐ [21] POP(LP)☐ [22] JMP(LP)

(B) Which line(s) of assembly language form the exit sequence? Include code responsible restoring values saved by the entry sequence. Check all the applicable lines.

☐ [01] ones:PUSH(LP)☐ [02] PUSH(BP)☐ [03] MOVE(SP, BP)☐ [04] ALLOCATE(2)☐ [05] PUSH(R1)☐ [06] LD(BP, -12, R0)☐ [07] ANDC(R0, 1, R1)☐ [08] ST(R1, 0, BP)☐ [09] SHRC(R0, 1, R1)☐ [10] ST(R1, 4, BP)☐ [11] BEQ(R0, lab1)☐ [12] LD(BP, 4, R1)☐ [13] PUSH(R1)☐ [14] BR(ones, LP)☐ [15] DEALLOCATE(1)☐ [16] LD(BP, 0, R1)☐ [17] ADD(R0, R1, R0)

☒ [18] lab1:POP(R1)☒ [19] MOVE(BP, SP)☒ [20] POP(BP)☒ [21] POP(LP)☒ [22] JMP(LP)

(C) Which line(s) of assembly language were generated when compiling the recursive call ones(rest). Check all the applicable lines.

☒ [01] ones:PUSH(LP)☒ [02] PUSH(BP)☒ [03] MOVE(SP, BP)☒ [04] ALLOCATE(2)☒ [05] PUSH(R1)☒ [06] LD(BP, -12, R0)☒ [07] ANDC(R0, 1, R1)☒ [08] ST(R1, 0, BP)☒ [09] SHRC(R0, 1, R1)☒ [10] ST(R1, 4, BP)☒ [11] BEQ(R0, lab1)☒ [12] LD(BP, 4, R1)☒ [13] PUSH(R1)☒ [14] BR(ones, LP)☒ [15] DEALLOCATE(1)☒ [16] LD(BP, 0, R1)☒ [17] ADD(R0, R1, R0)☒ [18] lab1:POP(R1)☒ [19] MOVE(BP, SP)☒ [20] POP(BP)

☒ [21] POP(LP)

☒ [22] JMP(LP)



(D) Which line(s) of assembly language were generated when compiling the statement "lowbit = x & 1;" Check all the applicable lines.

☐ [01] ones:PUSH(LP)

☐ [02] PUSH(BP)

☐ [03] MOVE(SP, BP)

☐ [04] ALLOCATE(2)

☐ [05] PUSH(R1)

☒ [06] LD(BP, -12, R0)

☒ [07] ANDC(R0, 1, R1)

☒ [08] ST(R1, 0, BP)

☐ [09] SHRC(R0, 1, R1)

☐ [10] ST(R1, 4, BP)

☐ [11] BEQ(R0, lab1)

☐ [12] LD(BP, 4, R1)

☐ [13] PUSH(R1)

☐ [14] BR(ones, LP)

☐ [15] DEALLOCATE(1)

☐ [16] LD(BP, 0, R1)

☐ [17] ADD(R0, R1, R0)

☐ [18] lab1:POP(R1)

☐ [19] MOVE(BP, SP)

☐ [20] POP(BP)


☐ [21] POP(LP)

☐ [22] JMP(LP)



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