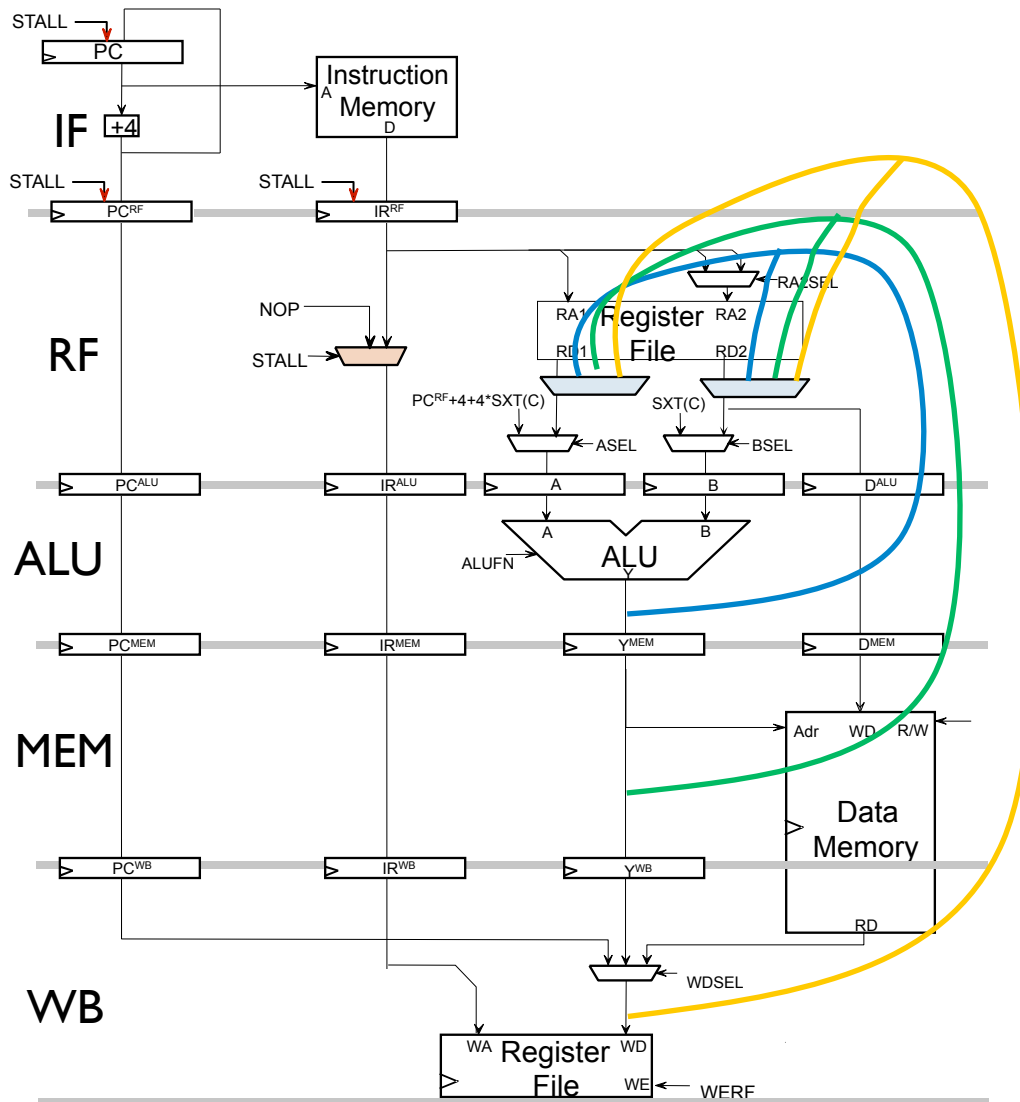


Bypass Logic



- Add bypass muxes to RF outputs
- Route ALU, MEM, WB outputs to mux inputs
- Bypass value if destination register of instruction in ALU, MEM, or WB matches source register of instruction in RF
 - But not R31!?
- What to do if multiple matches?
 - Select value from most recent instruction! (ALU > MEM > WB)