Tutorial : Leniency

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Leniency

1/1 point (ungraded)

In lecture we saw that a 2-input NOR gate might obey the static discipline (i.e., be a valid combinational device) without being a lenient combinational device. Is it possible for an inverter to be a valid combinational device without being lenient?

- ○ Yes
- ◉ No
- ○ Can't Tell

✔

Explanation

A device is lenient if it will generate a glitch-free output using a minimal number of required inputs. A 1-input device such as an inverter depends on only 1 input, and nothing else, so therefore it is always lenient.

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ⓘ Answers are displayed within the problem

Leniency

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A 2-input AND gate is made from a lenient CMOS 2-input NAND gate followed by a lenient CMOS inverter. Is the AND gate necessarily lenient?

- ○ Yes
- ○ No
- ○ Can't Tell

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Leniency

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If an inverter conforms to our definition of a combinational device, is it necessarily lenient?

- ○ Yes
- ○ No

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Why the inverter is lenient ?

Calculator

	Why in the following questions we are considering inverter to be lenient? Usually in lenient devices we ignore one or other inputs re...	
	Leniency tutorial - Problem 1 "Is it possible for an inverter to be a valid combinational device without being lenient?" Each inverter is always lenient. The currently...	6
	The Number of Inputs not specified. In CMOS Tutorial, the last problem "If an inverter conforms to our definition of a combinational device, is it necessarily lenient?" Bec...	2

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