ODROID-C1 pin map, v1.2

Wiring	Export	ODROID-C1 pin		Label	Header		Label	ODROID-C1 pin		Export	Wiring
–	–		–	3V3	1	2	5V0	–		–	–
8	74		I2CA_SDA	SDA1	3	4	5V0	–		–	–
9	75		I2CA_SCL	SCL1	5	6	GND	–		–	–
7	83	default w1	GPIOY.BIT3	#83	7	8	TXD1	TXD_B		113	–
–	–		–	GND	9	10	RXD1	RXD_B		114	–
0	88		GPIOY.BIT8	#88	11	12	#87	GPIOY.BIT7		87	1
2	116		GPIOX.BIT19	#116	13	14	GND	–		–	–
3	115		GPIOX.BIT18	#115	15	16	#104	GPIOX.BIT7		104	4
–	–		–	3V3	17	18	#102	GPIOX.BIT5		102	5
12	107	MOSI,PWM1	GPIOX.BIT10	MOSI	19	20	GND	–		–	–
13	106	MISO	GPIOX.BIT9	MISO	21	22	#103	GPIOX.BIT6		103	6
14	105	SCLK	GPIOX.BIT8	SCLK	23	24	CE0	GPIOX.BIT20 CE0		117	10
–	–		–	GND	25	26	#118	GPIOX.BIT21		118	11
–	76		I2CB_SDA	SDA2	27	28	SCL2	I2CB_SCL		77	–
21	101		GPIOX.BIT4	#101	29	30	GND	–		–	–
22	100		GPIOX.BIT3	#100	31	32	#99	GPIOX.BIT2		99	26
23	108	PWM0	GPIOX.BIT11	#108	33	34	GND	–		–	–
24	97		GPIOX.BIT0	#97	35	36	#98	GPIOX.BIT1		98	27
–	–		ADC.AIN1	AIN1	37	38	1V8	1V8		–	–
–	–		–	GND	39	40	AIN0	ADC.AIN0		–	–