

VIVEK KAR

B.Tech. | NIT Rourkela

Third Year, Electronics & Instrumentation Engg.
DOB: 08 August 2003
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Education

2021-PRESENT

B.TECH., EIE

NIT Rourkela

CGPA :8.82/10

MAY 2021

INTERMEDIATE

DAV Chandrasekharpur , Bhubaneshwar

Percentage: 95.6%

MAY 2019

MATRICULATION

DPS, Rourkela

Percentage: 96.2%

Links

Github:// [vivekkar88](#)

LinkedIn:// [vivek kar](#)

Skills

HARDWARE LANGUAGE :

Verilog HDL

GENERAL PROGRAMMING :

C

HARDWARE :

FPGA (BASYS 3)

SOFTWARES :

Multisim

LANGUAGES :

English, Hindi, Odia

Relevant Courses

Digital Electronics

Verilog

Microprocessor 8085

Static timing analysis (STA)

Work Experience/Projects

JUNE 2023

FPGA Implementation

Hardware Implementation

Topic: FIFO

The aim of the project was to implement and simulate the working of a synchronous FIFO . This project includes learning on how to implement any hardware on FPGA using Xilinx Vivado

.....**Know More**

JUNE 2023

Vending Machine

Simulation Project

Topic: Verilog Project

Implemented Virtual simulation of a vending machine using verilog language. Designed a vending machine with product worth 15 units and denomination 5,10 available. ...**Know More**

JUNE 2023

Multisim

Simulation Project

Topic: Digital Security Management

A system that assures the security and protection of a room or other enclosed space is what the room security project in the field of digital electronics wants to build.....**Know More**

Achievements/Certifications

2021

Academic Excellence

Received

Received award from the management of DAV public school for being in the brand of students to secure 95 plus in intermediate

Extra Curricular Activities

2023-NOW

Rotaract

Currently an active member of the official social service club of NIT-R and indulge in social activities regularly

2021-2022

Co-Ordinator, Publicity Team

Nirutsav

Was in the publicity team of NIT R cultural fest Nirutsav 2021. Our job was the promotion of events on both offline and online platforms.