RandBitGen:1 fde fdr and2 Mxor_shift_reg_F[4]_shift_reg_F[1]_XOR_3_o1 Mmux_shift_reg_F[4]_seed[4]_mux_1_OUT1 data_out and3b1 and2b2 or2b1 fd inv _n0048_inv1 _n00481 rst_frame_in_d2_OR_5_o1 dclk_fall_frame_in_AND_5_o1 dclk_in_d2 fd fd dclk_in_d1 frame_in_d2 fd

RandBitGen

frame_in_d1