

- Test Bench
- ManEncoder
  - rst
  - clk
  - nrz\_in
  - manout
  - manclk
  - manclk\_sync
  - en\_sync\_reg
  - manchester\_timer[4:0]
  - sampling\_ctr[4:0]
    - [4]
    - [3]
    - [2]
    - [1]
    - [0]
  - num\_bits\_sampled[3:0]
  - nrz\_hold\_reg
  - nrz\_sync\_reg
  - state
    - st...
    - next\_state
  - processing\_frame
  - en\_sampling\_ctr
  - clr\_sampling\_ctr
  - en\_num\_bits
  - clr\_num\_bits
  - en\_hold\_reg
  - set\_hold\_reg
  - set\_sync\_reg
  - sync\_nrz[4:0]
- ManDecoder

