

- Test Bench
  - clk
  - rst
  - rx\_d\_a
  - rx\_d\_b
  - xmt\_a
  - sw[7:0]
  - tx\_d\_a
  - tx\_d\_b
  - led[7:0]
  - tx\_d\_a\_nrz
  - rx\_d\_a\_nrz
  - clk\_period
- ManEncDec\_A
  - rst
  - clk
  - nrz\_in
  - manout
  - manin
  - nrz\_out
  - clkdiv[8:0]
  - rclk
  - bauddivide[8:0]
- ManEncDec\_B
  - rst
  - clk
  - nrz\_in
  - manout
  - manin
  - nrz\_out
  - clkdiv[8:0]
  - rclk
  - bauddivide[8:0]

