

# **Simulink Design Verifier Report**

**Torch\_Bridge\_Updated**

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## **Simulink Design Verifier Report: Torch\_Bridge\_Updated**

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# Chapter 1. Summary

## Analysis Information.

Model:	Torch_Bridge_Updated
Mode:	Property proving
Model Representation:	Built on 16-Oct-2020 00:57:29
Status:	Completed normally
PreProcessing Time:	6s
Analysis Time:	20s

## Objectives Status.

Number of Objectives:	1
Objectives Falsified with Counterexamples:	1

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# Chapter 2. Analysis Information

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## Model Information

File:	Torch_Bridge_Updated
Version:	1.21
Time Stamp:	Fri Oct 16 00:54:43 2020
Author:	vivek vivian

## Analysis Options

Mode:	PropertyProving
Rebuild Model Representation:	IfChangeIsDetected
Proving Strategy:	Prove
Proof Assumptions:	UseLocalSettings
Test Objectives:	UseLocalSettings
Assertions:	UseLocalSettings
Maximum Analysis Time:	300s
Block Replacement:	off
Parameters Analysis:	off
Include expected output values:	off
Randomize data that do not affect the outcome:	off
Additional analysis to reduce instances of rational approximation:	on
Save Data:	on
Save Harness:	off
Save Report:	off

## Constraints

### Analysis Constraints

Name	Analysis Constraint
criteria	T

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## Analysis Information

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<b>Name</b>	<b>Analysis Constraint</b>
criteria	T
criteria	T

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# Chapter 3. Proof Objectives Status

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## Objectives Falsified with Counterexamples

#	Type	Model Item	Description	Analysis Time (sec)	Counterexample
1	Proof objective	Verification Subsystem/Proof Objective	Objective: T	19	1 [5]

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# Chapter 4. Properties

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## Verification Subsystem/Proof Objective

### Summary.

Model Item: Verification Subsystem/Proof Objective

Property: Objective: T

Status: Falsified

### Counterexample.

Time	0	1	2	3	4
Step	1	2	3	4	5
In_One	1	0	0	1	1
In_Two	1	1	0	0	1
In_Five	0	0	1	0	0
In_Ten	0	0	1	0	0