

Assignment 1  
Vivek Pandya (vpandya)

**Problem 1**

Part 1:

Property  $p ::=$

- $\varepsilon$
- $| \quad n = \text{val} \mid n \neq \text{val} \mid n < \text{val} \mid n > \text{val}$
- $| \quad s = \text{str} \mid s \neq \text{str}$
- $| \quad p1 \vee p2$
- $| \quad p1 \wedge p2$

$\text{bool} ::= \text{true} \mid \text{false}$

Schema  $\tau ::=$

- $\text{number}\langle p \rangle$
- $| \quad \text{string}\langle p \rangle$
- $| \quad \text{bool}$
- $| \quad [\tau^{k\langle p \rangle}]$
- $| \quad \{(\text{string}\langle p \rangle : \tau)^{k\langle p \rangle}\}$

Part 2:

$$\begin{array}{c}
\frac{}{\text{false} \sim \text{bool}} \text{ (S-BOOL-FALSE)} \qquad \frac{}{\text{true} \sim \text{bool}} \text{ (S-BOOL-TRUE)} \\
\\
\frac{}{n \sim \text{number}} \text{ (S-NUM)} \qquad \frac{\text{seq is \textbackslash n terminated sequence of characters}}{seq \sim \text{string}} \text{ (S-STRING)} \\
\\
\frac{n \sim \text{properties}}{n \sim \tau} \text{ (S-NUM-PROPERTY)} \qquad \frac{seq \sim \text{properties}}{seq \sim \tau} \text{ (S-STRING-PROPERTY)} \\
\\
\frac{}{\{\} \sim \tau} \text{ (S-EMPTY-OBJECT)} \qquad \frac{}{[] \sim \tau} \text{ (S-EMPTY-ARRAY)} \\
\\
\frac{k \sim \text{properties}}{\{(s\langle p \rangle : \tau)^k\} \sim \{s_1\langle p \rangle : \tau, \dots, s_k\langle p \rangle : \tau\}} \text{ (S-CARDINALITY-OBJECT)} \\
\\
\frac{k \sim \text{properties}}{[\tau^k] \sim [\tau_1, \dots, \tau_k]} \text{ (S-CARDINALITY-ARRAY)} \\
\\
\frac{\text{all the elements of array have same type}}{[j_1, \dots, j_n] \sim \tau} \text{ (S-ARRAY)} \\
\\
\frac{\forall s \in \{s_1, \dots, s_n\} \ s \sim \text{properties}}{\{s_1\langle p \rangle : \tau, \dots, s_k\langle p \rangle : \tau\} \sim \tau} \text{ (S-OBJECT)}
\end{array}$$

## Problem 2

Part 1:

$$\begin{array}{c}
\frac{}{(\varepsilon a, j) \mapsto (a : j)} \text{ (EPSILON-ACCESSOR)} \qquad \frac{s \sim \text{string}}{(.sa, \{s : j\}) \mapsto (a : j)} \text{ (KEY-ACCESSOR)} \\
\\
\frac{0 \leq k \leq n}{([k]a, [j_0, j_1, j_2, \dots, j_k, \dots, j_n]) \mapsto (a, j_k)} \text{ (ARRAY-ACCESSOR)} \\
\\
\frac{}{(|a, [j_1, \dots, j_k, \dots, j_n]) \mapsto ([ (a, j_1), \dots, (a, j_k), \dots (a, j_n) ])} \text{ (MAPS-ACCESSOR)}
\end{array}$$

Part 2:

$$\begin{array}{c}
\frac{}{\varepsilon \sim \tau} \text{ (VALID-EPSILON-ACCESSOR)} \qquad \frac{\{(s : j)\} = \tau \quad s \sim \text{string}}{.s \sim \tau} \text{ (VALID-KEY-ACCESSOR)} \\
\\
\frac{[j] = \tau \quad n \leq \text{size of array}}{[n] \sim \tau} \text{ (VALID-ARRAY-ACCESSOR)} \qquad \frac{[j] = \tau}{| \sim \tau} \text{ (VALID-MAPS-ACCESSOR)}
\end{array}$$

*Accessor safety*: for all  $a, j, \tau$ , if  $a \sim \tau$  and  $j \sim \tau$ , then there exists a  $j'$  such that  $(a, j) \mapsto^* \varepsilon, j'$ .

*Proof.* • Induction hypothesis: Let  $A = a_1 a_2 \dots a_n$  is series of accessor for which **Accessor safety** holds true.

$$(A, j) \mapsto^* (\varepsilon, j')$$

- Let say  $(.sA, j_0) \mapsto (A, j)$  then by inversion lemma of VALID-KEY-ACCESSOR  $j_0 \sim \{(s : j)\}$  and due to induction hypothesis following holds true  $(.sA, j_0) \mapsto^* (\varepsilon, j')$  thus accessor safety holds true.
- Let say  $([n]A, j_0) \mapsto (A, j)$  then by inversion lemma of VALID-ARRAY-ACCESSOR  $j_0 \sim [j]$  and due to induction hypothesis following holds true  $([n]A, j_0) \mapsto^* (\varepsilon, j')$  thus accessor safety holds true.
- Let say  $(|A, j_0) \mapsto (A, j)$  then by inversion lemma of VALID-MAPS-ACCESSOR  $j_0 \sim [j]$  and due to induction hypothesis holds true for all values in  $[j]$ , following holds true  $(|A, j_0) \mapsto^* (\varepsilon, j')$  thus accessor safety holds true.

Thus with structural induction we have proved that if **Accessor safety** holds on sub-part then it holds for valid composition of accessor sequence.

□