Vivian Zeru
Willing to Relocate | 502-415-1280 | vivianzeruportfolio.vercel.app | vivian.zeru@vanderbilt.edu | linkedin.com/in/vivian-zeru/

Education

MS in Electrical & Computer Engineering | *Vanderbilt University* *Expected May 2027*
- Accelerated Graduate Program in Engineering: Receiving MS/BE degrees in 4 years concurrently (high academic standing)
- Coursework: Advanced Digital Electronics (Transistor-Level Digital Circuit Design), VLSI Design (Cadence, AWS), Electronics 2 (Analog Circuits)

BE in Electrical & Computer Engineering | *Vanderbilt University (GPA: 3.64)* *Expected May 2027*
- Cornelius Vanderbilt Scholar: Awarded to <1% of applicants for high academic/community achievement
- Coursework: Microelectronic Systems, Microcontrollers (AVR), Electronics 1, Electromagnetics, Analog Circuits/Systems, Digital Systems (RISC-V), Rapid Prototyping (Arduino, Fusion 360 CAD)

Technical Skills

Hardware Design/Simulation : Cadence (with Amazon WorkSpaces) for VLSI Design, Eagle, Altium, SPICE (LTSpice), Verilog, Intel Quartus/ModelSim, Fusion 360

Hardware Debugging/Assembly : Oscilloscopes, Multimeters, Function Generators, DC Power Supplies, SMD Soldering (QFN/WLCSP), Soldering Iron, PCBA

Embedded Programming : Arduino (C++), Python (Raspberry Pi) STM32 (C), Assembly (AVR, RISC-V), Git

Hardware Experience

Undergraduate Research Assistant: Hardware Team | *SYMBIO-X Lab* *Feb 2025 –Present*
- Designing novel mixed-signal eye-tracking PCB in Eagle for medical applications (autonony, etc.).
- Executed precise SMD soldering techniques on QFN/WLCSP microscopic components for high-reliability assembly (PCBA) on 2+ wearable hardware sensors (applications in ECG, EEG, NIRS, heart, and hydration health monitoring).
- Debugged and validated low-power wearable PCB with multimeters and firmware flashing, enabling functional biosensor.

Electronics Engineer for Stormwater Runoff Device | *Engineers Without Borders* *Sep 2023 –Present*
- Designed 2-layer PCB (EasyEDA) with ESP8266 & sensors to decrease space & fit mechanical-design constraints for Solidworks-designed 3D-Printed box; debugged to improve layout signal integrity.
- Decreased microcontroller power consumption by ***99.9%*** (150 mA to 20 μA) using deep sleep mode every 15 seconds.

Hardware Projects

Standard VLSI Cell Design | *Cadence Virtuoso, AWS, VLSI Design, Transistor-Level Design* *Fall 2025*
- Designing a 45 nm node standard cell library (Inverter, NAND, NOR gates) at the transistor level, optimizing cell layout for area/performance with a 10-track height design (Homework 1 in VLSI Design Course).
- Performing standard DRC and LVS checks for design integrity and manufacturability.

Custom ESP32 Environmental PCB Sensor | *Eagle, System-Design, PCB Design, I2C, C++* *Summer 2025*
- Developed 2-layer PCB (schematic+layout) in Eagle to detect temperature, humidity, pressure, altitude: BME280.
- Integrated USB-C power delivery system with low-noise LDO regulator & 40 kHz crystal oscillator for stable operation.
- Achieved 25% size reduction with ICs; manually routed layout to ensure signal integrity for manufacture-ready device.

4-Bit ALU (Arithmetic Logic Unit) | *Verilog, Synopsys VCS, Digital Systems* *July 2025*
- Implemented 2 scalable design methodologies in Verilog: behavorial (cases) & structural (gate-level hardware).
- Developed comprehensive testbench for ***100% design verification,*** Synopsys VCS simulating 6.74% decrease in CPU runtime (structural).

Leadership and Professional Experience

Lab Proctor and Founding Member | *ECE Tech Crew* *Feb 2024 – Present*
- Trained 9 students in Keysight tools for debugging circuits in senior design & research (oscilloscopes, multimeters).
- Drove membership from 7 to 30+ with merchandise, flyers, signs; communicating on Slack/email with students & faculty.
- Managed ECE Makerspace 2-3 hours weeky to ensure the safety/quality of components and help students with electronics projects.
- Chosen to present to the ECE External Advisory Board (***10+ faculty***) at Vanderbilt on behalf of the organization.

2025 Vice President and Fall 2024 Secretary | *Engineers Without Borders* *Oct 2024 –Present*
- Manage internal/external logistics and communicate to 10+ members via weekly emails & 8+ Instagram posts.

Information Services Management (ISM) Intern | *UPS* *Jun 2024 – Aug 2024*
- Developed/deployed scheduling feature for nationwide aircraft maintenance employee application in production for 2285 monthly users; selected to present to executive leadership for clarity/technical depth.
- Created real-time asset tracking app in 24-hour hackathon, enabling $33M savings; selected top 9 of 53 teams.