



## W11 Mar 25 (D2) Codesign tasks

[Jose Ferreira](#)

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These questions are presented under the following assumptions:

- They may be selected to be part of the final exam
- Responses must be posted by the students (not me)
- I will call your attention to any mistakes or wrong content posted in response

**N.B.:** This question follows Jürgen Teich's "[Hardware/Software Codesign: The Past, the Present, and Predicting the Future](#)" , *Proceedings of the IEEE*, Vol. 100, May 13th, 2012.

“Apart from the necessity of specification, formal analysis, and cosimulation tools for performance and cost analysis, (...) the major synthesis problem in codesign of electronic systems (...) involves three major so-called *mapping* tasks.” Name these three tasks, and present a brief description of each one of them.

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