"Do not write anything on question-paper except Roll Number, otherwise it shall be deemed as an act of indulging in unfair means and action shall be taken as per rules."

Roll No.

B.C.A. (11)

1719

Comput. Sys. Archit.

B.C.A. Part-II EXAMINATION - 2022 Paper - X

BCA 204 - COMPUTER SYSTEM ARCHITECTURE

Time Allowed: Three Hours

Maximum Marks: 80

- Note: 1. Question No. 1 is Compulsory. Attempt any FOUR from the remaining questions.
 - 2. All questions carry equal marks.
- 1. Define the following in brief:

 $2 \times 8 = 16$

- (a) What is a Micro Processor?
- (b) What is Load and Store architecture?

- Define Instruction Cycle? (c) (d) What is LXI? (e) What is FLAG? (f) What is Instruction format? (g) Define System Bus. Write difference between LDA and STA. (h) Write short notes on the following: Explain instruction format in detail with an example (i)
- each. Explain accumulator based architecture in detail. (ii)
 - (iii) Explain jump instruction in detail.
 - Explain register set of 8085 in detail. (iv)
- Explain various Addressing techniques in detail. 3. (a)
 - Explain 10 data transfer instruction of 8085 in (b) detail. $8 \times 2 = 16$
- Write an Assembly language program in 8085 to copy any 4. 8 bit data available on X memory location and copy it into 5 adjacent locations. 16
- Explain DMA with diagram and its function in 5. (a) detail.
 - (b) Explain different addressing modes in detail. 8×2=16

2.

 $4 \times 4 = 16$

6. Explain following:

- 4×4=16
- (a) Draw 7 segment LED display
- (b) Floppy disk drive
- (c) CPU organization
- (d) UDU
- 7. (a) Difference between I/O mapped and memory mapped I/O
 - (b) Explain Stack.

 $8 \times 2 = 16$