Algorithm 1 MFR of finite field arithmetic circuits

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Require: Spec: f, buggy Impl: C modeled as a polynomial ideal F = \{f_1 \dots, f_s\} under RTTO >
Assume: C doesn't admit single-fix rectification //[Rao. et al, FMCAD'18]
Ensure: Rectification of C to match f
 1: procedure rectification
        remainder = verify(f, F + F_0) // Verification
 2:
 3:
         O_a = analyze (remainder) // Error Diagnosis
         I_n = PotentialNets()
 4:
 5:
        m=2; rectified = False; O_A=\emptyset; \mathcal{M}_i=\emptyset; \mathcal{F}_w=\emptyset
 6:
             \{O_A\} = SetsOfUniquePartitions(O_a, m)
 7:
             for each O_A^i \in O_A do \{\mathcal{M}_i\} = SetsOfIntersectionCovers(O_A^i)
 8:
 9:
                 for each \mathcal{M}_i^j \in \mathcal{M}_i do
10:
                     \{\mathcal{F}_w\} = SetsOfDistinctTargets(\mathcal{M}_i^j, m)
11:
                     for each f_w \in \mathcal{F}_w do
12:
                          F', P_k(X) = MFRSetup(F, f_w) //MFR Notations
13:
                         if MFRCheck(F') == 0 then //MFR Check
14:
                              rectified = True
15:
                              U = rectFunction() // Function Computation
16:
                              break out of all for loops
17:
        while (!rectified && (++m \le |O_a|))
18:
19:
        return U
```