

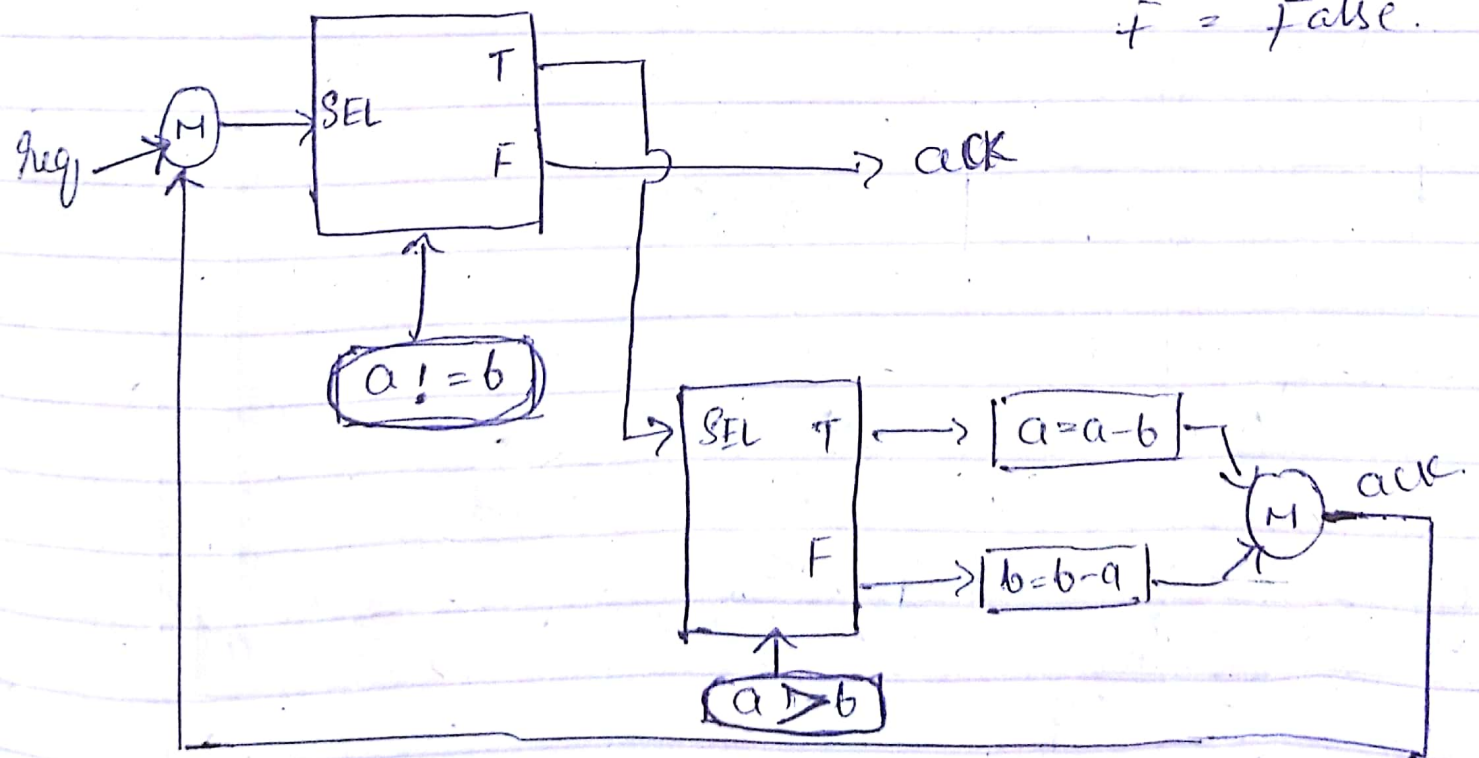
3.13

U1072596

VIKAS KUMAR RAO

circuit for (while/; ) loop.

M = Merge element  
req = request  
ack = acknowledgment  
SEL = select  
T = True  
F = False.



Full circuit:  $\rightarrow$  req/ack  $\rightarrow$  ack/req  $\Rightarrow$  data

C = C-element  
 00  $\rightarrow$  0  
 11  $\rightarrow$  1  
 01/10  $\rightarrow$  retain state

