

I am a second-year graduate student (pre-candidacy PhD student) in the ECE department at the University of Utah working under Prof. Priyank Kalla. I joined the program in Fall 2016, and I am exploring research topics in design automation and design verification of digital VLSI systems. I have completed some coursework in VLSI design, computer architecture, formal hardware verification, and Algorithms. Our group submitted two papers to the IWLS'18 conference and both papers got selected for representation at the prestigious event. I will be presenting a poster at the conference while a colleague of mine will be presenting another paper. I believe the exposure to IWLS will be very beneficial to me to get a big picture of design-flows, CAD methodologies, core-technologies and how these are applied in the semiconductor industry. To support my travel and attendance at IWLS, I request for the student travel grant and to consider this as my application for the same.