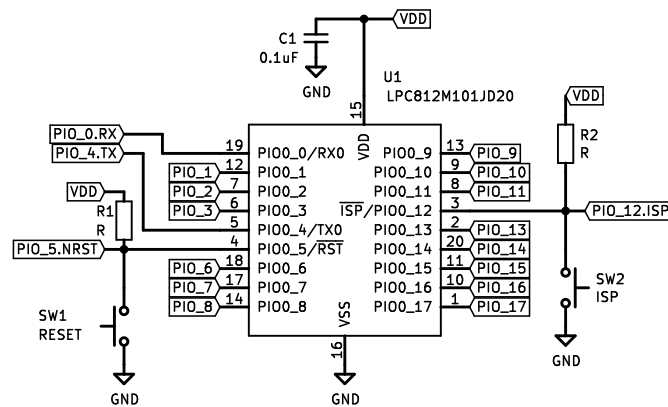


2.54" pitch breakout.  
Pins ordered according to SOIC-20



LPC812 (SOIC-20W)  
with minimum supporting components.

SW2 to enter ISP mode (held during reset).  
ISP activates USART0 for programming using built-in bootloader:  
PIO0\_0 - RXD  
PIO0\_4 - TXD

Sheet: /  
File: lpc812-breakout.sch

**Title:**

Size: USLetter Date:  
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**Rev:**  
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