

Control

The diagram illustrates a computer architecture layout. At the top left is a large red rectangle labeled 'Control'. To its right is a 2x2 grid of four yellow rectangles, each labeled 'ALU'. Below the 'Control' block and the 'ALU' grid is a wide blue rectangle labeled 'Cache'. At the bottom is a wide orange rectangle labeled 'DRAM'. All components are separated by thin black lines, and the entire diagram is enclosed in a black border.

ALU

ALU

ALU

ALU

Cache

DRAM