

# 34<sup>th</sup> IFIP/IEEE International Conference on Very Large-Scale Integration

## VLSI-SoC 2026

### October 11-14, 2026

### Limassol, Cyprus

[www.vlsi-soc.com](http://www.vlsi-soc.com)



University of  
Central Lancashire  
UCLan



VLSI-SoC 2026 is the 34<sup>th</sup> in a series of international conferences sponsored by IFIPTC 10 WG 10.5, IEEE CASS, IEEE CEDA and ACM SIGDA, which explores the state-of-the-art in the areas that surround Very Large Scale Integration (VLSI) and System-on-Chip (SoC). Previous conferences have taken place in Edinburgh, Trondheim, Tokyo, Vancouver, Munich, Grenoble, Gramado, Lisbon, Montpellier, Darmstadt, Perth, Nice, Atlanta, Rhodes, Florianópolis, Madrid, Hong Kong, Santa Cruz, Istanbul, Playa del Carmen, Daejeon, Tallinn, Abu Dhabi, Verona, Cuzco, Salt Lake City, Singapore, Patras, Sharjah, Tangerang, and Puerto Varas. The 2026 edition will take place at Limassol, Cyprus, a tourist and top destination in Southeast Europe.

**Paper Submission:** Papers should present original research and industrial results not published or submitted for publication in other forums. Electronic submission in PDF format to the EasyChair platform for VLSI-SoC 2025 is required. The proceedings will be published by IEEE and will be made available through IEEE Xplore. A selection of the conference best papers will be invited to submit an extended version to be included as chapters of a book to be published by Springer.

**Paper Format:** Papers should not exceed 4 pages + 1 page with only references (single-spaced, 2 columns, 10pt font). Submissions should be camera-ready, following the IEEE proceedings specifications.

**Paper Publication and Presenter Registration:** Papers will be accepted for regular or poster presentation at the conference. Every accepted paper MUST have at least one author registered at the conference by the time the camera-ready paper is submitted; the registered author is also expected to attend the conference and present the paper. A limited number of travel grants are available to needy PhD students from emerging countries.

**PhD Forum and PhD School:** A PhD Forum with a dedicated poster session will be included in the technical program, and PhD school on design and engineering of electronics systems will be co-located with the main conference.

#### General Chairs:

- Tiziana Margaria (Univ. of Limerick, IE),
- Ian O'Connor (École Centrale de Lyon, FR)

#### Program Chairs:

- Panagiota Nikolaou (Univ. of Central Lancashire, CY),
- Florenc Demrozi (Univ. of Stavanger, NO)

**Publication Chair:** Enrico Fraccaroli (UNC Chapel Hill, US)

**Tutorial Chair:** Alberto Bosio (École Centrale de Lyon, FR)

**Finance Chair:** Petros Stratis (EasyConferences, CY)

**Publicity (Europe):** Alessio Burello (Politecnico di Torino, IT)

**Industrial Chair:** Leticia B. Pöhls (Leibniz IHP Microelectronics, DE)

**Local Industrial Chair:** Andreas Diavastos (Cyprus Univ. of Technology, CY)

**Special Session Chair:** Theocharis Theocharides (Univ. of Cyprus, CY)

**Award Chair:** Katell Morin-Allory (TIMA Laboratory, FR)

**Ph.D. Forum Chair:** Fatih Ugurdag (Ozyegin Univ., TR)

**Panel Chair:** Sara Vinco (Politecnico di Torino, IT)

**Web Chair:** Matteo Iervasi (Univ. of Stavanger, NO)

**Local Arrangements:** Nicolas Stratis, Petros Stratis (EasyConferences, CY)



#### Topics of interest include (but not limited to) the following:

- Applications and Architectures for AI and ML
- Communication Architectures and Technologies
- Computing Paradigms
- Digital Circuits Design and Synthesis
- Embedded System Design
- Emerging Technologies and Applications
- Low-Power, Energy-Efficient and Thermal-Aware Design
- Security
- Sensing and Signal Processing Technologies
- Smart Systems, Devices and Applications
- System Specification, Simulation and Verification
- Test and Dependability

**Abstract Deadline:**

**28 April 2026**

**Full Paper Deadline:**

**05 May 2026**

**Author Notification:**

**16 June 2026**

**Camera Ready:**

**07 July 2026**

