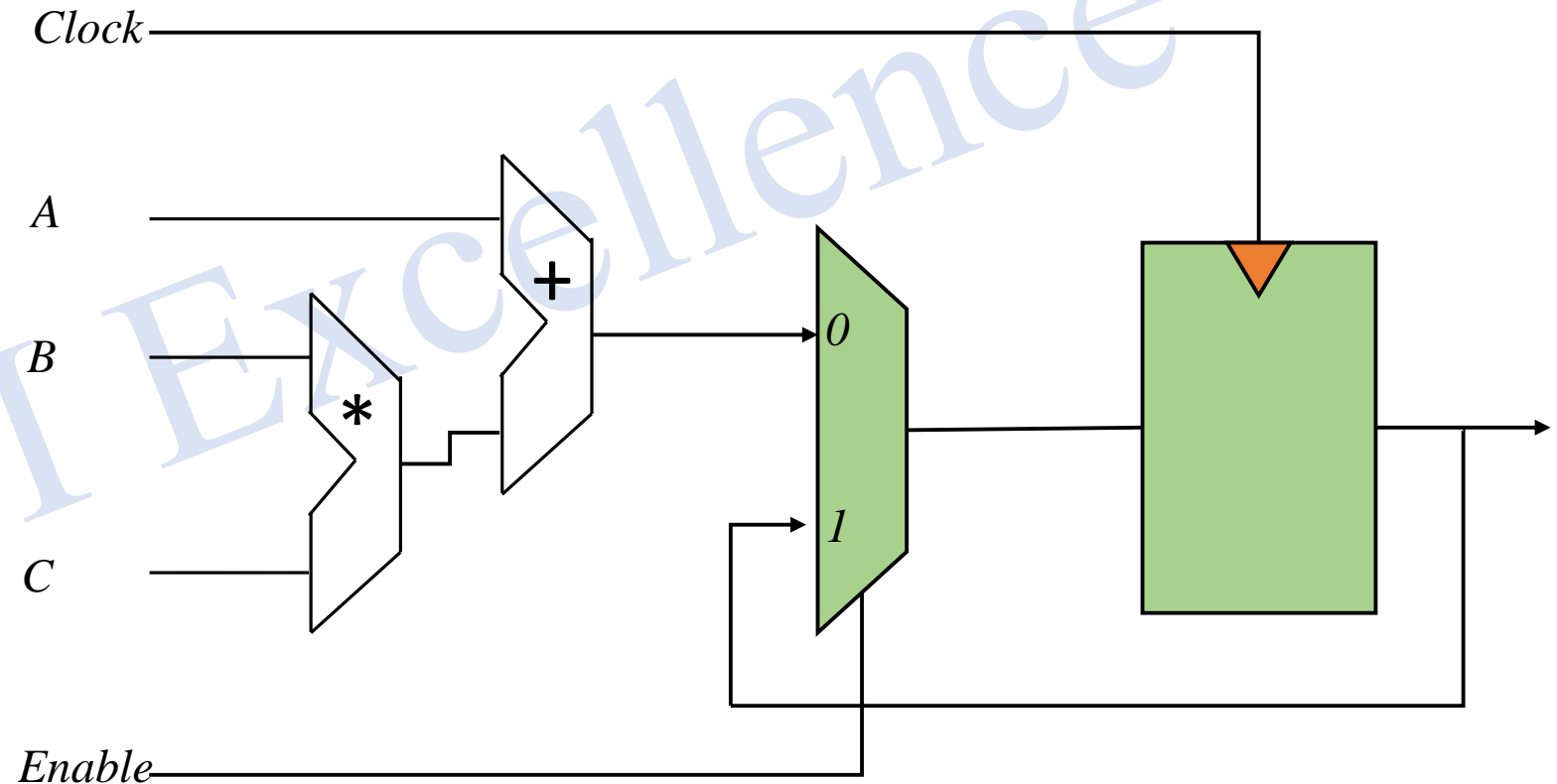


# VLSI Low Power Design : Interview Question #03

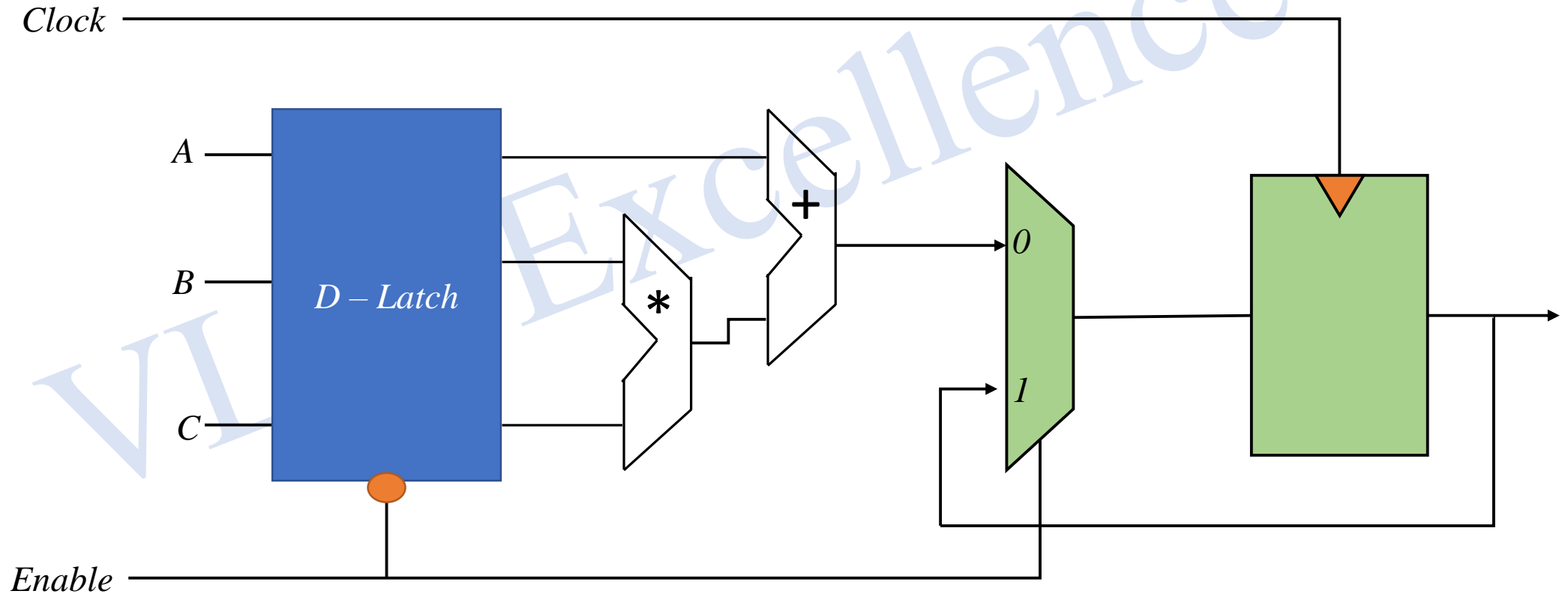
*Video Project [Link](#)*

# VLSI Low Power Design : Interview Question #03

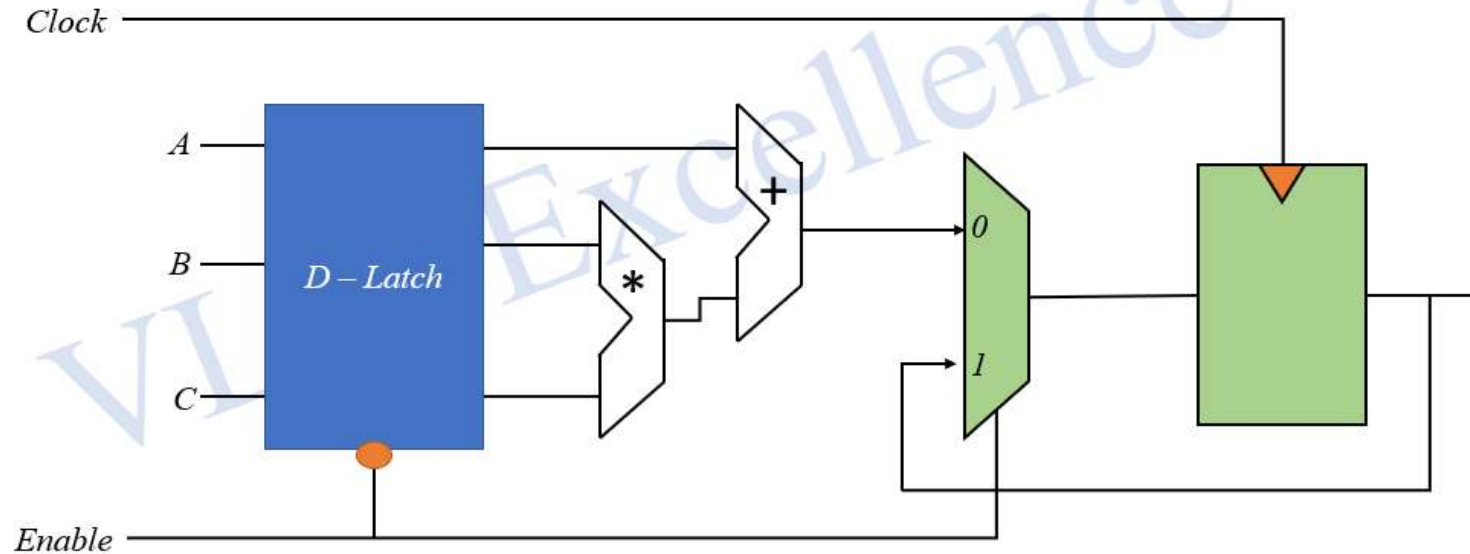
Optimize the given circuit for minimum power consumption !



## VLSI Low Power Design : Interview Question #03 – Solution



## VLSI Low Power Design : Interview Question #03 – Solution



When the datapath result is not being used, the datapath can be kept quiet by isolating it from its inputs with a latch. The power savings are due to lowered activity in the datapath operator.

## VLSI Low Power Design : Interview Question #03- Trade-Off

1. **Area Impact** – Extra Latch Circuit
2. **Timing Impact** – Need to handle carefully with added Latch in Data Path

VLSI Excellence

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Thanks !!