

VLSI-SoC 2021: Ph.D. and Student Forum

October 4-8, 2021

Singapore

Nanyang Technological University

Virtual Conference



Eligibility for Student Forum: Undergraduate, master and early Ph.D. students are invited to submit your work.

Eligibility for PhD Forum: The author must have completed at least one year of a Ph.D. program. Past PhD scholar who have defended their thesis in or after June 2020 are also encouraged to submit.

Presentation: Posters will be introduced in the Ph.D. Forum Session (two-minute time slot, one slide) and next presented in a full one-hour Poster Session.

Publication: Accepted posters will be given two pages in the soft proceedings.

Grants for PhD students: VLSI-SoC 2021 finances a limited number of travel grants through the Technical Committee TC-10 (Computer Systems Technology) and WG 10.5 of the International Federation for Information Processing - IFIP. In principle, PhD Forum grants are restricted to support students who cannot be fully supported by their institutions.

Student Forum grants are restricted to support under representative groups (women, latin, and others) of students who cannot be fully supported by their institutions.

How to Apply: Submit a two-page extended abstract of your research work in PDF format. Send submissions via EASYCHAIR platform of VLSI-SoC 2021.

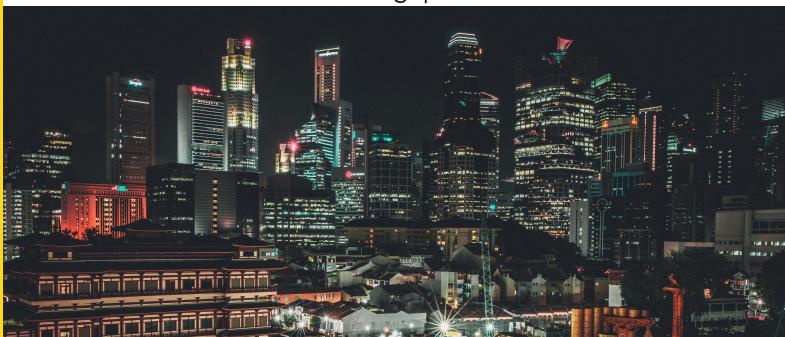
Paper Format: Papers should be compliant with the guidelines for regular papers.



VLSI-SoC 2021's Ph.D. and Student Forum is a poster session aimed at the exchange of ideas and experiences of students from different parts of the world. Elected students have an opportunity to discuss their thesis and/or research work with specialists within the system and design automation communities. This offers a good opportunity for students to receive valuable feedback and gain exposure in the job market. Furthermore, this forum also provides a great chance for industry officials to meet junior researchers, giving an avenue for incorporating the latest research developments into their companies.

VLSI-SoC 2021 will be held under the theme "Intelligent and Secure Edge Computing," exploring the design and optimization challenges around AI, machine learning, and cybersecurity on distributed and heterogeneous low-power circuits and systems powered by CMOS and beyond-CMOS technologies.

VLSI-SoC 2021 will be held in Singapore.



Topics of interest include, but are not limited to:

- Analog mixed-signal, sensors and RF
- VLSI circuits and SoC design
- Embedded systems design and software
- CAD tools and methodologies for digital IC design and optimization
- Verification, modeling and prototyping
- Design for testability, reliability and fault tolerance
- Hardware security
- Emerging technologies and new computing paradigms

General co-chairs:	Anupam Chattopadhyay (NTU, SG) Andrea Calimera (Politecnico di Torino, IT)
Program co-chairs:	Chip Hong Chang (NTU, SG) - Victor Grimblatt (Synopsys, CL)
Special session co-chairs:	Francesco Regazzoni (University of Amsterdam, NL & Università della Svizzera italiana, CH) - Mohammed Sabry (NTU, SG)
PhD forum chair:	Shivam Bhasin (Temasek Labs NTU, SG) - Trevor Carlson (NUS, SG)
Industrial chair:	Annamalai Muthu (CMElabs, SG)
Local co-chairs:	Manimuthu Arunmozh (NTU, SG) - Nandeesh Veeranna (ST Microelectronics, SG)
Publication chair:	Nicola Bombieri (University of Verona, IT)
Finance chair:	Debjyoti Bhattacharjee (IMEC, BE)
Publicity co-chairs:	Swaroop Ghosh (Penn State, USA) - Shahar Kvatinsky (Technion, IL) Farhad Merchant (RWTH Aachen, DE) - Ian O'Connor (ECL, FR) Graziano Pravadelli (University of Verona, IT) Srinivas Katkoori (University of South Florida, USA)
Website co-chairs:	Valentino Peluso - Roberto G. Rizzo (Politecnico di Torino, IT)
Steering committee:	Ibrahim Elfadel (Khalifa University, UAE) - Masahiro Fujita (University of Tokyo, JP) Manfred Glesner (TU Darmstadt, DE) - Matthew Guthaus (UC Santa Cruz, USA) Salvador Mir (TIMA, FR) - Ian O'Connor (ECL, FR) - Graziano Pravadelli (University of Verona, IT) Ricardo Reis (UFGRGS, BR) - Luis Miguel Silveira (INESC ID, PT) - Chi-Ying Tsui (HKUST, Hong Kong, CN) Fatih Ugurdag (Ozyegin University, TR)