



CALL FOR SPECIAL SESSIONS

Aim and Scope:

The VLSI-SoC 2021 Organizing Committee invites proposals for Special Sessions to be held during the 2021 edition of the **IFIP/IEEE International Conference on Very Large Scale Integration** in Singapore, from 5th to 7th October 2021.

Besides regular technical papers, special sessions complement the regular technical program by highlighting new and emerging research topics or innovative applications of established approaches. Special Sessions target hot and emerging topics in the field of "Intelligent and Secure Edge Computing", but can also cover any other area focusing on challenging open problems in the areas of Very Large Scale Integration (VLSI) and System-on-Chip (SoC) design. Proposers of special sessions will serve as organizers of the session, contacting speakers, moderators, and ensuring the timeliness of the paper submission. Prospective organizers should submit their special session proposal of up to two pages including the following:

- Session title
- Relevance of the special session to VLSI-SoC 2021
- Intended target audience
- Format of the session
- List and short description of the proposed contributions and short bio of the speakers
- Short bio of the organizers

Upon acceptance, special session presenters can prepare either a single paper for the entire session or one paper per contributor. Papers should not exceed 6 pages (single-spaced, 2 columns, 10pt font). Submissions should be camera-ready, following the IEEE proceedings specifications. Papers accepted for the special session will go through a review process. The participation of speakers from industry is encouraged and will be positively evaluated.

Special session co-chairs:

- Francesco Regazzoni (University of Amsterdam, NL & Università della Svizzera italiana, CH)
- Mohammed Sabry (NTU, SG)

Submission:

Submissions should to be made via Email (specialsession-vlsisoc2021@a.alari.ch) with the subject "**VLSI2021 - Special Session Proposal**"

