Vasishta Malisetty

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EDUCATION

Northeastern University, Boston, MA — May 2026
Candidate for Bachelor of Science in Electrical and Computer Engineering — **GPA**: 3.85
Activities: Generate Product Development Studio, John Martinson Honors Program, Intramural Soccer

TECHNICAL SKILLS

Hardware: PCB Schematic Design and Layout, Arduino, Oscilloscope, Multimeter, Soldering
Programming Languages: C++, Python, NI LabVIEW, MATLAB, SystemVerilog, RISC-V
Tools: KiCAD, Git, Linux, LTspice, Solidworks, AutoCAD

WORK EXPERIENCE

Raytheon — Jun. 2024 – Aug. 2024
Systems Engineering Intern — Marlborough, MA

- Generated virtual simulations using Jenkins to debug Raytheon's SPY-6 naval radar software, reducing radar testing expenses
- Developed scenarios using MATLAB to test the SPY-6 radar's ability to identify and track common electronic attack patterns, neutralizing the effects of these attacks on the radar
- Organized useful scenario generation tools across various Raytheon teams into one PowerPoint deliverable using ClearCase, allowing 66% of the SPY-6 department to learn the scenario generation process within two months

Philips — Jan. 2024 – Jun. 2024
Hardware Test Engineering Co-op — Andover, MA

- Automated hardware lifecycle test fixtures using NI LabVIEW to qualify Philips patient monitoring products against international reliability standards, saving Philips over 170 hours per product requiring validation
- Developed five instrument drivers for a 32 SPDT relay board, linear actuator, DC stepper motor, power supply, and load cell, allowing the computer to power and control the hardware peripherals via USB, GPIB, and COM ports
- Designed user-friendly control panels for the hardware lifecycle test fixtures, saving Philips $3090 per employee on LabVIEW training by enabling untrained Philips employees to operate the test fixtures
- Soldered wire harnesses to connect a relay board, load cell, and power supply with a PCB screw terminal block, enabling autonomous collection of insertion force and contact resistance measurements for 20,000 cycles
- Performed highly accelerated life testing on Philips patient monitoring prototypes to pinpoint critical failure modes and formulate design improvements, resulting in a 20% increase in product service life

Rite Aid — Jun. 2023 – Aug. 2023
Data Security Intern — Hopkinton, MA

- Scripted 1119 Atomic Red Team tests using PowerShell, pinpointing gaps within Rite Aid's network protection software
- Formulated 34 regex patterns for use in Rite Aid's endpoint security integrations by investigating 532 unidentified Indicators of Compromise associated with emerging ransomware groups, successfully detecting a malware threat

LEADERSHIP EXPERIENCE

Sensify: Modular Virtual Reality Controller – Lead Electrical Engineer — Aug. 2024 – Present

- Lead a team of five engineers in the electrical development of a modular virtual reality controller that provides realistic touch simulation to users

PROJECTS

C-STAR: Autonomous Concrete Sounding Robot – Hardware Engineer — Jan. 2024 – Apr. 2024

- Created an autonomous concrete sounding robot that detects delaminations in concrete structures
- Owned the drivetrain subsystem by designing a custom PCB with an ESP32 microcontroller and H-Bridge Motor Drivers to control brushed DC drive motors using industry-standard PWM control signals
- Developed odometry and PID algorithms using C++ to schedule interrupts to read quadrature encoder data, calculate velocity and distance parameters, and send movement commands through Bluetooth to drive the robot

SEBIK: Automated Tabletop Injection Molder – Hardware Engineer — Sept. 2023 – Dec. 2023

- Built an automated tabletop injection molder to produce one common medical product every four minutes to address medical supply shortages in rural hospitals
- Designed the injection ram subsystem using a custom PCB with N-channel MOSFETs and an ATmega328PB microcontroller to control pneumatic pistons, enabling molten polypropylene to be injected every four minutes
- Programmed exception handling mechanisms in C++ after analyzing potential failure modes within the injection ram subsystem using a DFMEA, ensuring user safety throughout the injection process