Name	Algebraic Equation	Truth Table	Gate Symbol	Switch Circuit	Venn Diagram
AND	F=XY X·Y X*Y X ^ Y	XY F 00 0 01 0 10 0	X- Y- And Gate	X Y Switches in series	XY
OR	F=X+Y X ∨ Y	XY F 00 0 01 1 10 1 11 1	X Or Gate	X	XY
NOT	F=X X' ¬X —X	X F 0 1 1 0	X - F	= + <b>X</b> \	X
NAND	<b>F=</b> X ↑ Y  (XY)'  X   Y	XY F 00 1 01 1 10 1 11 0	X - P-F Nand Gate	+ <b>X</b> \ = - <b>Y</b> \	XY
NOR	F=X+Y X↓Y (X+Y)' X▽Y	XY F 00 1 01 0 10 0 11 0	X Y Nor Gate	= 'X'Y	XY
XOR	<b>F=X</b> ⊕ <b>Y</b> X ⊻ <b>Y</b>	XY F 00 0 01 1 10 1 11 0	X Y X X Sate	= + × × ×	X Y
XNOR	F=X⊕Y (X⊕Y)'	XY F 00 1 01 0 10 0 11 1	X Xnor Gate	X Y Y	XY
BUF	F=X	X F 0 0 1 1	X - F Buffer	- X × × × × × × × × × × × × × × × × × ×	X
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