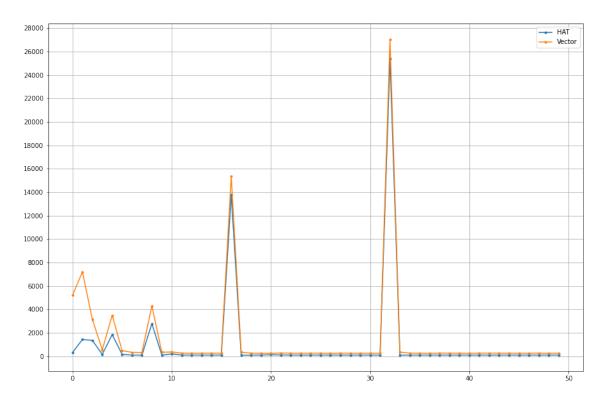
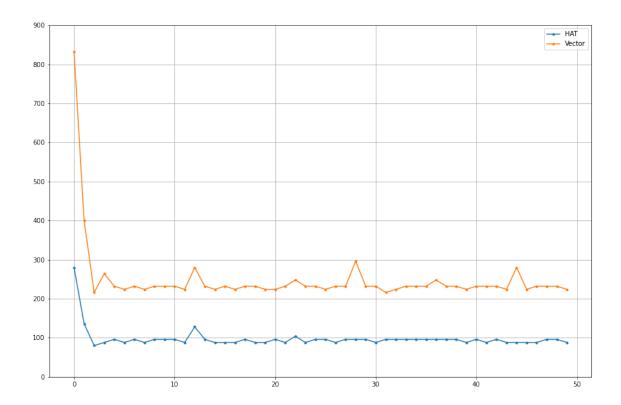
ICCS315: Assignment 1 Vanessa Rujipatanakul 30/01/2023

1: Resizable Arrays

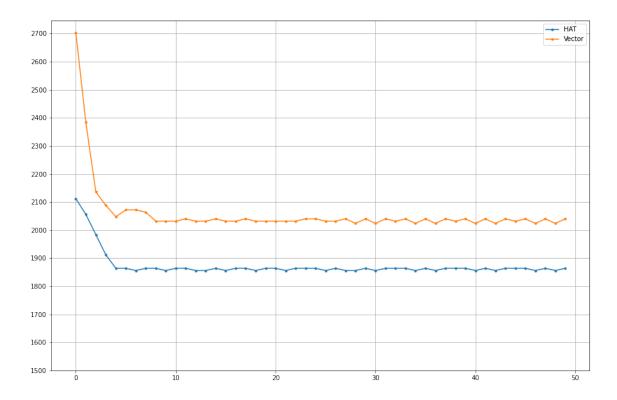
(a) Append Latency HAT ≈ 96 cycles Vector ≈ 260 cycles



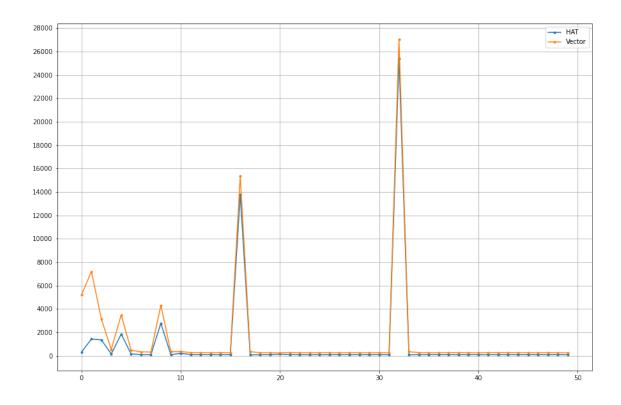
(b) Access Latency HAT ≈ 96 cycles Vector ≈ 232 cycles



(c) Scan Latency HAT ≈ 1864 cycles Vector ≈ 2036 cycles



(d) Overall Latency HAT ≈ 96 cycles Vector ≈ 264 cycles



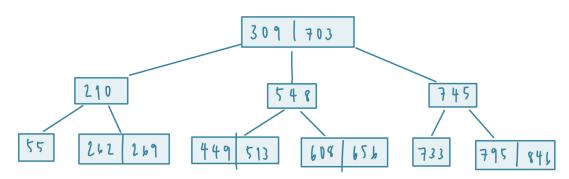
3: Skip Lists

(a)

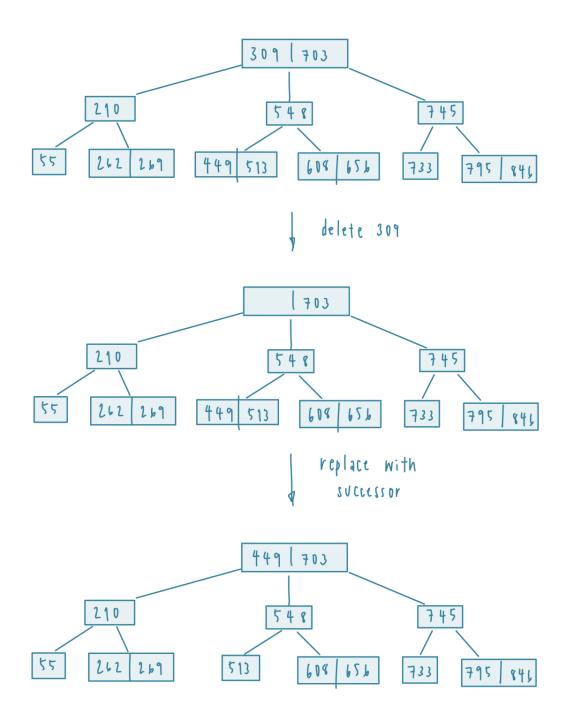
(b)

4: (a, b) tree

(a)



(b)



4: B-Tree Speed