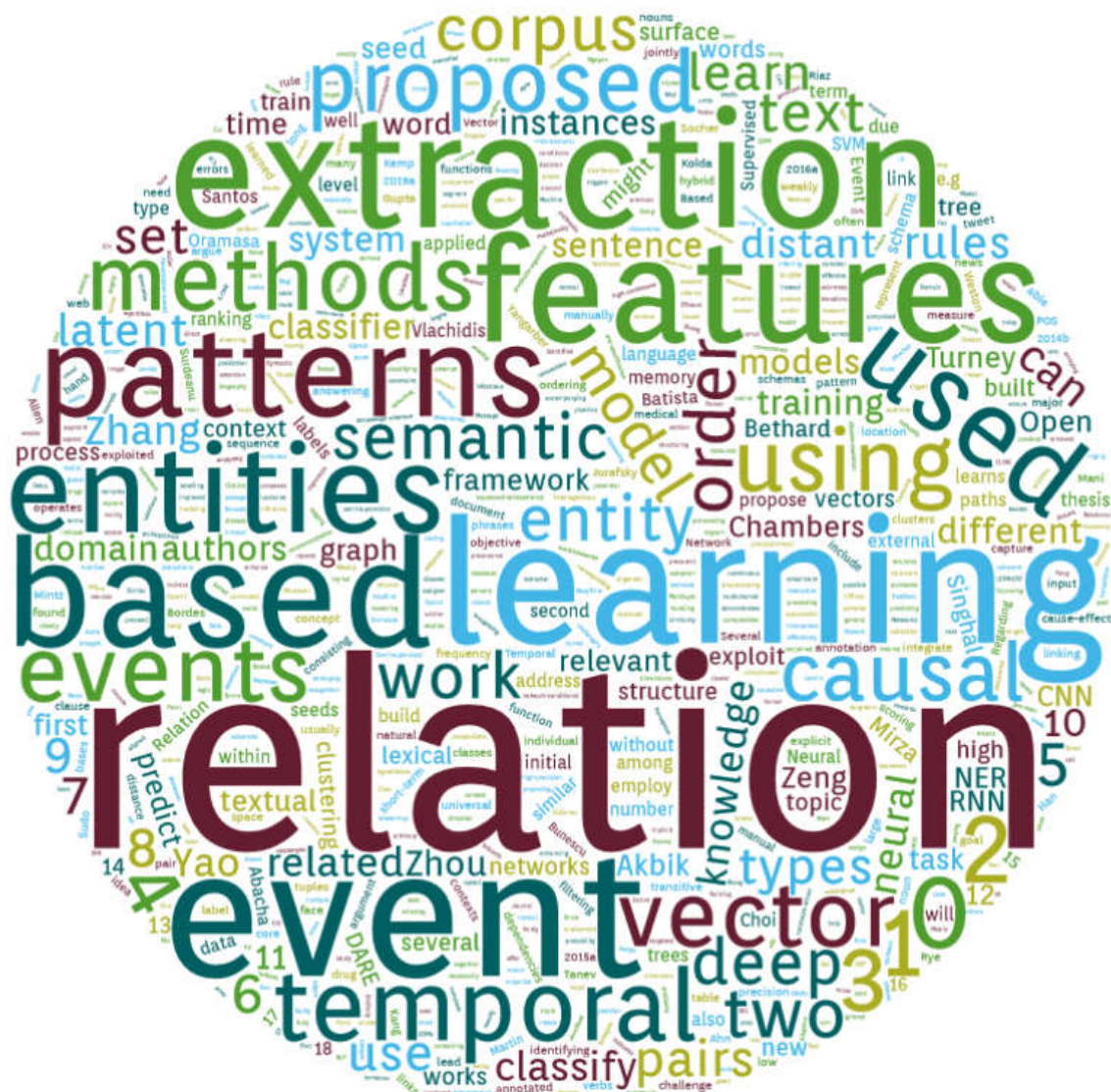


## Technical Challenge Patent Classification

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Laboratory for Systems, Software and Semantics



## ► Patent classification



# ELECTRICITY?

## ELECTRIC SWITCHES?

# Methodology

## ► US patent structure

### (12) United States Patent Bagheri et al.

(10) Patent No.: **US 10,834,118 B2**  
(45) Date of Patent: **Nov. 10, 2020**

### (54) **AMBIGUITY RESOLUTION SYSTEM AND METHOD FOR SECURITY INFORMATION RETRIEVAL**

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(\*) Notice: Subject to any disclaimer, the term of this  
patent is extended or adjusted under 35  
U.S.C. 154(b) by 227 days.

(21) Appl. No.: **15/837,061**

(22) Filed: **Dec. 11, 2017**

(65) **Prior Publication Data**  
US 2019/0182285 A1 Jun. 13, 2019

(51) **Int. Cl.**  
**H04L 29/06** (2006.01)  
**G06F 9/44** (2018.01)  
(Continued)

(52) **U.S. Cl.**  
CPC ..... **H04L 63/1433** (2013.01); **G06F 9/44**  
(2013.01); **G06F 11/362** (2013.01);  
(Continued)

(58) **Field of Classification Search**  
CPC ... H04L 63/1433; H04L 63/1483; G06F 9/44;  
G06F 11/362; G06F 11/3672;

### (56) **References Cited**

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(Continued)

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(74) *Attorney, Agent, or Firm* — Yee & Associates, P.C.

### (57) **ABSTRACT**

A method for ambiguity resolution in retrieving security  
information. A unified representation model utilizing the  
security information mined from a group of sources is  
generated by a computer system, wherein the unified rep-  
resentation model represents terms in the security infor-  
mation using vectors that describe locations in space for the  
terms. Distances between the terms indicate correlations  
between the terms, and the terms are for security concepts in  
the security information. The computer system represents  
the security concepts utilizing a group of weighted graphs  
that identifies semantic-relatedness between the terms uti-  
lizing the vectors in the unified representation model,  
wherein the group of weighted graphs enables interpreting  
queries with reduced ambiguity. The computer system inter-  
prets a seed query utilizing the group of weighted graphs.  
The computer system retrieves a portion of the security  
information utilizing the seed query interpreted utilizing the  
group of weighted graphs.

accordance with an illustrative embodiment.

### DETAILED DESCRIPTION

The present invention may be a system, a method, and/or  
a computer program product. The computer program prod-  
uct may include a computer-readable storage medium (or  
media) having computer-readable program instructions  
thereon for causing a processor to carry out aspects of the  
present invention.

The computer-readable storage medium can be a tangible  
device that can retain and store instructions for use by an  
instruction execution device. The computer-readable storage  
medium may be, for example, but is not limited to, an  
electronic storage device, a magnetic storage device, an  
optical storage device, an electromagnetic storage device, a  
semiconductor storage device, or any suitable combination  
of the foregoing. A non-exhaustive list of more specific  
examples of the computer-readable storage medium includes  
the following: a portable computer diskette, a hard disk, a  
random access memory (RAM), a read-only memory (ROM),  
an erasable programmable read-only memory (EPROM or  
Flash memory), a static random access memory (SRAM),  
a portable compact disc read-only memory (CD-ROM),  
a digital versatile disk (DVD), a memory stick, a floppy  
disk, a mechanically encoded device such as punch-  
cards or raised structures in a groove having instructions  
recorded thereon, and any suitable combination of the  
foregoing. A computer-readable storage medium, as used herein,  
is not to be construed as being transitory signals per se,  
such as radio waves or other freely propagating electromag-  
netic waves, electromagnetic waves propagating through a  
waveguide or other transmission media (e.g., light pulses  
passing through a fiber-optic cable), or electrical signals  
transmitted through a wire.

Computer-readable program instructions described herein  
can be downloaded to respective computing/processing  
devices from a computer-readable storage medium or to an  
external computer or external storage device via a network,  
for example, the Internet, a local area network, a wide area  
network and/or a wireless network. The network may com-  
prise copper transmission cables, optical transmission fibers,  
wireless transmission, routers, firewalls, switches, gateway  
computers and/or edge servers. A network adapter card or  
network interface in each computing/processing device  
receives computer-readable program instructions from the  
network and forwards the computer-readable program  
instructions for storage in a computer-readable storage  
medium within the respective computing/processing device.

Computer-readable program instructions for carrying out  
operations of the present invention may be assembler  
instructions, instruction-set-architecture (ISA) instructions,  
machine instructions, machine dependent instructions,  
microcode, firmware instructions, state-setting data, or  
either source code or object code written in any combination  
of one or more programming languages, including an object  
oriented programming language such as Smalltalk, C++ or

area network (WAN), or the connection may be made to an  
external computer (for example, through the Internet using  
an Internet Service Provider). In some embodiments, elec-  
tronic circuitry including, for example, programmable logic  
circuitry, field-programmable gate arrays (FPGA), or pro-  
grammable logic arrays (PLA) may execute the computer-  
readable program instructions by utilizing state information  
of the computer-readable program instructions to personal-  
ize the electronic circuitry, in order to perform aspects of the  
present invention.

Aspects of the present invention are described below with  
reference to flowchart illustrations and/or block diagrams of  
methods, apparatus (systems) and computer program prod-  
ucts according to embodiments of the invention. It will be  
understood that each block of the flowchart illustrations  
and/or block diagrams, and combinations of blocks in the  
flowchart illustrations and/or block diagrams, can be imple-  
mented by computer-readable program instructions.

These computer program instructions may be provided to  
a processor of a general-purpose computer, special purpose  
computer, or other programmable data processing apparatus  
to produce a machine, such that the instructions, which  
execute via the processor of the computer or other program-  
mable data processing apparatus, create means for imple-  
menting the functions/acts specified in the flowchart and/or  
block diagram block or blocks. These computer program  
instructions may also be stored in a computer-readable  
medium that can direct a computer, other programmable  
data processing apparatus, or other devices to function in a  
particular manner, such that the instructions stored in the  
computer-readable medium produce an article of manufac-  
ture including instructions which implement the function/act  
specified in the flowchart and/or block diagram block or  
blocks.

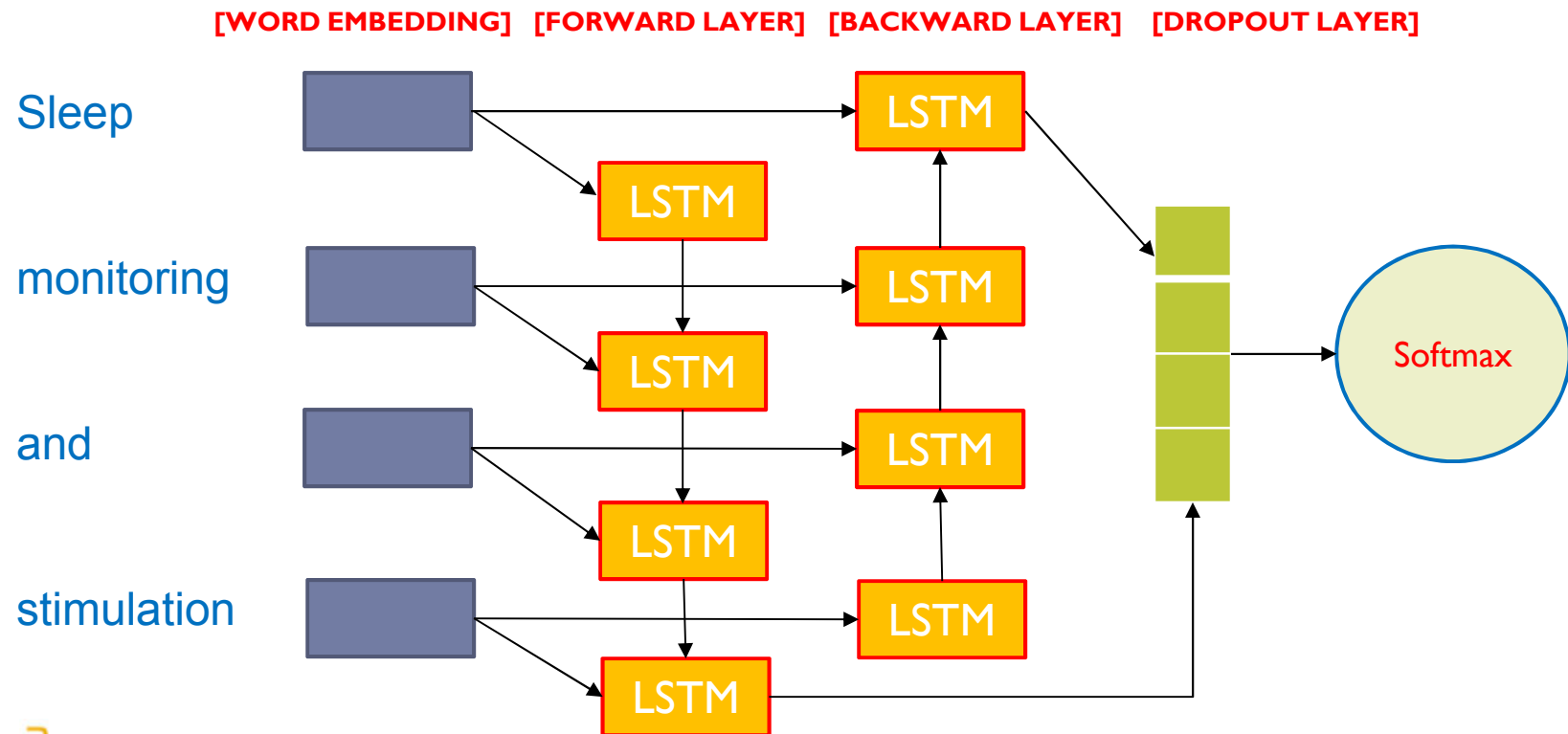
The computer-readable program instructions may also be  
loaded onto a computer, other programmable data process-  
ing apparatus, or other device to cause a series of operational  
steps to be performed on the computer, other programmable  
apparatus or other device to produce a computer imple-  
mented process, such that the instructions which execute on  
the computer, other programmable apparatus, or other  
device implement the functions/acts specified in the flow-  
chart and/or block diagram block or blocks.

The flowchart and block diagrams in the figures illustrate  
the architecture, functionality, and operation of possible  
implementations of systems, methods, and computer pro-  
gram products according to various embodiments of the  
present invention. In this regard, each block in the flowchart  
or block diagrams may represent a module, a segment, or a  
portion of instructions, which comprises one or more  
executable instructions for implementing the specified logi-  
cal function(s). In some alternative implementations, the  
functions noted in the block may occur out of the order noted  
in the figures. For example, two blocks shown in succession  
may, in fact, be executed substantially concurrently, or the  
blocks may sometimes be executed in the reverse order,  
depending upon the functionality involved. It will also be



# Methodology

- ▶ Bidirectional Long Short Term Memory (RNN) (Liu et al., 2016)
  - ▶ Using sequential information
  - ▶ LSTM uses passing input from last output to retain information that able to leverage all information at the end to make predictions.
- ▶ Proposed model



# Experimentation

## ▶ Experimental dataset

- ▶ <https://bulkdata.uspto.gov/data/patent/grant/redbook/fulltext/2020/ipg200107.zip>
- ▶ <https://bulkdata.uspto.gov/data/patent/grant/redbook/fulltext/2020/ipg200114.zip>

▶ 12,344 patent records

## ▶ Categories

### Main categories (8)

<b>A</b>	HUMAN NECESSITIES
<b>B</b>	PERFORMING OPERATIONS; TRANSPORTING
<b>C</b>	CHEMISTRY; METALLURGY
<b>D</b>	TEXTILES; PAPER
<b>E</b>	FIXED CONSTRUCTIONS
<b>F</b>	MECHANICAL ENGINEERING; LIGHTING; HEATING; WEAPONS; BLASTING
<b>G</b>	PHYSICS
<b>H</b>	ELECTRICITY

### Sub-categories (478)

<b>A01B</b>	SOIL WORKING IN AGRICULTURE OR FORESTRY ...
<b>A01C</b>	PLANTING; SOWING; FERTILISING,...
<b>A01D</b>	HARVESTING; MOWING,...
<b>A01F</b>	PROCESSING OF HARVESTED PRODUCE ...
<b>A01G</b>	HORTICULTURE,...
<b>A01H</b>	NEW PLANTS,...
<b>A01J</b>	MANUFACTURE OF DAIRY PRODUCTS,...
<b>A01K</b>	ANIMAL HUSBANDRY, ...
...	
<b>F01D</b>	MECHANICAL METHODS OR APPARATUS...
...	
<b>H01H</b>	ELECTRIC SWITCHES,...

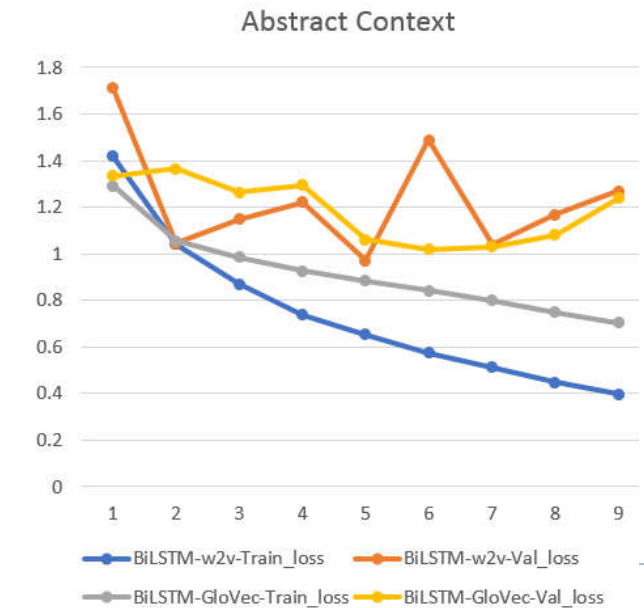
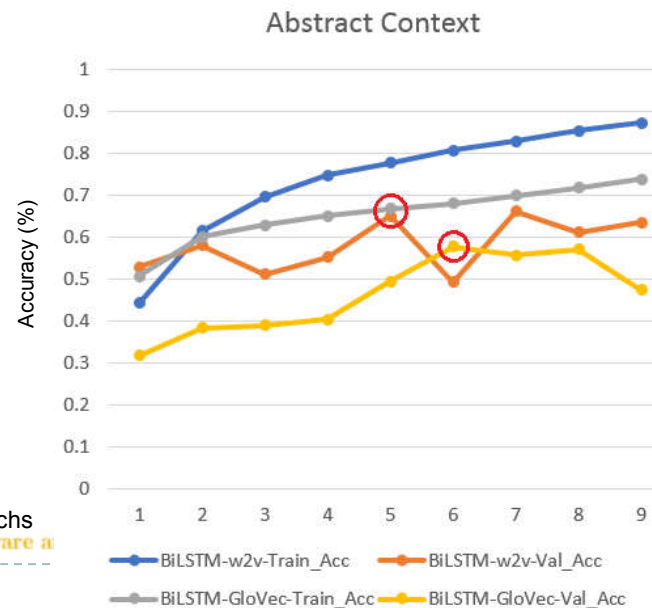
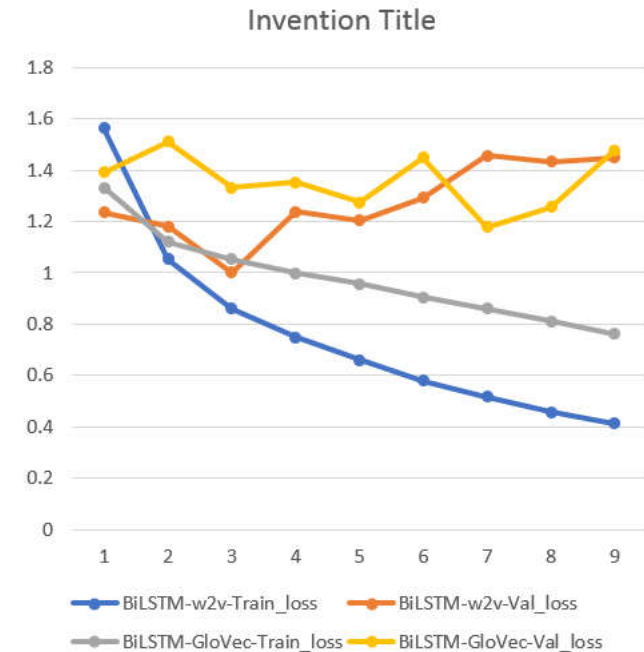
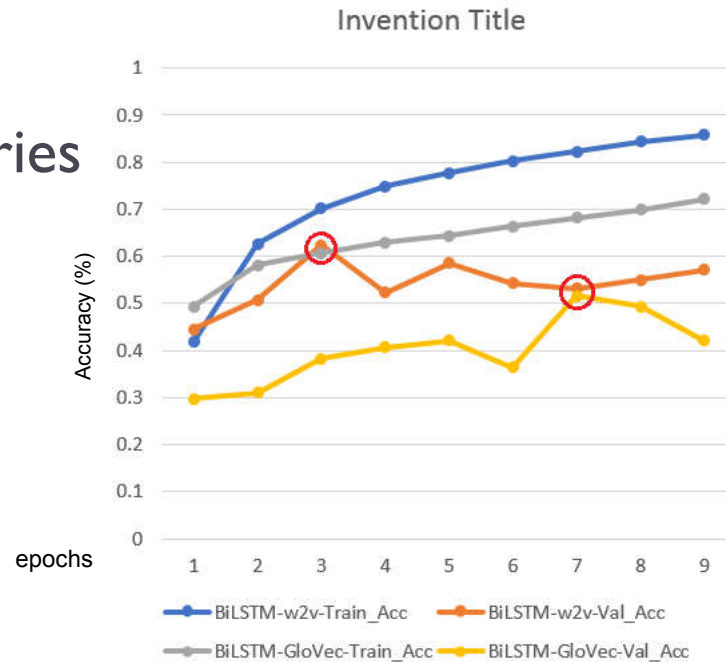
## ▶ Word Embedding

▶ Word2Vec

▶ GloVec

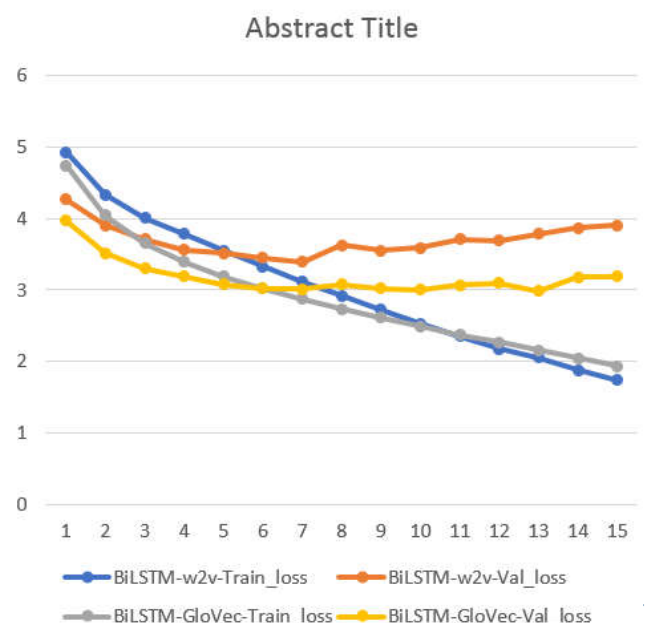
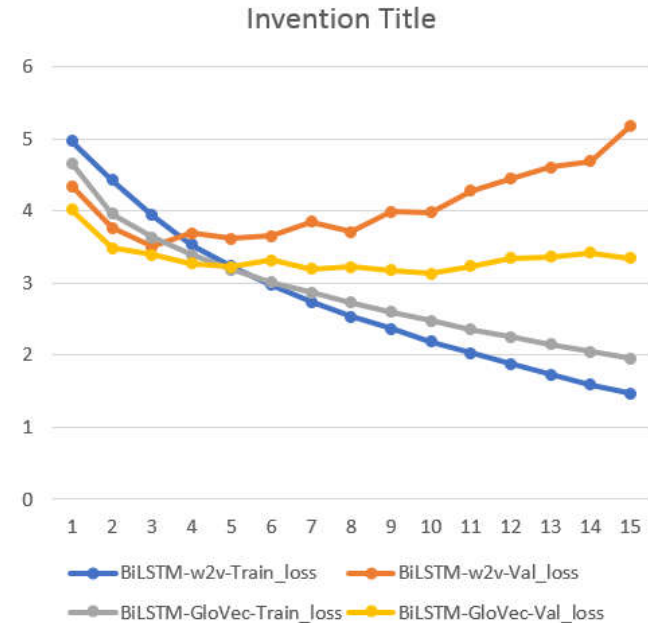
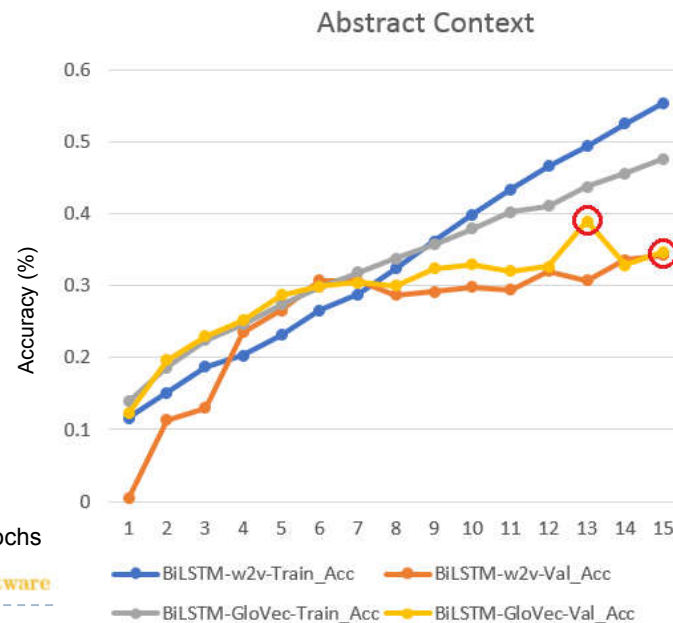
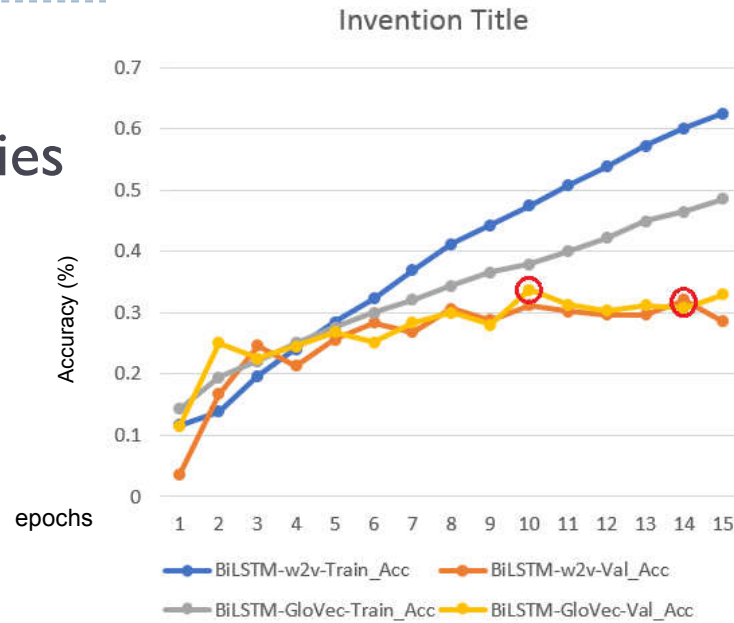
# Experimental results (Analysis)

## ► Main categories



# Experimental results

## Sub categories



# Experimental results (15 Epochs)

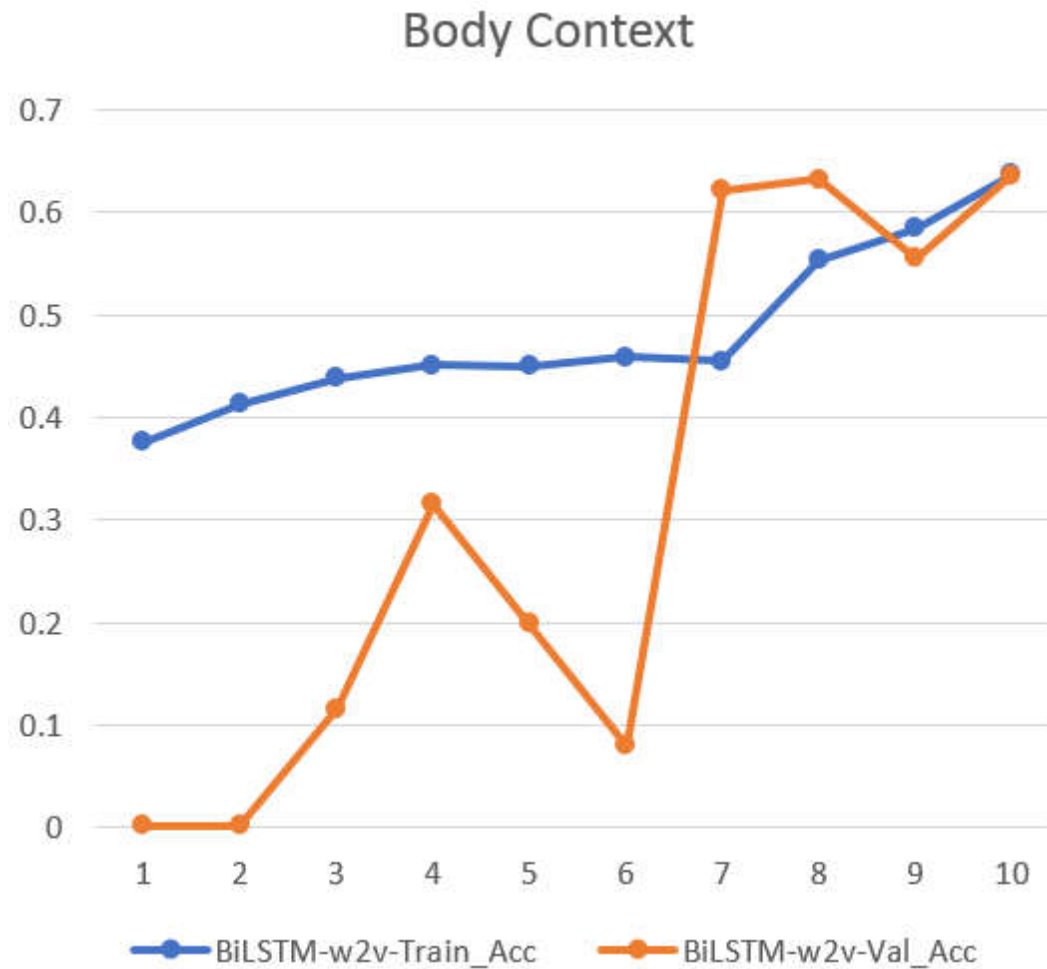
## ► Main categories and sub categories (15 Epochs)

	Main category (%)	Sub category (%)
<b>Invention Title</b>		
BiLSTM-Word2Vec (Avg)	54.43	25.88
BiLSTM-Word2Vec (Max)	<b>62.37</b>	32.13
BiLSTM-GloVec (Avg)	42.31	27.51
BiLSTM-GloVec (Max)	52.31	<b>33.80</b>
<b>Abstract Context</b>		
BiLSTM-Word2Vec (Avg)	58.54	25.59
BiLSTM-Word2Vec (Max)	<b>66.20</b>	34.23
BiLSTM-GloVec (Avg)	47.26	29.04
BiLSTM-GloVec (Max)	57.73	<b>38.93</b>



# Discussion

- ▶ Patent classification based on body context



# Discussion

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- ▶ Patent classification based on body context
  - ▶ BiLSTM is good? unigram and bigram?
  - ▶ Training time
  - ▶ Body context (summary) ?
  - ▶  $Tf*Idf$
- ▶ Compare with traditional ML classification
  - ▶ SVM,
  - ▶ kNN
  - ▶ ...
- ▶ Patent Storage
  - ▶ Hadoop
  - ▶ Lucene
  - ▶ ...

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Thank you for your attention.  
Q&A

