

Port A Functions

Port#	Pin	A	B	C	D	Extra	Syster Net	
0	48	PWMH0	TIOA0	A17		WKUP0	GPS_PPS	
1	47	PWMH1	TIOB0	A18		WKUP1		TP8
2	44	PWMH2	SCK0	DATRG		WKUP2		TP9
3	43	TWDO	NPCS3				I2C	
4	36	TWCKO	TCLK0			WKUP3	I2C	
5	35	RXD0	NPCS3			WKUP4	GPS_RX	
6	34	TXD0	PCK0				GPS_TX	
7	32	RTS0	PWMH3				XIN32 LOS_LIU	
8	31	CTS0	ADTRG			WKUP5	XOUT3: #RST_LIU	
9	30	URXD0	NPCS1	PWMF10		WKUP6	URXD0_SAMBA_RX	
10	29	UTXD0	NPCS2	PWMF11			UTXD0_SAMBA_TX	
11	28	NPCS0	PWMH0			WKUP7	#SPI_CS0	
12	27	MISO	PWMH1				SPI_MISO	
13	22	MOSI	PWMH2				SPI_MOSO	
14	21	SPCK	PWMH3			WKUP8	SPI_SCK	
15	20	TF	TIOA1	PWML3		WKUP14 /PIODCEN1		TP13
16	19	TK	TIOB1	PWML2		WKUP15/PIODCEN2	TX_CLK	input!
17	9	TD	PCK1	PWMH3		AD0	TX_DATA	output
18	10	RD	PCK2	A14	PWMF	AD1	RX_DATA	inout
19	13	RK	PWML0	A15		AD2	RX_CLK	input
20	16	RF	PWML1	A16		AD3	(rx_frame)	TP10
21	11	RXD1	PCK1			AD8	CLK2048 (to LIU)	
22	14	TXD1	NPCS3	NCS2		AD9		TP11
23	15	SCK1	PWMH0	A19		PIODCCLK		TP12
24	23	RTS1	PWMH1	A20		PIODC0	LED1	
25	25	CTS1	PWMH2	A23		PIODC1	LED2	
26	26	DCD1	TIOA2	MCDA2		PIODC2	LED3	
27	37	DTR1	TIOB2	MCDA3		PIODC3	(rx_frame)	TP14
28	38	DSR1	TCLK1	MCCDA		PIODC4		TP15
29	41	RI1	TCLK2	MCCK		PIODC5		TP16
30	42	PWML2	NPCS2	MCDA0		WKUP11/PIODC6	SPI_CS2	
31	52	NPCS1	PCK2	MCDA1		PIODC7	SPI_CS1	

Inst

TC0
PWM
TWIO
UART0
USART0
USART1
SSC
SPI