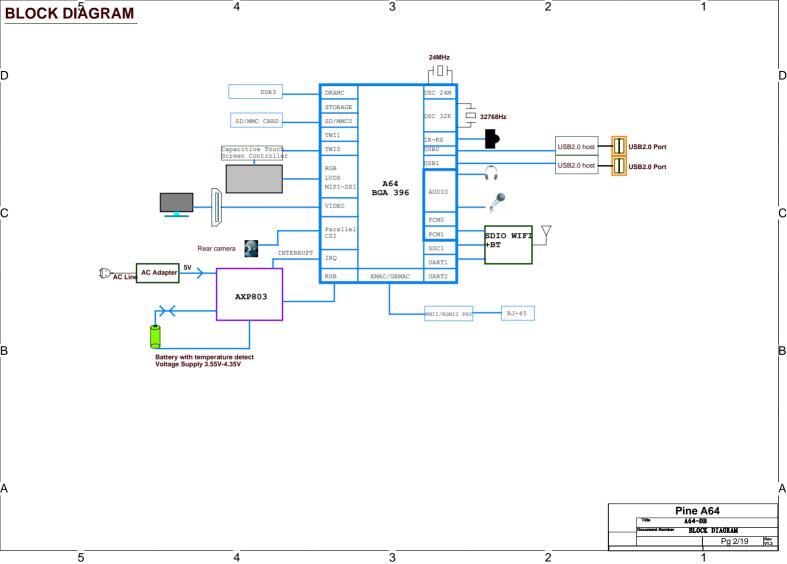
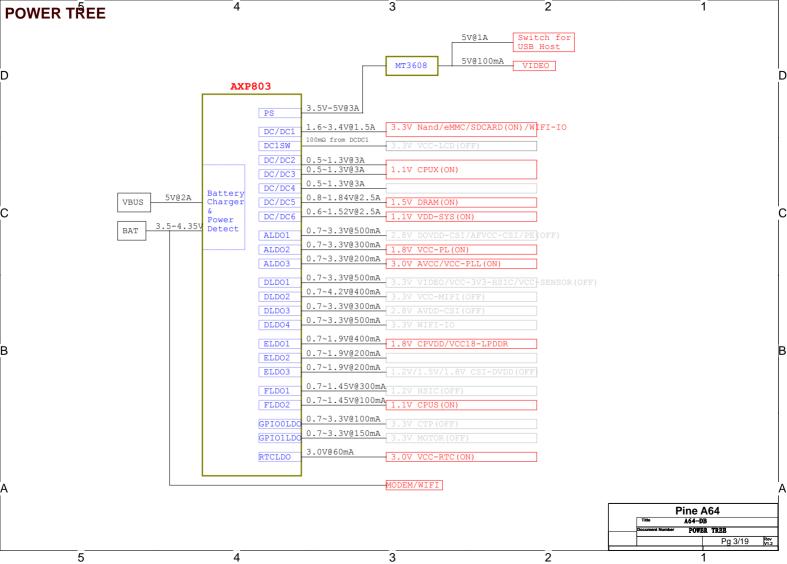
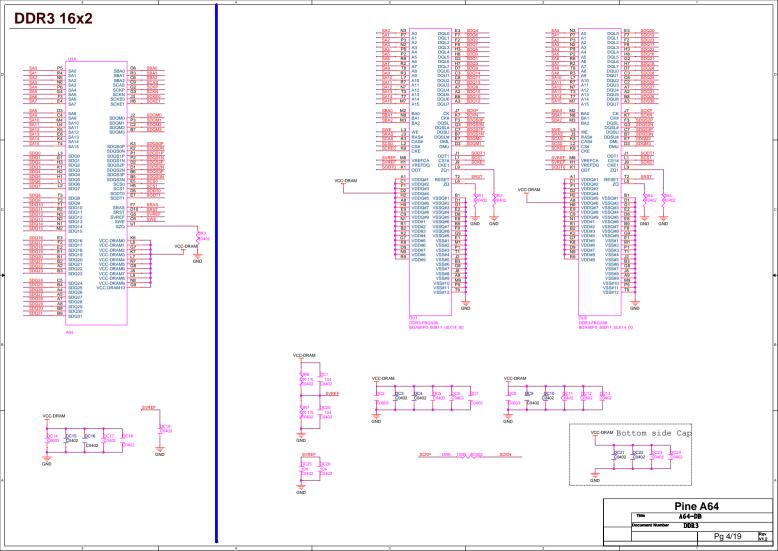
VERSION HISTORY		3	2		1
	Revision	Description	Date	Drawn Checke	d Approved
Index:	Ver 1.0	Release version	2015-04-22		
PO1: REVISION HISTORY PO2: BLOCK DIAGRAM PO3: POWER TREE P04: DDR3 16X2 P05: CPU P06: PMIC P07: NAND/eMMC P08: AUDIO P09: T-CARD/USB P10: CAMERA P11: LCD P12: Euler Bus P13: VIDEO P14: LCD POWER P15: WIFI+BT C P16: CTP/IR P17: P1-2 Bus P18: EMAC/GMAC P19: EMAC/GMAC	Ver 1.2		2015-09-28		
В					
Α				P Title Document Number	ine A64 A64-DB VERSION HISTORY Pg 1/19 Rev M12
5		3	2		1







CPU VCC-PL VCC-IO VCC3V3-VIDEOI VCC3V3-DSI B19 X32KOUT M16 HVCC GND0 GND4 VCC-DSI N19 G4 - I-GND X32KIN VCC-PI GND GND VCC-IO0 14 AP-CK32KO X32KEOLIT VCC-IO1 X24MO HHPD SHOULD HSDA VDD-CPUS PL0/S-RSB-SCK/S-TWI-SCK PL1/S-RSB-SDA/S-TWI-SDA HSCL DSI-CKP DSI-CKN GND4 GND5 X24MI L4 N4 L5 GND MIPI-DSI-CKP VDD-CPUS DCDC1 MI -PMILEN 13 GND5 GND6 GND7 VCC-EFUSE PL2/S-UART-TX HCEC DSI-DOP VDD-CPUX WL-WAKE-AP VDDRP-FFLISE DI 2/C LIADT DV DOI DON MIDI DOI DOI H22 H23 G22 PL4/S-JTAG-MS PL5/S-JTAG-CK PL6/S-JTAG-DO BT-RST-N BT-WAKE-AP 13 13 13 13 13 GND8 GND9 GND10 VDD-CPUX0 AA1 VDD-CPUX1 AC1 VDD-CPUX2 V2 HTXCP/ нтхср DSI-DIP H16 VCC-RTC VCC-RTC HTXCN HTX0P DSI-D1N M22 DSI-D2P M22 MIPI-DSI-D1N MIPI-DSI-D2P AP-WAKE-BT HTYOR G22 HTXON G23 HTXON F22 HTX1P D23 HTX1N N16 VCC-PLL R6 C7 D7 PL7 PL8 HTXON VDD-CPUX2 VDD-CPUX3 VDD-CPUX4 VDD-CPUX6 VDD-CPUX7 VDD-CPUX8 VDD-CPUX8 VDD-CPUX9 VCC-PLI PL7/S-JTAG-DI PL8/S-TWI-SCK DSI-D2N DSI-D3P MIPI-DSI-D2N MIPI-DSI-D3P GND11 GND12 G16 RTC-VIO GND4 C GND12 GND13 GND14 PL9/S-TWI-SDA DI Q DSI-D3N P7 GND13 R7 GND14 U7 GND15 K8 GND16 GND17 PI 10/S-PWM HTX2F LCD-F PL11 E22 HTX2N XH14 E17 G18 RESET PL11/S-CIR-RX HTX2N X RESET PL12 K8 GND16 M8 GND17 P8 GND18 R8 GND20 U8 GND21 V8 GND22 J9 GND23 K9 GND24 AD MANUE AA11 AA12 A16 T F17 KEYADC VDD-CPUX10 VDD-CPUX11 KEYADC (VDD-CPUX12 CPUS-UTX FFI VDD-CPUX13 CPUS-URX VDD-CPUX14 VDD-CPUX15 VDD-CPUX16 GND25 GND26 VDD-CPUX17 UBOOT _____TP202 TEST2 VDDFB-CPUX T6 Away from the board outline and senstive signal. VDDFB-CPUX 6 Away from the board outline and sensitive signal. Around GND line. Bouting in Power Layer. VDD-SYS GND27 N9 P9 R9 T9 U9 GND28 GND29 GND30 GND31 AP-NMI# AP-RESET# VDD-SYS0 GND32 VDD-SYS0 J13 VDD-SYS1 J13 GND32 GND33 VCC3V3-USB GND34 GND35 VDD-SYS2 VDD-SYS3 -GND AVCC VCC-USB GND36 GND37 VDD-SYS4 VDD-SYS5 C0402 Close to AP CPVEE, VEE, CPVDD, CPP, CPN 6,9 6,9 GND38 VDD-SYS6 VDD-SYS7 CPVDD USB0-DM GND39 VDD-SYS7 VDD-SYS8 P14 GND40 USB1-DP C22 6,9 E11 E10 G13 B12 B11 VIDD GND41 GND42 VDD-SYS9 GND GND CPP CPN VEF USB1-DM USB1-DM VDD-SYS10 GND43 GND44 HSIC-VCC K16 CPVEE and VEE were Kelvin or VCC1V2-HSIC M11 N11 VRA1 HSIC-DAT HSIC-STR H19 X GND46 VRA2 GND47 R11 HBIAS MDIAC GND48 GND49 GND72 P13 GND71 T13 GND50 GND51 GND71 GND70 C10 HPOLITER HP-FR K12 GND50 L12 GND51 M12 GND52 N12 GND53 P12 GND54 R12 GND55 T12 GND56 U12 GND57 U12 GND56 K13 GND58 K13 GND59 EAROUTP A13 HPOUTI HPOUTL GND69 GND68 HP-DET HP-DET GND67 LINEOUTP LINEOUTN D16 GND66 GND65 HS-MIC MIC-DET GND64 GND63 R15 MICIN1P MICIN1N LINEINP B14 X MICIN2P GND62 MICINION GND60 GND61 PHONEOUTP F16 X DHONEIND GND GND PHONEINN A64 A64 DECOUPLE CAP VDD-SYS VCC-RTC CPVDD VDD-CPUS VDD-CPUX GND C14 C18 C19 C0402 C0402 GND CC30 GND GND GND GND AVCC VRP VCC-PLL DCDC1 XIN GND CPVEE GND XOUT C37 C0402 Pine A64 ₩ GND A64-DB Title COMOS CND GND4 CPU Pg 5/19 Rev V1.2

