F. Vogel	HPCSEI HSA6	12-929-246
General	Threads	HPI
-> setx kbmap ch - variant de - nodead keys	compile g++-std=c++M-pthread	compile mpisc, mpic++
hahe file make	import # include < thread>	import # include < mpi. h>
CXX = g + t	launch have stal :: thread + (for, ang 1);	run mpiexec -np 4 ./a.out
CFLAGS = - Wall - 03 -stel=c++M	land 2 stal: thread ([&] () { "do southing });	info MPI_Comm. vank (MPI_COMM_WORLD, & vank);
		MPI_Com_size (MPI_Comm_world, &size);
test: test.cpp	matex:	
\$(CXX) \$(CFLAG) -0 \$@ \$<	import #include <matex></matex>	Example
	declare stal:: matex mtx;	int main (intarge, char **argv) {
cleah:	lock mtx. lock ();	MPI_ Init (& ange, & anger);
rm-f test		int nown;
	mfx. unlock();	MPI_Comm_rouk (MPI_Cornhworld, &um).
	lock guard stal: lock guard stal: mutex ((mtx);	if (num == 0) { // master
	Hoched when initalized	MPI-states states;
	to contact when destroyed	char txt[100];
	unique lock stal: unique lock cotal: unitex & (luty, oh	
	to locks work with multiple lock (he develock)	
	l. unlock(); 1. lock(); for manual	std: cout « +xt « 'lu';
		} else { Norher
	Example	stanstring text = "Bla";
	int nthreads = 4;	MPI_ Send (cout_ cout x char *> (text. cstr()),
	int natep = N/uthreads;	text size O+1, MPI - CHAR, O, WZ
	stel :: vector < stel :: thread > threads (uthreads);	MPI_COMM_ WORLD);
	for (int t= 0, t < nthroads; t++) {	}
	threads [t] = thread ([&, t] {	MPT_Finalize O;
	for (int i = + * nstep; i < (++) * nutep; i+) {]
	≥[i] = x[i] + y[i];	
	});	Chen MA
	}	compile gtt - fopenmp -std=c++11
	for (stol: threadle +: threads) {	import # include comp. h)
	tijoic ();	into omp-get-thread-num () -> id
	}	omp-get-num-threads () -> #threads

F. Vogel	HPCSE I	4516		12-929-44
	ridge point	1: Locality	Bugs	
Andahls Law: S(n)= 1 Plot	7	2: Con-unication	Bugs Threads :	. Pass flreod id by value, not by reference
(1-p)+ ' h	7	3: Computation	-> SIMO :	Race condition by interleaving of Gods
Sustations Law: S(4) = 1-p+4p (week realize)	2	limit by comp freak = f(rx)		: Underfield behavior by Goding not allocated memory
7(1)		limit by DRAH - x - x bpeak = flva		implicit barrier @ end of for, all threads have to cal
Strong scaling: $S(u) = \frac{T(n)}{T(u)} E(u) = \frac{S(u)}{n}$	0.01 0.4 1 10 100 1000	d		it. Fix: schodule (dynamic) howait
(ω)	Op. Int. [Flop/Byk]		- MPT :	Don't updark asynchronous send buffer
- ophical * messessed			- Threads	. Not paying all needed vouriebles
3,			> Threads :	Rea condition by not boding result double
			> Threads:	. Result reduction before join is called a each thread
			- Threads	Marker thread; not releasing both before calling soin
# of threads				can realt in deadlocks
T(a)				Ordering heeded to prevent deadlach
Weak scaling: $E(u) = \frac{T(1)}{T(u)}$ fixed problem size $\frac{T(1)}{T(u)}$ per thread				(replacing with asgrations as works too)
Whe: Problem size for 1				
Roofline Model				
Operational Itersity [Flop/Byte] r = #of Flops #of mem accesses. # of By tes				
# of Pierracceles o				
* Example: RHSi, = C, (q'in, + q'in, + q'in, + q'in, + q'in, - hq'i)			region of the second of the se	
>> 6Flops (4ADD + 2 MUL)				
-> Mem Acces:				
>> No cache: Sread + 1 write = 6				
+ Inhink coch: 1 read + 1 write = 6				
-> Op. Int. (Flops = 0.25 Flop/B			- Supplier	
			STATE OF THE PROPERTY OF THE P	
Diffick Coole: 6 Flops = 0.75 Flop/B				
Vominal performance				
→ fpeak = processor clock/sec · vectorsize · instructions/clock · # cores				
-> bpeak = menory clock (sec · charmed size · #charme's / 8[bib/Byte]				